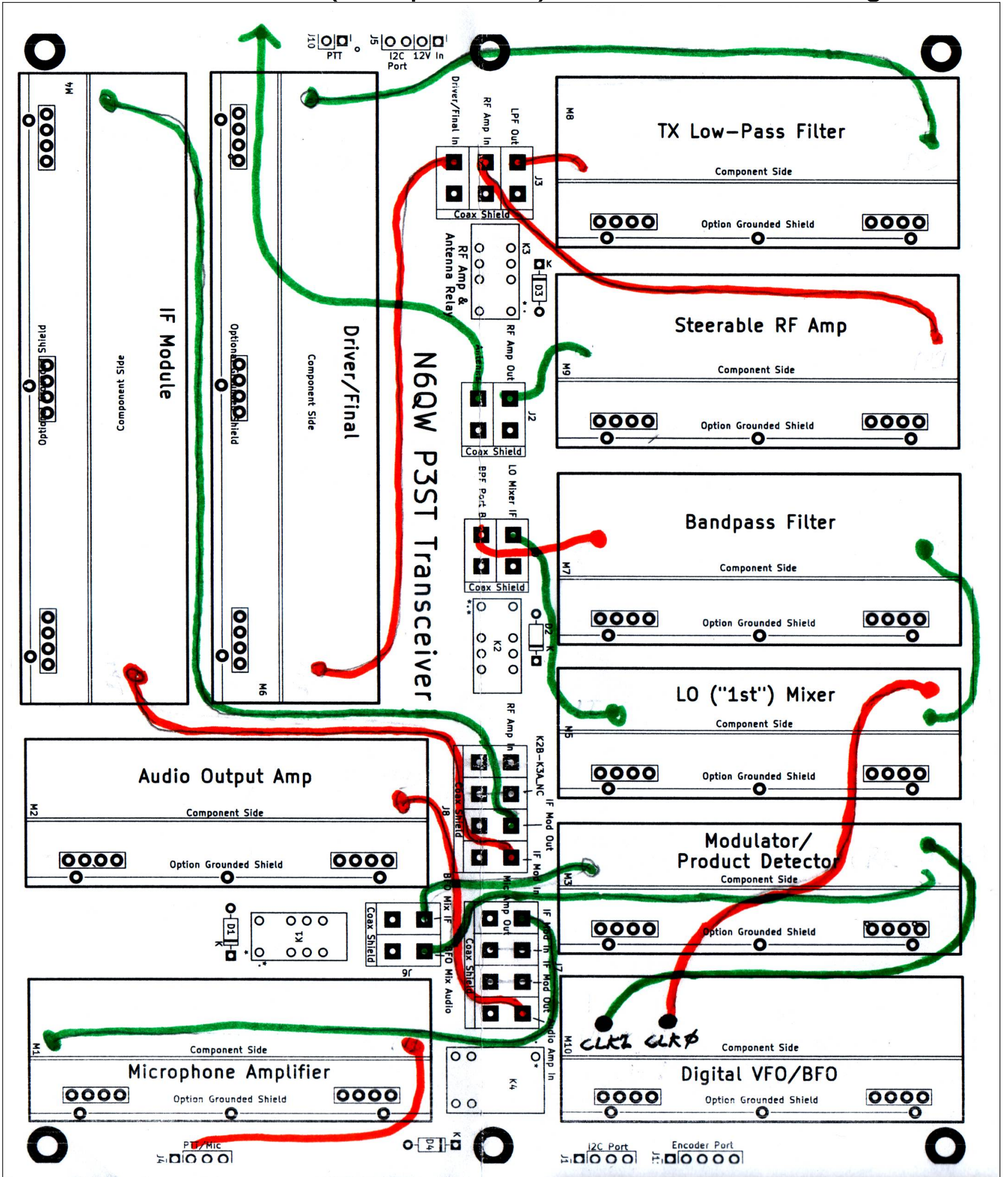


P3ST RG174 (or equivalent) Inter-Module Cabling



Note 1: For a full understanding of these interconnections, consult the P3ST **block diagram** and the **motherboard schematic**.

Note 2: Some of the square pads provided for interconnections between the signal relays (in the center "aisle" on the motherboard) are not used. They represent connections made by circuit trace on the PCB itself.

Note 3: For J2, J3, and J9, pads for the coax shield connections are on the right-hand side. For J6, J7, and J8, they are on the bottom rows.

Note 3: In addition to RF and IF connections, the two mixer boards take input from the Si5351 module on the Digital VFO/BFO. By custom (and by firmware) Clock 0 goes to the VFO mixer (a.k.a., "1st" or "LO" mixer) and Clock 2 goes to the BFO mixer (a.k.a., "Modulator/Product Detector"). Clock 1 is not used.

Note 5: As is customary, the PTT connection is closed by grounding the line through the microphone switch. The audio signal from the mic is wired directly to the Mic Amp input.