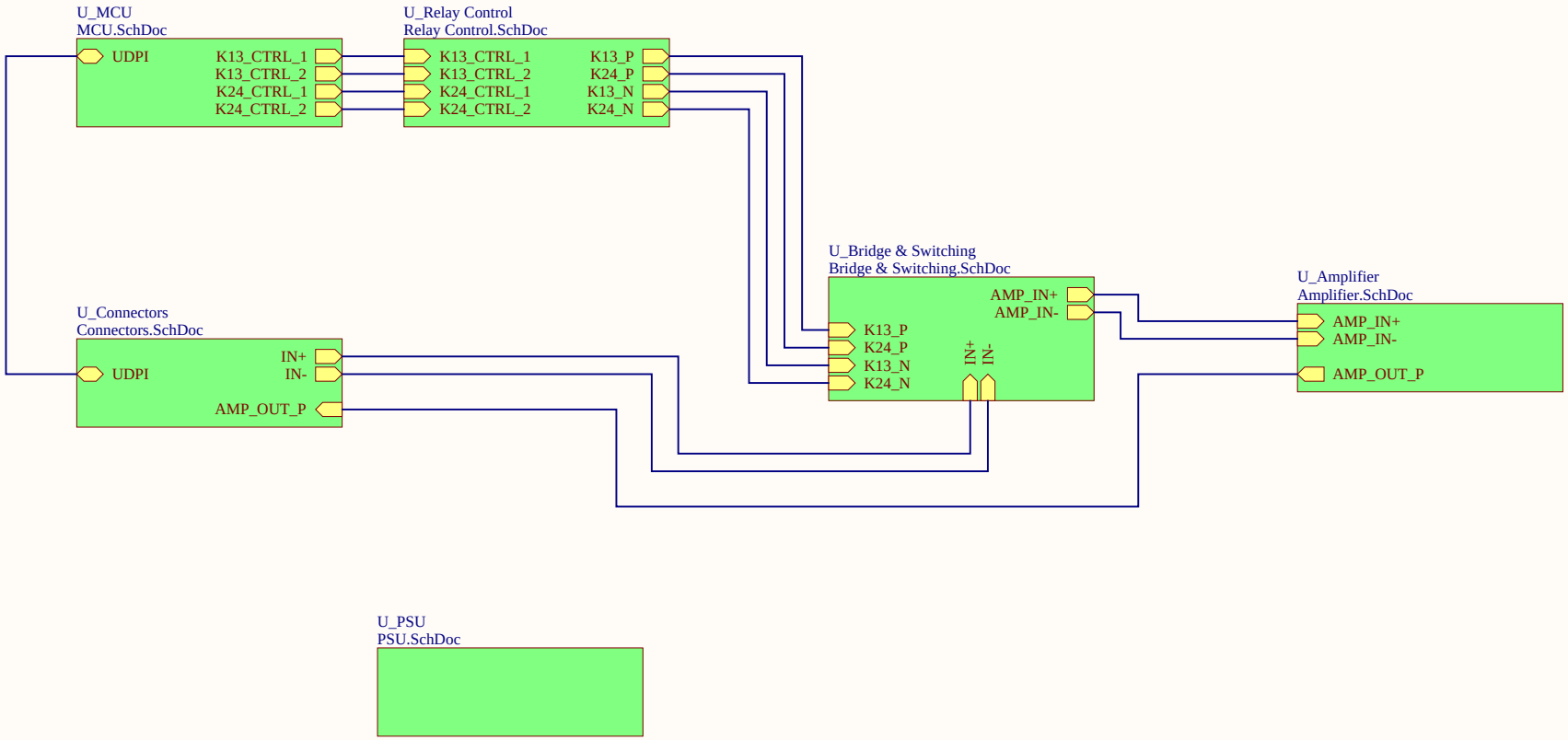

Project:	***Universal Wheatstone Bridge Amp***		
File:	TopSCH.SchDoc		
Size: A4	Author: Leon Baždar	Revision: V1.0	
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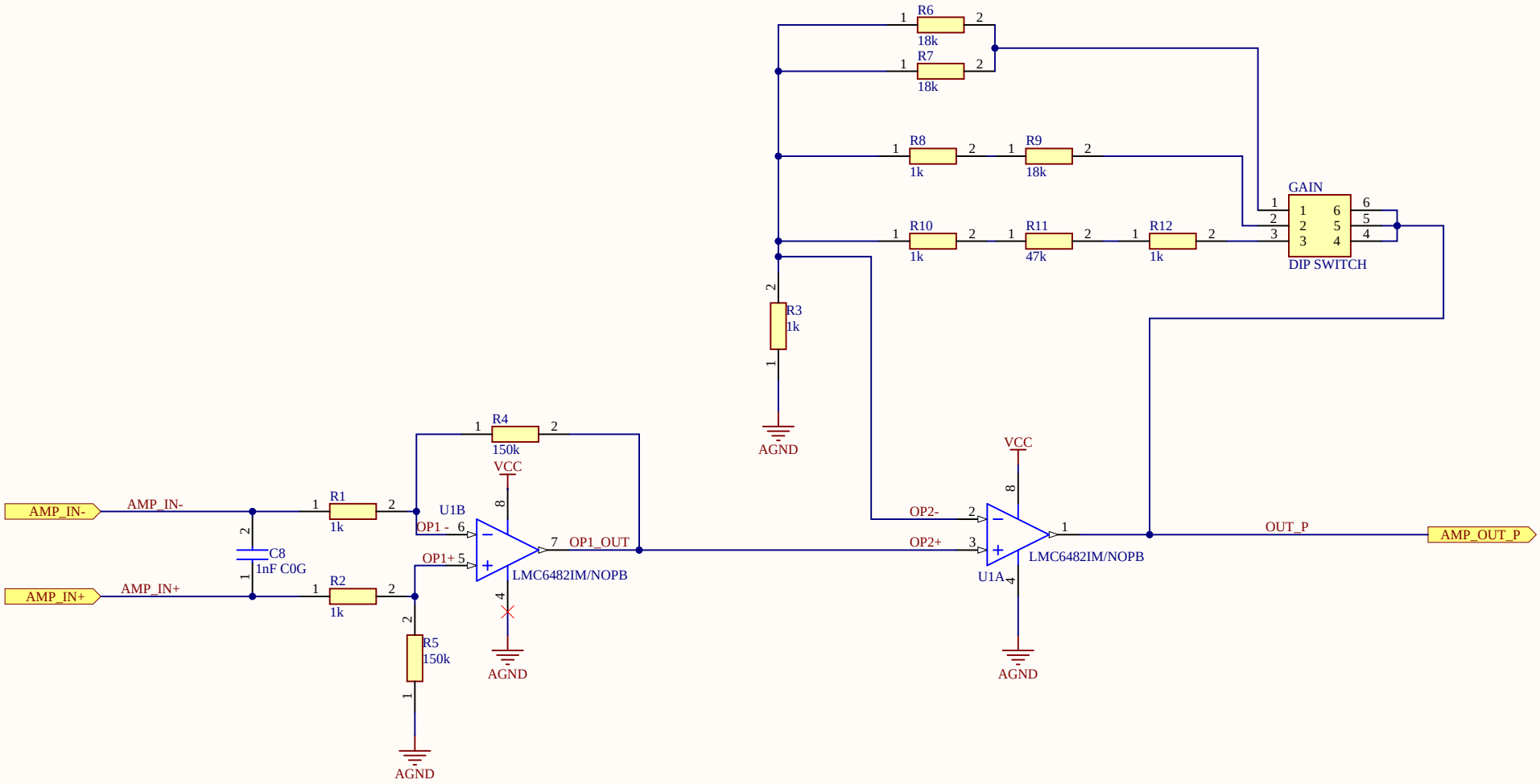

Project:	*Universal Wheatstone Bridge Amp*		
File:	Amplifier.SchDoc		
Size: A4	Author: *	Revision: V1.0	
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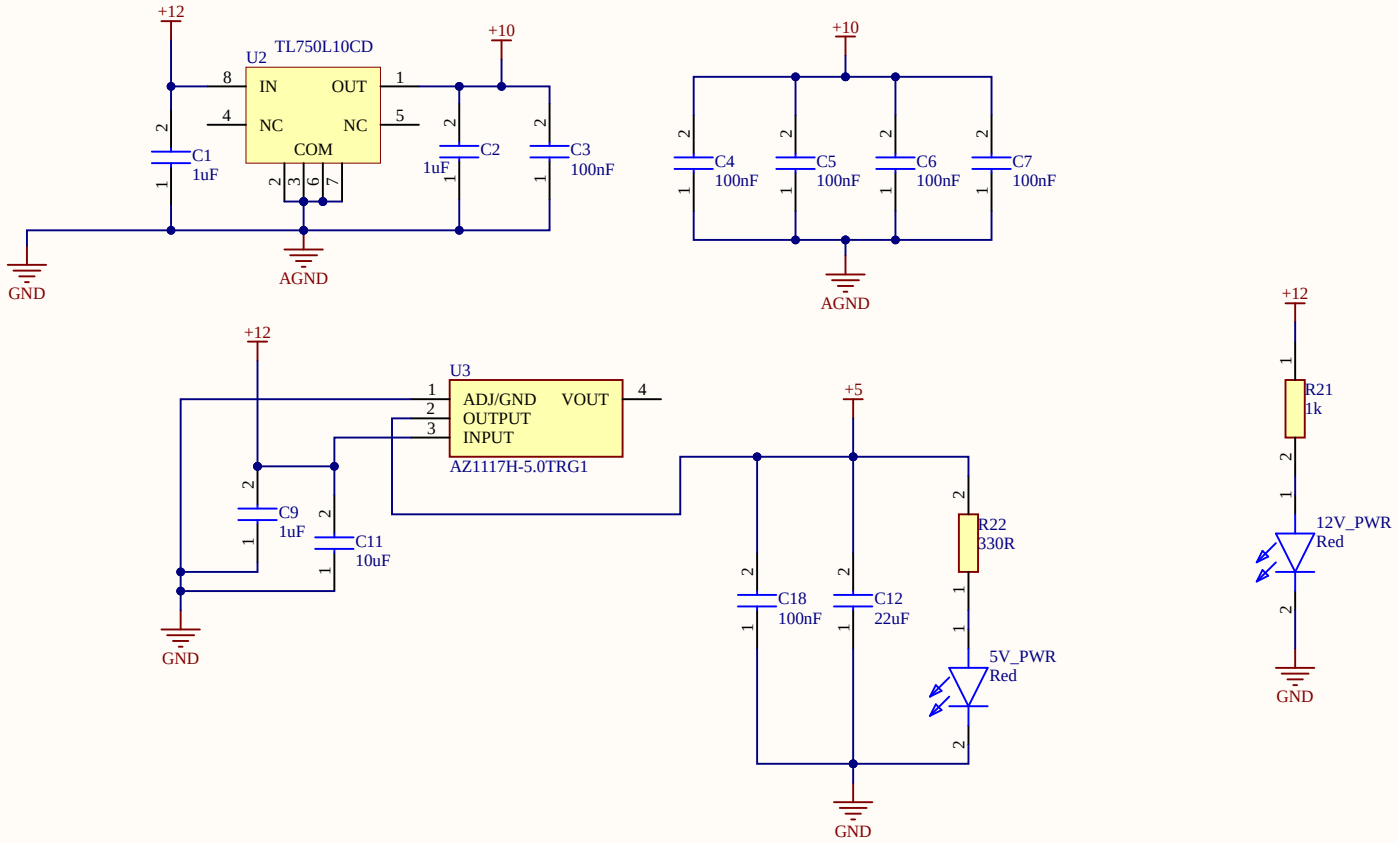

Project:	*Universal Wheatstone Bridge Amp*		
File:	PSU.SchDoc		
Size: A4	Author: *	Revision: V1.0	
Date: 7.3.2022.		Sheet 3 of 7	

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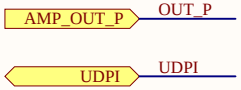
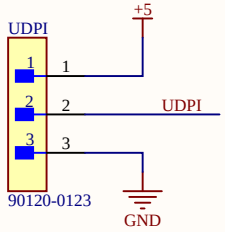
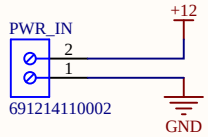
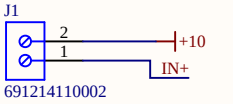
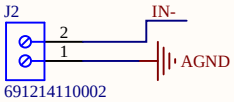


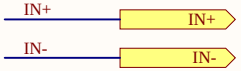


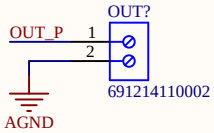
UDPI Programmer Header











Project:	***Universal Wheatstone Bridge Amp***		
File:	Connectors.SchDoc		
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IN+
IN_P
IN-
IN_N

AMP_IN+
AMP_IN+
AMP_IN-
AMP_IN-

K1 & K3- Full or Partial Bridge selection

K13_P
K13_N
K1
FULL_BR_P
FULL_BR_N
IN_P
IN_N
COIL+ COIL-
NC_1 NC_2
COM_1 COM_2
NO_1 NO_2
2-1462038-1
PART_BR_IN_P
PART_BR_IN_N
K2
K24_P
K24_N
WH_A
WH_VCC
AGND

K13_P
K13_N
K3
FULL_BR_P
FULL_BR_N
AMP_IN+
AMP_IN-
PART2_BR_P
PART2_BR_N
2-1462038-1

K24_P
K24_N
K4
IN_P
WH_B
PART2_BR_P
PART2_BR_N
WH_A
WH_B
2-1462038-1

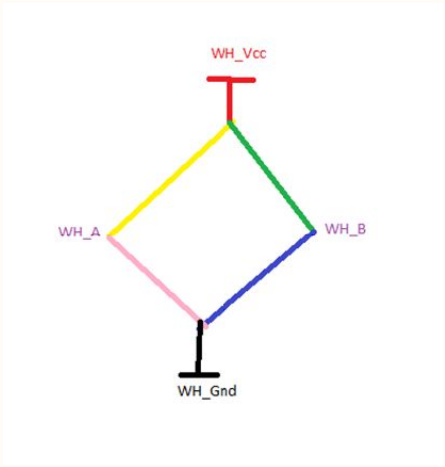
K1 & K3 Same state
K2 & K4 Same state

K1 & K3 OFF = FULL BRIDGE {
IN+ -> AMP_IN+
IN- -> AMP_IN-
}

K2 & K4 - Partial Bridge type selection

NC - Half Bridge
NO - Quarter Bridge

WH_B
WH_A
RN1
ORNTA1001BT1
VCC
WH_VCC
AGND
K13_P
K13_P



K24_P
K24_P

K13_N
K13_N

K24_N
K24_N

K1 & K3 ON = PARTIAL BRIDGE {

K2 & K4 OFF = HALF BRIDGE:
{
IN+ -> AMP_IN+
WH_B -> AMP_IN-
}

K2 & K4 ON = QUARTER BRIDGE
{
IN+ -> WH_VCC
IN- -> WH_A

WH_A -> AMP_IN+
WH_B ->AMP_IN-
}

}

Project:	*Universal Wheatstone Bridge Amp*		
File:	Bridge & Switching.SchDoc		
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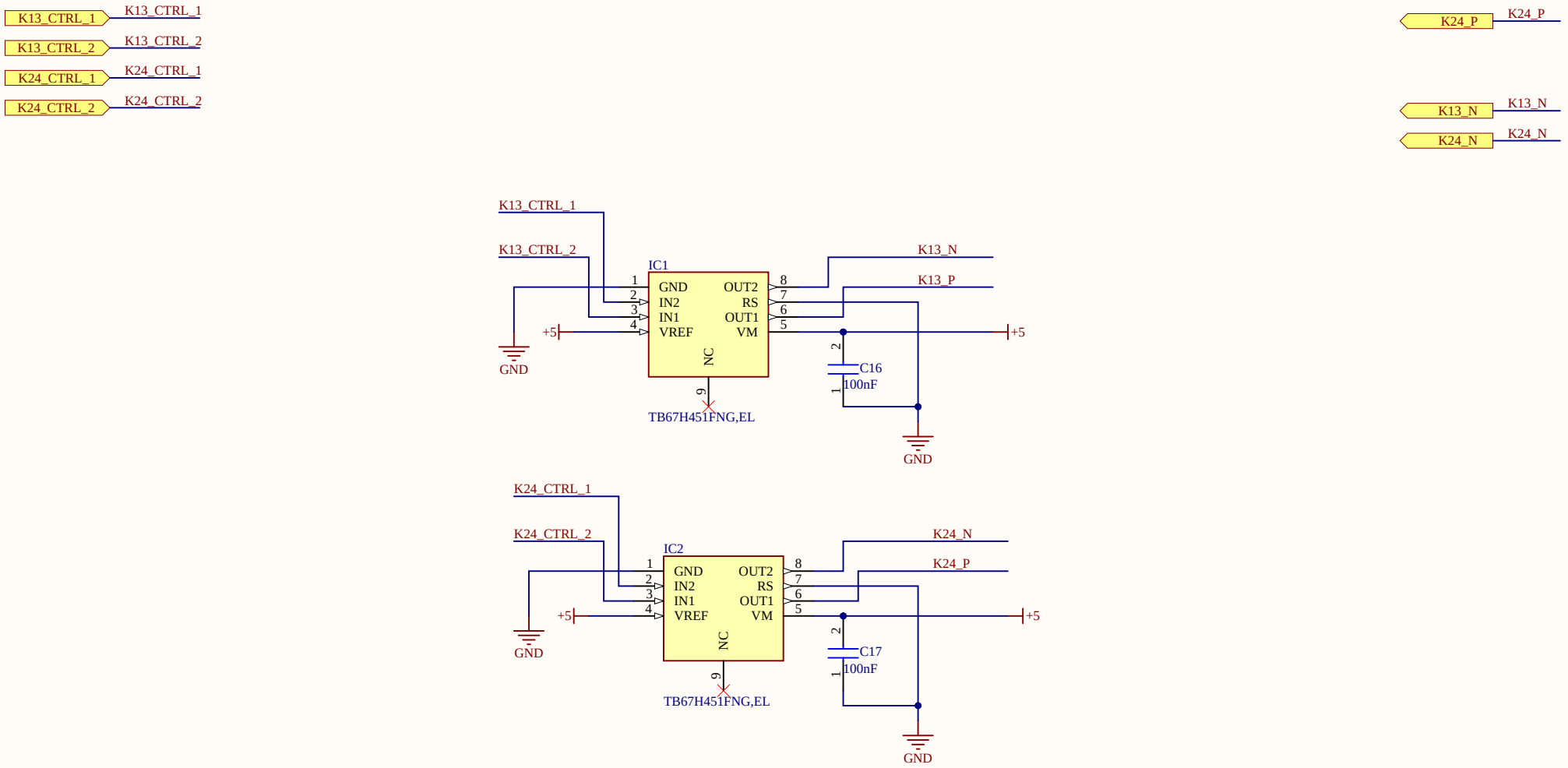

Project:	*Universal Wheatstone Bridge Amp*		
File:	Relay Control.SchDoc		
Size: A4	Author: Leon Baždar	Revision: V1.0	
Date: 7.3.2022.		Sheet 6 of 7	

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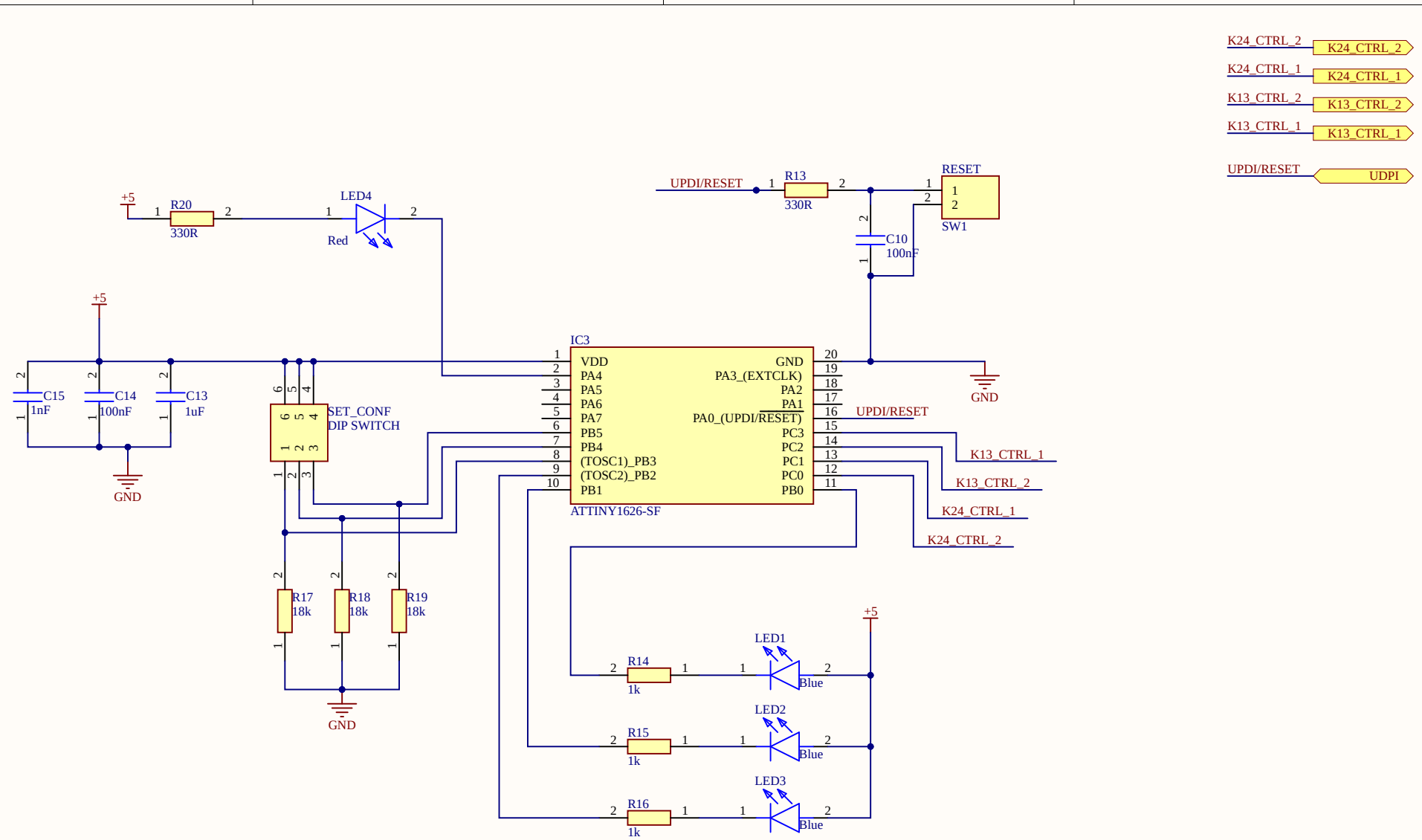

Project:	*Universal Wheatstone Bridge Amp*	
File:	MCU.SchDoc	
Size: A4	Author: Leon Baždar	Revision: V1.0
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Board Stack Report