

Simon Fraser University

Mechatronic Systems Engineering

Final Exam for ENSC 226: Electronic Circuits

Instructor: Behraad Bahreyni

16 April 2012

Time: 150 minutes

Name: Solutions

Student number: _____

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Question	Mark
1	/ 10
2	/ 15
3	/ 20
4	/ 20
5	/ 20
6	/ 10
7	/ 15
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Physical model of a diode: $I_D = I_S \left(e^{\frac{V_D}{V_T}} - 1 \right)$

For BJTs:

	Active	Saturation	Cut-off
	BE forward biased BC reverse biased	BE forward biased BC forward biased	BE reverse biased BC reverse biased
npn	$I_C = \beta I_B = \alpha I_E, V_{BE} \approx 0.7V$	$I_B > \frac{I_C}{\beta}, V_{CE} \approx 0.2V$	$I_B \approx I_C \approx I_E \approx 0$
pnp	$I_C = \beta I_B = \alpha I_E, V_{EB} \approx 0.7V$	$I_B > \frac{I_C}{\beta}, V_{EC} \approx 0.2V$	$I_B \approx I_C \approx I_E \approx 0$

$$I_C(\text{active}) = I_S e^{\frac{V_{BE}}{V_T}} \left(1 + \frac{|V_{CE}|}{V_A} \right)$$

$$\alpha = \frac{\beta}{\beta + 1}$$

$$g_m = \frac{1}{r_m} = \frac{I_C}{V_T}$$

$$r_{\pi} = \beta r_m = \frac{\beta}{g_m}$$

$$V_T = 25mV$$

$$r_o = \frac{V_A}{I_C}$$

The diagrams show three BJT models. The first is an active model with base current I_B entering the base, collector current I_C leaving the collector, and emitter current I_E leaving the emitter. The second is a saturation model with a base-emitter diode and a dependent current source $I_S \exp(V_{BE}/V_T)$. The third is a cut-off model with a base-emitter diode and a dependent current source $I_S \exp(V_{EB}/V_T)$.

For MOSFETs:

	Saturation	Triode	Cut-off
NFET ($V_{TH} > 0$)	$V_{GS} > V_{TH} \text{ \& } V_{GD} < V_{TH}$	$V_{GS} > V_{TH} \text{ \& } V_{GD} > V_{TH}$	$V_{GS} < V_{TH} \text{ \& } V_{GD} < V_{TH}$
PFET ($V_{TH} < 0$)	$V_{GS} < V_{TH} \text{ \& } V_{GD} > V_{TH}$	$V_{GS} < V_{TH} \text{ \& } V_{GD} < V_{TH}$	$V_{GS} > V_{TH} \text{ \& } V_{GD} > V_{TH}$

$$I_D(\text{saturation}) = \frac{K'}{2} (V_{GS} - V_{TH})^2 (1 + \lambda V_{DS})$$

$$K' \equiv K_n = \mu_n C_{ox} \frac{W}{L} \text{ for NFETs}$$

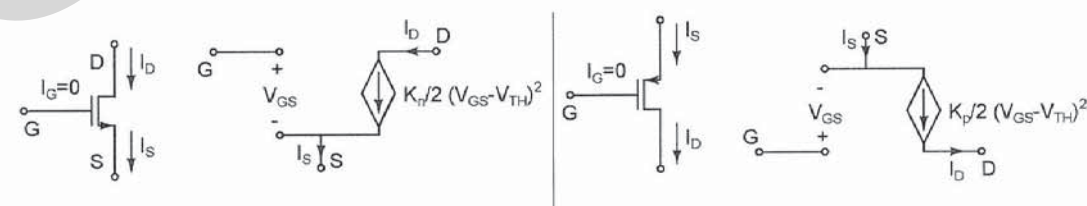
$$K' \equiv K_p = \mu_p C_{ox} \frac{W}{L} \text{ for PFETs}$$

$$I_D(\text{triode}) = \frac{K'}{2} [2(V_{GS} - V_{TH})V_{DS} - V_{DS}^2]$$

$$r_{DS}(\text{deep triode}) \approx \frac{1}{K'(V_{GS} - V_{TH})}$$

$$g_m = K'(V_{GS} - V_{TH}) = \sqrt{2K'I_D} = \frac{2I_D}{V_{GS} - V_{TH}}$$

$$r_o = \frac{1}{\lambda I_D}$$



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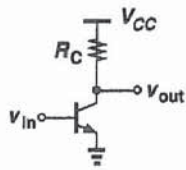
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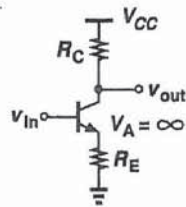
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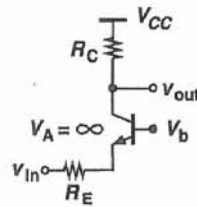
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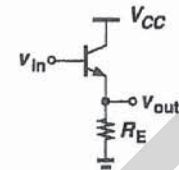
$$A_v = -g_m (R_C \parallel r_o)$$



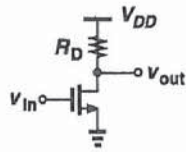
$$A_v = -\frac{R_C}{\frac{1}{g_m} + R_E}$$



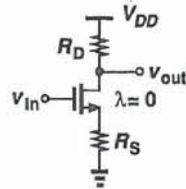
$$A_v = \frac{R_C}{\frac{1}{g_m} + R_E}$$



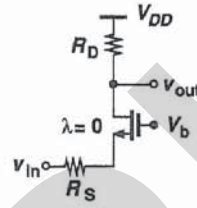
$$A_v = \frac{R_E \parallel r_o}{\frac{1}{g_m} + R_E \parallel r_o}$$



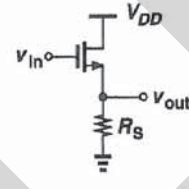
$$A_v = -g_m (R_D \parallel r_o)$$



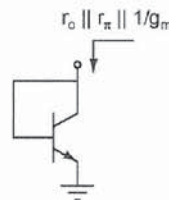
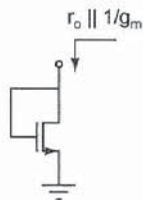
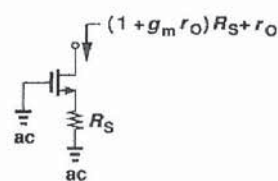
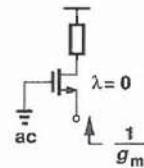
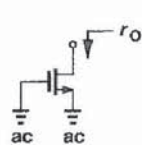
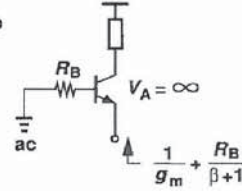
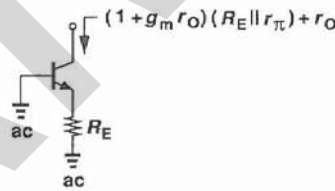
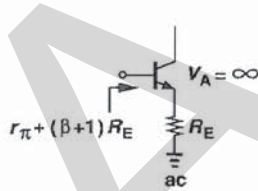
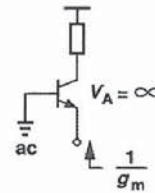
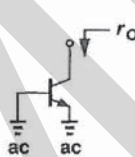
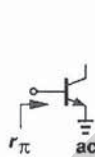
$$A_v = -\frac{R_D}{\frac{1}{g_m} + R_S}$$



$$A_v = \frac{R_D}{\frac{1}{g_m} + R_S}$$



$$A_v = \frac{R_S \parallel r_o}{\frac{1}{g_m} + R_S \parallel r_o}$$



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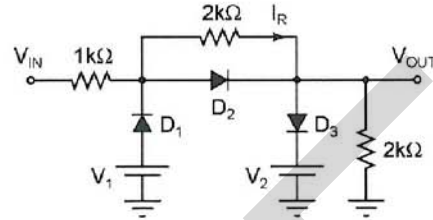
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Question 1 [10 marks]

Consider the diode circuit to the right where the diodes are **ideal**, $V_1 = -5V$, and $V_2 = +2V$. The input voltage is slowly increased from $-10V$ to $+10V$.



1.a. [5 marks] What is the range for I_R ?

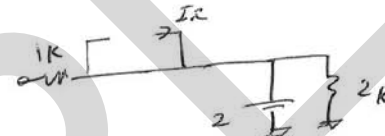
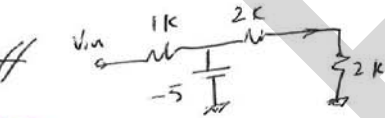
$$-1.25mA < I_R < 0$$

$$V_{in} \rightarrow -\infty \rightarrow D_1: on, D_2: off, D_3: off$$

$$V_{in} \Rightarrow -10 \rightarrow I_R = \frac{-5}{4k} = -1.25mA$$

$$V_{in} \rightarrow +\infty \rightarrow D_1: off, D_2: on, D_3: on$$

$$\rightarrow I_R = 0$$



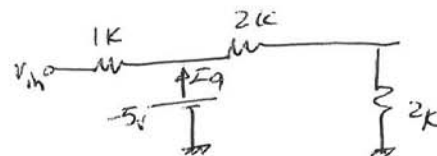
1.b. [5 marks] Diode D_1 turns off as the input voltage is increased from $-10V$. What is the minimum input voltage that switches diode D_1 off?

$$V_{in} \rightarrow -\infty \rightarrow D_1: on, D_2: off, D_3: on$$

$$I_{D1} = \frac{-5 - V_{in}}{1k} - \frac{5}{4k}$$

$$I_{D1} = 0 \rightarrow V_{in} = -\left(5 + \frac{5}{4}\right) = -6.25V$$

$$V_{in} = -6.25V$$



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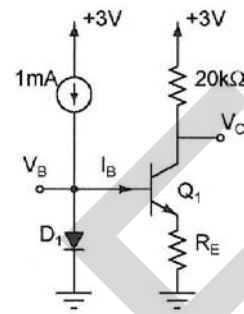
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Question 2 [15 marks]

Consider the circuit to the right where the base current of Q_1 can be ignored (i.e., $I_B \approx 0$ or $\beta \rightarrow \infty$). For both the transistor and the diode, assume $V_T = 25\text{mV}$, $I_S = 10^{-15}\text{A}$, and $V_A = \infty$. Provide the answers with three significant digits.

2.a. [5 marks] What is V_B ?

$$V_B = 0.691\text{ V}$$

$$V_B = V_T \ln \frac{I_D}{I_S} = 0.691 \quad (2)$$

(3)

2.b. [7 marks] If $R_E = 1\text{k}\Omega$, what is the collector current of Q_1 (Hint: use iterations)?

$$I_C = 0.0672\text{ mA}$$

$$V_B = V_{BE} + I_C R_E \quad (2)$$

$$\rightarrow 0.691 = V_T \ln \frac{I_C}{I_S} + I_C R_E$$

at least 3 iterations
(3)

$$I_C = 67.4\text{ }\mu\text{A}$$

$$V_{BE} = 0.623\text{ V}$$

either (2)

2.c. [3 marks] What is V_C for the collector current calculated in the above?

$$V_C = 1.652\text{ V}$$

$$V_C = V_{CC} - I_C R_C = 1.652\text{ V} \quad (1)$$

(2)

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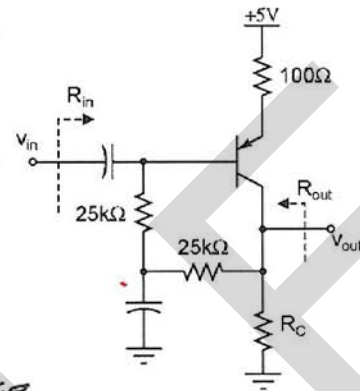
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Question 3 [20 marks]

Consider the circuit shown to the right where the capacitors are assumed to be AC short circuits. Assume $\beta = 100$, $V_T = 25\text{mV}$, $V_{EB}(\text{on}) = 0.6\text{V}$, $V_{EC}(\text{sat}) = 0.2\text{V}$, and $|V_A| = \infty$.

3.a. [4 marks] If $R_C = 10\text{k}\Omega$, what is the collector current of the transistor? Verify the operating region of the transistor.

$$I_C = 0.411\text{ mA}$$

Operating region: Active

$$V_{CC} = I_E(100 + R_C) + I_B(50\text{k}) + V_{EB} \quad (2)$$

$$\rightarrow I_C = 411\mu\text{A} \quad (1)$$

$$V_C = 4.15\text{V}, \quad V_E = 4.96\text{V}$$

$$\left. \begin{array}{l} V_{EC} \approx 0.8\text{V} \\ V_{EB} \approx 0.6\text{V} \end{array} \right\} \rightarrow \text{Active} \quad (1)$$

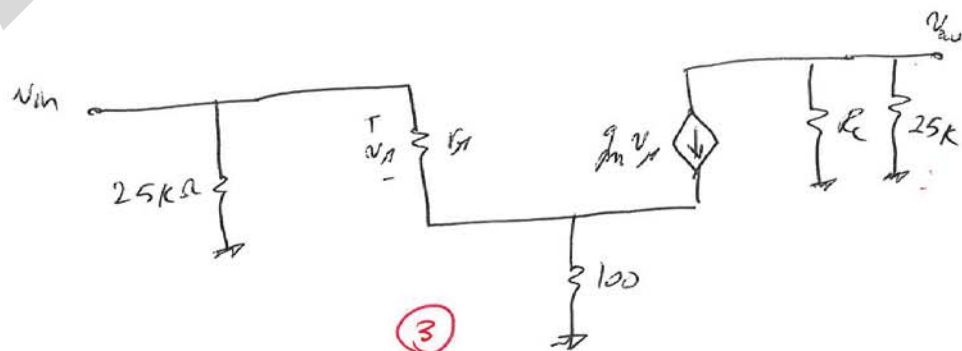
3.b. [4 marks] Calculate the small signal parameters and draw the small signal equivalent circuit.

$$g_m = 16.4\text{ mS}$$

$$r_\pi = 6.08\text{ k}\Omega$$

$$g_m = \frac{I_C}{V_T} = 16.4\text{ mS}$$

$$r_\pi = \beta / g_m = 6.08\text{ k}\Omega$$



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3.c. [4 marks] What is the voltage gain of the circuit?

$$A_v = \frac{v_{out}}{v_{in}} = \underline{-44.2} \text{ V/V}$$

$$A_v = \frac{-g_m R_C}{1 + g_m R_E} = \frac{-16.4 \text{ m} \times (25 \text{ k} \parallel 10 \text{ k})}{1 + 16.4 \text{ m} \times 100} = -44.2 \text{ V/V}$$

(2)

3.d. [4 marks] What is the input resistance of the circuit?

$$R_{in} = \underline{9.822} \text{ k}\Omega$$

$$R_{in} = 25 \text{ k} \parallel (r_{\pi} + (\beta + 1) R_E)$$

$$= 9.822 \text{ k}\Omega$$

(2)

3.e. [4 marks] What is the output resistance of the circuit?

$$R_{out} = \underline{7.142} \text{ k}\Omega$$

$$R_{out} = R_C \parallel 25 \text{ k}\Omega$$

$$= 7.142 \text{ k}\Omega$$

(2)

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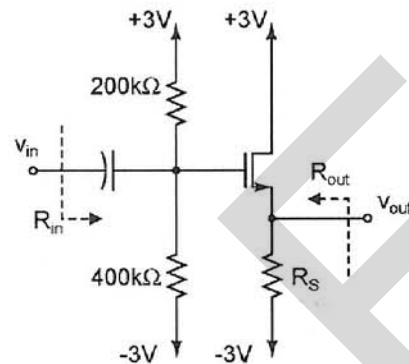
Question 4 [20 marks]

Consider the circuit shown to the right where the capacitor is AC short circuit. Assume $R_S = 15k\Omega$, $V_{TH} = 2V$, $K_n = 1mA/V^2$, and $\lambda = 0$.

4.a. [6 marks] What is the drain current of the transistor? Verify the operating region of the transistor.

$$I_D = \underline{0.103} \text{ mA}$$

Operating region: Active



$$V_G = -\frac{2}{6} \times 3 + \frac{4}{6} \times 3 = +1V \quad (1)$$

$$V_G = V_{GS} + I_D R_S - 3 \Rightarrow 1 = V_{GS} + \frac{15k \times 1mA/V^2}{2} (V_{GS} - 2)^2 - 3$$

$$7.5V_{GS}^2 + 26 - 29V_{GS} = 0 \Rightarrow V_{GS} = \begin{cases} 2.45V > V_{TH} \checkmark \\ 1.41V < V_{TH} \end{cases} \quad (1)$$

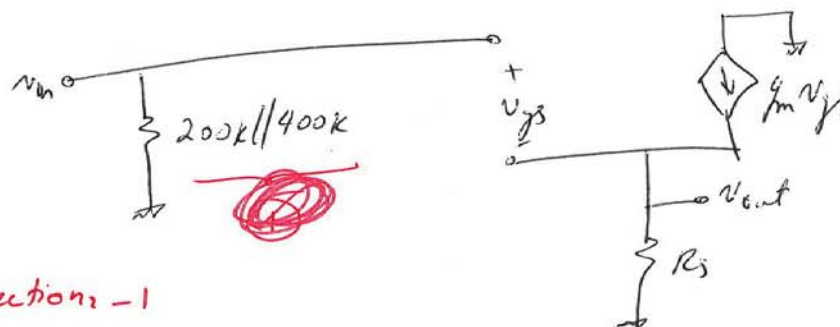
$$\Rightarrow V_{GS} = 2.45V \Rightarrow I_D = 103\mu A \quad (1)$$

$$V_D = 3V > V_G - V_{TH} = 1 - 2 = -1V \quad \left. \begin{array}{l} V_{GS} > V_{TH} \\ V_D > V_G - V_{TH} \end{array} \right\} \Rightarrow \text{Active} \quad (2)$$

4.b. [3 marks] Calculate g_m and draw the small signal equivalent circuit.

$$g_m = \underline{0.454} \text{ mS}$$

$$g_m = \sqrt{2K_n I_D} = 0.454 \text{ mS}$$



Wrong connection: -1

Missing ground: -1

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4.c. [4 marks] What is the voltage gain of the circuit?

$$A_v = \frac{v_{out}}{v_{in}} = \underline{0.87} \text{ V/V}$$

$$A_v = \frac{g_m R_c}{1 + g_m R_s} = \frac{15k}{15k + \frac{1}{0.454mS}} = 0.87 \text{ V/V} \quad (2)$$

(2)

4.d. [3 marks] What is the input resistance of the circuit?

$$R_{in} = \underline{133} \text{ k}\Omega$$

$$R_{in} = 200k \parallel 400k = 133k\Omega \quad (1)$$

(2)

4.e. [4 marks] What is the output resistance of the circuit?

$$R_{out} = \underline{1.92} \text{ k}\Omega$$

$$R_{out} = R_s \parallel \frac{1}{g_m} = 1920\Omega \quad (2)$$

(2)

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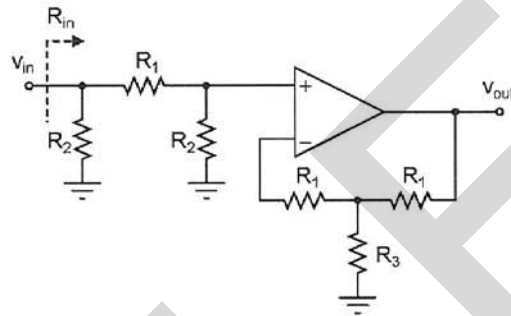
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Question 5 [20 marks]

The OpAmps are ideal in circuits shown below.

5.a. [5 marks] What is the input resistance of the circuit as a function of resistor values?

$$R_{in} = R_2 \parallel (R_1 + R_2)$$



$$R_{in} = R_2 \parallel (R_1 + R_2)$$

(5)

5.b. [5 marks] What is the voltage gain of the circuit assuming $R_1 = 2k\Omega$, $R_2 = 10k\Omega$, and $R_3 = 100\Omega$?

$$A_v = \frac{v_{out}}{v_{in}} = +21 \text{ V/V}$$

$$v^+ = v_{in} \left(\frac{R_2}{R_1 + R_2} \right) = v^-$$

$$I^- = 0 \rightarrow v_{out} = \left(1 + \frac{R_1}{R_3} \right) v^+ \quad (2)$$

$$\rightarrow v_{out} = \left(1 + \frac{R_1}{R_3} \right) \left(\frac{R_2}{R_1 + R_2} \right) v_{in} \quad (8)$$

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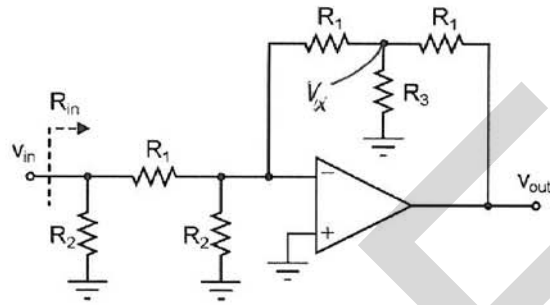
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5.c. [4 marks] What is the input resistance of the circuit as a function of resistor values?

$$R_{in} = R_1 \parallel R_2$$

$$R_{in} = R_2 \parallel R_1 \quad (4)$$



5.d. [6 marks] What is the voltage gain of the circuit assuming $R_1 = 2k\Omega$, $R_2 = 10k\Omega$, and $R_3 = 100\Omega$?

$$A_v = \frac{v_{out}}{v_{in}} = -22 \text{ V/V} \quad (2)$$

$$\frac{v_x}{R_3} + \frac{v_x}{R_1} + \frac{v_x - v_{out}}{R_1} = 0$$

$$v_x = -\frac{R_1}{R_3} v_{in}$$

$$v_{out} = -R_1 \left(\frac{2}{R_1} + \frac{1}{R_3} \right) v_{in} \quad (2)$$

$$A_v = -\left(2 + \frac{R_1}{R_3} \right) \quad (2)$$

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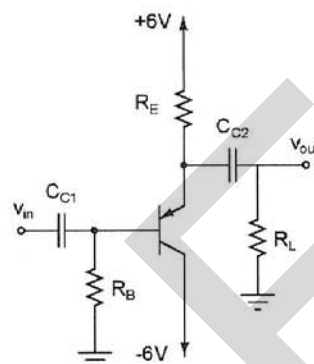
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Question 6 [10 marks]

Shown to the right is a common-collector circuit similar to what you studied in Lab 3.

6.a. [4 marks] We monitor the output signal while applying a signal with a constant amplitude to the input of the circuit. We notice that the amplifier gain at 1kHz is smaller than the gain at 100kHz. The gain remains relatively constant for frequencies higher than 100kHz. Explain what may be the most possible reason for this observation.



Impedance of coupling capacitors is high @ LF

6.b. [6 marks] We measure the gain of the amplifier at 1MHz with $R_L = 100\Omega$ to be 0.9V/V. When we change the load resistance to $R_L = 200\Omega$, the gain increases to 0.95V/V. What is the output resistance of the circuit? (Hint: you do not need device parameters or component values.)

$$A_v = A_{v0} \frac{R_L}{R_L + r_o}$$

$$\rightarrow 0.9 = A_{v0} \frac{100}{100 + r_o}$$

$$0.95 = A_{v0} \frac{200}{200 + r_o}$$

$$\rightarrow r_o = 11.76\Omega$$

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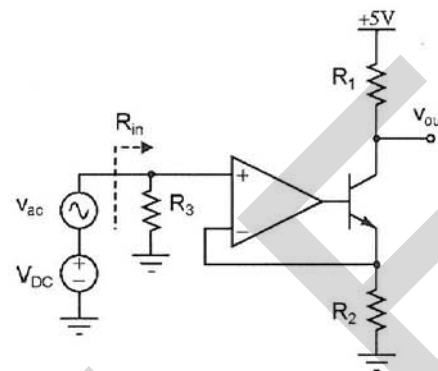
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Question 7 [15 marks]

The OpAmp is ideal in circuit shown to the right.

The transistor parameters are $\beta = 100$, $V_T = 25\text{mV}$, $V_{BE(on)} = 0.6\text{V}$, and $V_{CE(sat)} = 0.2\text{V}$. Assume $R_1 = 35\text{k}\Omega$, $R_2 = 1\text{k}\Omega$, and $R_3 = 1\text{M}\Omega$.

V_{DC} is a 100mV DC voltage source and v_{ac} is a small signal AC voltage source.



7.a. [4 marks] What are the DC collector current and voltage of the transistor?

$$I_C = 0.0991 \text{ mA}$$

$$V_C = 1.53 \text{ V}$$

$$I_C = \frac{V_{DC}}{R_2} \times \frac{\beta}{\beta+1} = \frac{0.1\text{V}}{1\text{k}} \times \frac{100}{101} = 99.1 \mu\text{A} \quad (3)$$

$$\rightarrow V_C = 5 - I_C R_C = 5 - 99.1 \mu\text{A} \times 35\text{k}\Omega = 1.531 \text{ V} \quad (1)$$

7.b. [7 marks] What is the small signal voltage gain of the circuit?

$$A_v = \frac{v_{out}}{v_{ac}} = -34.65 \text{ V/V} \quad (4)$$

$$v_{out} = -i_c R_1 = -\frac{v_{ac}}{R_2} R_1 \times \frac{\beta}{\beta+1} \Rightarrow A_v = -\frac{R_1}{R_2} \times \frac{\beta}{\beta+1} \quad (2)$$

$$\rightarrow A_v = -34.65 \text{ V/V} \quad (1)$$

7.c. [4 marks] What is the input resistance of the circuit?

$$R_{in} = 1\text{M}\Omega$$

$$R_{in} = R_3 = 1\text{M}\Omega \quad (4)$$

Name: _____

Student number: _____

Final exam

Time: 150 minutes

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You may use this space to answer any of the questions with proper reference to the question.

SAMPLE