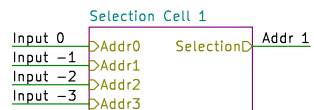
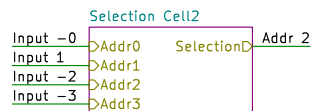


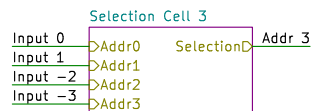
File: SelectionCell.kicad_sch



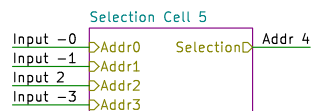
File: SelectionCell.kicad_sch



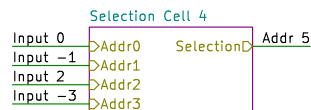
File: SelectionCell.kicad_sch



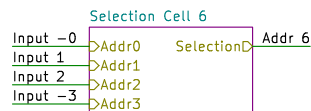
File: SelectionCell.kicad_sch



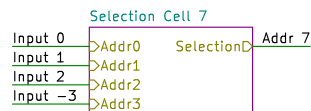
File: SelectionCell.kicad_sch



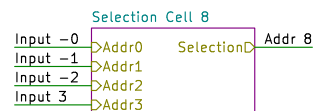
File: SelectionCell.kicad_sch



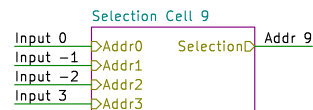
File: SelectionCell.kicad_sch



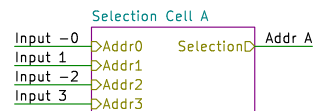
File: SelectionCell.kicad_sch



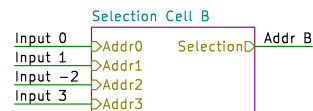
File: SelectionCell.kicad_sch



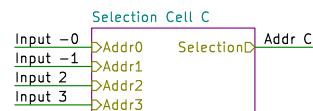
File: SelectionCell.kicad_sch



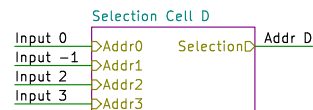
File: SelectionCell.kicad_sch



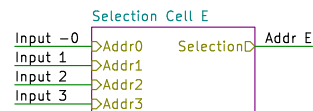
File: SelectionCell.kicad_sch



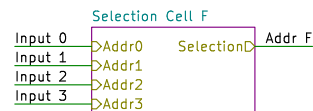
File: SelectionCell.kicad_sch



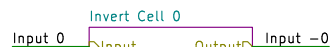
File: SelectionCell.kicad_sch



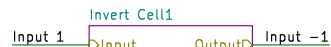
File: SelectionCell.kicad_sch



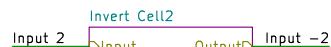
File: SelectionCell.kicad_sch



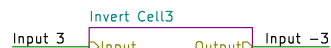
File: InvertCell.kicad_sch



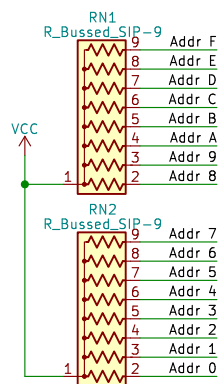
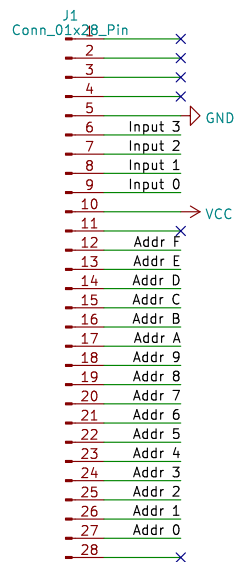
File: InvertCell.kicad_sch



File: InvertCell.kicad_sch



File: InvertCell.kicad_sch



VCC

GND

VCC

GND

Matthew Whited

Out-of-Band Development

Sheet: /

File: Address Decoder - 4 to 16.kicad_sch

Title: Diode Transistor Logic - 4 to 16 Address Decoder

Size: A4

Date: 2023-03-08

Rev: 1

KiCad E.D.A. kicad 7.0.1

Id: 1/21