Skip to Navigation





- Blog
- Projects
- Forum
- About
- Contact
- Shop
- Free PCBs

Facebook Twitter RSS Comments

Bus Pirate

From DP

Language: English · Español · 中文(简体)



- Bus Pirate v3.6 available now for \$30.15, including worldwide shipping (http://www.seeedstudio.com/depot/bus-pirate-v3assembled-p-609.html?cPath=61 68)
- Bus Pirate v3.6 is also available at Adafruit Industries
 (http://www.adafruit.com/index.php?
 main_page=product_info&cPath=8&products_id=237)
 (USA) EpicTinker (http://www.epictinker.com/Bus-Pirate-v3-assembled-p/thm180c4m.htm) (USA) Watterott
 Electronic (http://www.watterott.com/en/Bus-Pirate) (Germany) Evola (http://evola.fr/en/dangerous-

Name: Bus Pirate
Get one for \$27.15 at Seeed Studio
(http://www.seeedstudio.com/depot/bus-pirate-v3-assembled-p-609.html?cPath=61_68)

Price: \$27.15
Status: Mature
Manufacturing: Shipping
Bus Pirate Forum
(http://dangerousprototypes.com/forum/viewforum.php? f=4)

prototypes/164-Bus-Pirate-v3-6.html) (Europe) Anibit (https://anibit.com/product/ptt07001) (USA) Hackaday (http://store.hackaday.com/products/buspirate-v3-6-thm180c4m) (USA)

The Bus Pirate is an open source hacker multi-tool that talks to electronic stuff. It's got a bunch of features an intrepid hacker might need to prototype their next project. This manual is an effort to link all available Bus Pirate information in one place.

■ Read about the Bus Pirate v3 design (http://dangerousprototypes.com/2009/10/04/prototype-bus-pirate-v3/); the v3b update (http://dangerousprototypes.com/2010/04/29/prototype-bus-pirate-v3b/); and the v3.5 update (http://dangerousprototypes.com/docs/Bus_Pirate_v3.5).

An initial batch of Bus Pirate v4 is now available to early adopters and testers. We think v3.x is still the best choice for most people, but v4 is the way of the future.

- Bus Pirate v4 available now for \$40.15, including worldwide shipping (http://www.seeedstudio.com/depot/bus-pirate-v4-for-developers-p-740.html?cPath=174)
- Bus Pirate v3 vs v4 comparison
- Bus Pirate v4 hardware overview

Contents

- 1 Introduction
 - 1.1 Features
 - 1.2 Hardware
 - 1.3 Tutorials
 - 1.4 Forums
 - 1.5 Linux, OS X
- 2 Download
- 3 Reference
 - 3.1 User terminal mode
 - 3.2 Binary scripting mode
 - 3.3 I/O header and hardware
 - 3.4 Various probe cable pinouts
- 4 Application guides
 - 4.1 JTAG
 - 4.1.1 JTAG and OpenOCD
 - 4.1.2 FPGA and CPLD programming
 - 4.1.3 JTAG terminal mode
 - 4.2 AVR programming
 - 4.3 flashrom
 - 4.4 Logic Analyzer with SUMP
 - 4.5 Oscilloscope (very low speed)
 - 4.6 Utilities
- 5 Chip demonstrations
- 6 Firmware upgrades
 - 6.1 Updating Bus Pirate v4
 - 6.2 Updating Bus Pirate v3
 - 6.3 Bus Pirate v3 bootloader upgrades
 - 6.4 Program with a programmer
- 7 Hardware documentation

- 7.1 Bus Pirate v3.x
- 7.2 Bus Pirate v4.x
- 7.3 Legacy hardware
- 7.4 Mods
- 7.5 Known Defects
- 7.6 Manufacturing resources
- 8 Cases and accessories
- 9 Source
- 10 License

Introduction

Features

• Features overview (original (http://dangerousprototypes.com/bus-pirate-manual/bus-pirate-feature-overview/))

Hardware

- Hardware overview
- Bus Pirate Hardware SelfTest

Tutorials

- Bus Pirate 101 tutorial (original (http://dangerousprototypes.com/2009/07/23/bus-pirate-101/))
- Bus Pirate 102 tutorial (original (http://dangerousprototypes.com/2009/11/13/bus-pirate-102/))

Forums

Bus Pirate forums (http://dangerousprototypes.com/forum/index.php?board=4.0)

Linux, OS X

- Connecting with Mac OS X (http://dangerousprototypes.com/2009/08/12/bus-pirate-connecting-with-mac-osx/)
- Bus Pirate and Mac OSX tutorial (http://dangerousprototypes.com/2010/09/03/bus-pirate-and-mac-osx-tutorial/)
- Using the Bus Pirate with Ubuntu (http://dangerousprototypes.com/2010/01/21/using-the-bus-pirate-with-ubuntu/)
- Using the Bus Pirate with Arch Linux (http://dangerousprototypes.com/2010/03/18/using-the-bus-pirate-with-archlinux/)
- Create /dev/bus pirate on Linux (http://dangerousprototypes.com/2009/10/16/create-devbus pirate-on-linux/)
- Using the Bus Pirate with Fedora 14

Download

Unfortunately it seems that Dangerous Prototypes have abandoned the Bus Pirate firmware development (and the Bus Pirate v4 also), despite that their official firmware has never reached a truly stable state: the quantity of bug report threads at forums, especially regarding flashrom, is a direct proof of that.

In attempt to change this desperate situation around BP v4, **kallisti5** (http://dangerousprototypes.com/forum/memberlist.php?mode=viewprofile&u=52560) has started a Bus Pirate

community firmware project: to include the important fixes and clean-ups, committed by several prominent community members, that Dangerous Prototypes still did not merge to official version of firmware (as of 19.09.2016) - and to continue its' future development.

Although the primary target of community is to significantly improve the user experience for Bus Pirate v4, the Bus Pirate v3 support will be maintained at all costs (despite the difficulties while compiling with the recent MPLAB XC16 compilers)

Source code and build instructions:

- Source code on Github (https://github.com/BusPirate/Bus Pirate)
- Build instructions

Latest builds of Bus Pirate community project (updated - 18th September 2016)

- https://github.com/mikebdp2/Bus_Pirate/tree/master/package_latest
- http://dangerousprototypes.com/forum/viewtopic.php?f=28&t=8498#p65290

Contains:

- 1) BPv3 different optimization firmware builds only (you could read why at the bottom of this thread https://github.com/BusPirate/Bus Pirate/issues/7)
- **2)** BPv4 three directories:

```
bootloader - different optimization bootloader builds
firmware - different optimization firmware builds
combined - I install bootloader and firmware of the same optimization, and then instantly make a complete backup of PIC24FJ256GB106
```

• 3) SHA1 checksums.txt inside each directory

About the optimization levels:

```
    0 - Do not optimize. The compiler's goal is to reduce the cost of compilation and to make debugging produce the expected results
    1 - Optimize. Optimizing compilation takes somewhat longer, and a lot more host memory for a large function. The compiler tries to
    2 - Optimize even more. The compiler performs nearly all supported optimizations that do not involve a space-speed trade-off
    s - Optimize yet more favoring size (superset of O2)
    3 - Optimize yet more favoring speed (superset of O2)
```

Even on level 3 my BPv4 passes a self test successfully, no errors. So you could try a stronger optimization first, and then - if it is causing you some issues - you tell us about them on Github and meanwhile could use a lighter optimization version (such as level 0 or 1)

Special builds of BPv3 firmware - extra features enabled!

http://dangerousprototypes.com/forum/viewtopic.php?f=28&t=8498&start=15#p65313

Your feedback and patches are welcome! Here:

- Issues on Github (https://github.com/BusPirate/Bus Pirate/issues)
- Discussion at DP forum (http://dangerousprototypes.com/forum/viewtopic.php?f=28&t=8498)

Old legacy stuff:

However, if for some reason you would like to install the official v6.1 firmware (almost 5 years old!), we wouldn't stop you. Here is a link to its' package:

Bus Pirate package v6.1
 (https://github.com/DangerousPrototypes/Downloads/blob/master/BusPirate.package.v6.1.zip)

Alternatively, you could get the sources for slightly newer (but still old) official v6.3 firmware:

Official source code v6.3 (https://github.com/DangerousPrototypes/Bus Pirate)

but please note that you will need to compile it by yourself (cannot find a compiled hex file for it)

Reference

User terminal mode

The Bus Pirate is accessed from a command line in a serial terminal. The Bus Pirate always starts in high impedance mode (Hi-Z), a safe mode with all outputs disabled. It's intended to protect any connected devices from conditions beyond their specifications. From there, a bus mode can be selected to use the Bus Pirate with a specific protocol.

- Bus Pirate user interface
- Bus Pirate menu options guide
- HEX/DEC/BIN number entry and output display
- Bus modes (main firmware): 1-Wire, UART, I2C, SPI, raw 2-wire, raw 3-wire, HD44780 LCDs, MIDI
- Bus modes (bonus firmware, v5.9+): PC keyboard, DIO, JTAG
- BASIC script engine reference
- Bus Pirate self-test guide

Binary scripting mode

The Bus Pirate has a binary access mode that can be used with software or scripts. There's example scripts in C, Python, Perl, and more (https://github.com/DangerousPrototypes/Bus Pirate/tree/master/scripts) in the GitHub repository.

Binmode protocol pages: bitbang, SPI, I2C, UART, 1-Wire, and raw-wire.

A guide to getting started with the pyBusPirateLite Python library: Bus Pirate Scripting in Python

I/O header and hardware

- Bus Pirate I/O Pin Descriptions
- Practical guide to Bus Pirate pull-up resistors
- Mixed voltage interfacing with the Bus Pirate
- LCD library and adapter

Various probe cable pinouts

- Common Bus Pirate cable pinouts
- User contributed pin reference labels (http://dangerousprototypes.com/category/pin-reference/)

Application guides

Several applications can use the Bus Pirate as a programmer or debugger.

JTAG

The Bus Pirate is a slow serial port device intended for human-speed interaction. It was NEVER intended to do JTAG duties. Because it's open source, cheap, and versatile, the community hacked various JTAG features into it. They're great in a pinch, but no substitute for the real thing!

The Bus Pirate supports JTAG in three ways: with the OpenOCD debug application, as an XSVF player, and through a user-mode terminal interface.

JTAG and OpenOCD

The Bus Pirate is supported as a JTAG programmer/debugger by OpenOCD. If your target is supported by OpenOCD it may work. Slowly:) You will need to use a Bus Pirate firmware version which supports the binary JTAG protocol—not all do.

Version	Banner	Works?
6.1	Firmware v6.1 r1676	enabled
6.0	Firmware v6.0 r1625	enabled
5.10	Firmware v5.10 (r559)	disabled
5.9-extras	Firmware v5.9 (r529) [HiZ 2WIRE 3WIRE KEYB LCD DIO]	disabled
5.9	Firmware v5.9 (r539)	disabled
5.8	Firmware v5.8 (r504)	enabled
robots custom (http://dangerousprototypes.com/forum/viewtopic.php? f=27&t=1702#p17536)	Firmware v6.0RC (r572)	enabled

Note that the above is about binary JTAG / OpenOCD support. JTAG terminal mode is not enabled in any of the above firmwares (not even 5.9-extras, even though it was advertised to be enabled in there at some point).

- Gonemad's Bus Pirate/OpenOCD walk through
- OpenOCD JTAG debugger (http://openocd.berlios.de/web/)
- Tutorial by the developer of OpenOCD support (http://michaldemin.wordpress.com/2010/02/27/how-to-buspirate-and-openocd/)
- Bus Pirate OpenOCD walk-through (http://dangerousprototypes.com/2010/04/15/bus-pirate-openocd-walk-through/)
- Bus Pirate JTAG with OpenOCD (http://dangerousprototypes.com/2010/06/07/bus-pirate-jtag/)
- JTAG posts (http://dangerousprototypes.com/category/jtag/)
- Bus Pirate & OpenOCD forum (http://dangerousprototypes.com/forum/index.php?board=27.0)
- Bus Pirate JTAG connections for OpenOCD
- Debricking Dockstar (http://dangerousprototypes.com/2011/08/17/dockstar-debricking-jtag-with-bus-pirate)

FPGA and CPLD programming

As a stand-alone XSVF player to program CPLDs/FPGAs. This requires a simple firmware swap.

Bus Pirate JTAG XSVF player

JTAG terminal mode

The -extras firmware has a the old user terminal JTAG mode from the Hack a Day demo (http://hackaday.com/2008/12/01/bus-pirate-firmware-update-v0c-jtag-and-more/). This was removed from the main firmware because nobody used it - it isn't particularly useful to enter JTAG commands manually. (OpenOCD uses the *binary* JTAG mode, which is different from the user terminal JTAG mode.)

■ JTAG terminal mode guide

AVR programming

The Bus Pirate can be used to program Atmel AVR microcontrollers. AVRDUDE version 5.8 and later support the Bus Pirate as a programmer directly. AVRDude is the most common software for programming AVR microcontrollers. Many packages such as WinAVR and the Arduino IDE bundle a version of AVRDude.

There is also an alternate firmware that can be installed on to Bus Pirate to make it an STK500 v2 AVR programmer clone.

See Bus Pirate AVR Programming - Instructions and resources for AVR programming with the Bus Pirate.

flashrom

A flash chip programmer for Linux and Windows.

- flashrom bios/flash programmer (http://www.flashrom.org/Flashrom) (Latest Windows compile from forum (http://dangerousprototypes.com/forum/index.php?topic=154.msg2755#msg2755))
- Bus Pirate flashrom forum (http://dangerousprototypes.com/forum/index.php?board=40.0)

Logic Analyzer with SUMP

The Bus Pirate can act as a low speed logic analyzer using a SUMP-compatible protocol. Jawi's latest OLS client (http://www.lxtreme.nl/ols/) includes extended support for the Bus Pirate.

Logic analyzer mode (original (http://dangerousprototypes.com/2009/11/03/bus-pirate-logic-analyzer-mode/))

Oscilloscope (very low speed)

- Python Oscilloscope script (http://dangerousprototypes.com/forum/index.php?topic=976.0)
- Python Oscilloscope script info
- miniscope v3 (http://tomeko.net/miniscope v3/index.php)
- miniscope info
- PirateScope in GIT (http://github.com/tgvaughan/PirateScope) (Python, Discussion (http://dangerousprototypes.com/forum/index.php?topic=1513.0))

Utilities

Binary SPI sniffer utility

Chip demonstrations

Here's a comprehensive list of Bus Pirate (http://www.buspirate.com) chip demonstrations. It includes Ian's old demonstrations from Hack a Day (http://hackaday.com/category/parts/), and the most recent demos from Dangerous Prototypes (http://dangerousprototypes.com). Tutorials are arranged by Bus Pirate hardware version.

■ Interfacing to on-board 24AA64 I2C EEPROM

Bus Pirate v2&v3

- MMA7455L 3-axis accelerometer (SPI, IC2)
- 3EEPROM explorer board (original (http://dangerousprototypes.com/2009/07/30/prototype-bus-pirate-3eeprom-explorer-board/)), DS2431 (1wire), 24AA (I2C), 25AA (SPI)
- Wii Nunchuck quick guide (original (http://dangerousprototypes.com/2009/08/19/bus-pirate-wii-nunchuck-quick-guide/)) (I2C)
- iButton serial number (original (http://dangerousprototypes.com/2009/11/23/chips-1-wire-ibutton-overview/)) (1-Wire)
- 2AA02E48 EEPROM with MAC address (original (http://dangerousprototypes.com/2009/10/06/chips-24aa02e48-i2c-eeprom-with-mac-address/)) (I2C)
- HD44780 LCD adapter v2 (LCD)
- DS1307 real time clock (original (http://dangerousprototypes.com/2009/09/04/chips-ds1307-i2c-real-time-clock/)
) (I2C)
- NA204SD02 Futaba VFD character display (http://hackaday.com/2009/07/13/parts-4x20-vfd-character-display-na204sd02/) (raw2wire)
- SLE4442 (FedEx Kinko's) smart card update (original (http://dangerousprototypes.com/2009/08/31/bus-pirate-sle4442-smart-card-update/)) (raw2wire)
- 25AA/25LC serial EEPROM (http://hackaday.com/2009/06/30/parts-spi-eeprom-25aa25lc/) (SPI)
- ShiftBrite with A6281 3 channel LED driver (http://hackaday.com/2009/06/29/parts-shiftbrite-rgb-led-module-a6281/) (SPI)
- PCF8563 real time clock calendar (http://hackaday.com/2009/06/26/parts-i2c-real-time-clock-calendar-pcf8563/)
 (I2C)
- Sure LED matrix (original (http://dangerousprototypes.com/2010/01/18/demo-sure-led-matrix/)) (raw3wire)
- AT45DB041D 4Mbit flash memory (original (http://dangerousprototypes.com/2010/01/25/demo-at45db041d-4mbit-spi-rom/)) (SPI)
- HMC6352 digital compass (original (http://dangerousprototypes.com/2010/02/01/demo-hmc6352-digital-compass-breakout-board/)) (I2C)
- Parallax RFID reader (original (http://dangerousprototypes.com/2010/02/15/demo-parallax-rfid-reader/))
 (UART)
- KS0074 serial LCD (original (http://dangerousprototypes.com/2010/02/08/demo-ks0074-serial-lcd/)) (SPI)
- SURE DE-DP014 character matrix (http://dangerousprototypes.com/2010/07/12/demo-sure-electronics-de-dp014/) (raw3wire)
- 23K256 32K serial RAM (SPI)
- 74F138 binary decoder (http://dangerousprototypes.com/2010/12/25/74f138-xmas-demo/)
- DS28EA00 temperature sensor (1-Wire)
- MCP9800 temperature sensor (I2C)
- DS18S20 temperature sensor (1-Wire) (Original (http://www.settorezero.com/wordpress/il-bus-pirate-cosa-e-come-funziona-esempio-di-comunicazione-con-bus-1-wire/))
- Servo driver demo

Bus Pirate vla

- LTC2640 digital to analog converter (http://hackaday.com/2009/01/22/how-to-bus-pirate-v1-improved-universal-serial-interface/) (SPI, raw3wire)
- LTC2631A digital to analog converter (http://hackaday.com/2009/02/09/parts-ltc2631a-i2c-digital-to-analog-converter/) (I2C)
- PC keyboard (http://hackaday.com/2009/01/26/parts-at-keyboard/) (PC Keyboard)

- DS1807 audio volume potentiometer (http://hackaday.com/2009/02/16/parts-i2c-audio-volume-potentiometer-ds1807/) (I2C)
- SparkFun 4×4 RGB button pad controller (http://hackaday.com/2009/03/09/parts-4x4-rgb-button-pad-controller-spi/) (raw3wire)
- DS1801 audio volume potentiometer (http://hackaday.com/2009/03/16/parts-ds1801-spi-audio-volume-potentiometer/) (SPI)
- MCP6S26 programmable gain amplifier (http://hackaday.com/2009/03/30/parts-programmable-gain-amplifier-mcp6s26/) (raw3wire)

Bus Pirate v0a

- 24LC1024 EEPROM (http://hackaday.com/2008/11/19/how-to-the-bus-pirate-universal-serial-interface/#EEPROM) (I2C)
- EM406 GPS (http://hackaday.com/2008/11/19/how-to-the-bus-pirate-universal-serial-interface/) (UART)
- SLE4442 (FedEx Kinko's) smart card (http://hackaday.com/2008/11/25/how-to-read-a-fedex-kinkos-smart-card-sle4442/) (raw2 wire)
- DS1077 133MHz programmable oscillator (http://hackaday.com/2008/11/28/parts-133mhz-162khz-programmable-oscillator-ds1077/) (I2C)
- XC9572XL CPLD (http://hackaday.com/2008/12/01/bus-pirate-firmware-update-v0c-jtag-and-more/) (JTAG)
- DS1822 temperature sensor (http://hackaday.com/2008/12/10/parts-1-wire-temperature-sensor-ds1822) (1-Wire)
- PCF8574 IO Expander (http://hackaday.com/2008/12/27/parts-8bit-io-expander-pcf8574/) (I2C)
- DS2431 1K EEPROM (http://hackaday.com/2008/12/24/parts-1k-1-wire-eeprom-ds2431/) (1-Wire)
- SHT1x/7x humidity and temperature sensor (http://hackaday.com/2008/12/29/parts-precision-humidity-and-temperature-sensor-sht1x7x/) (raw2wire)
- TC74 temperature sensor (http://hackaday.com/2009/01/02/parts-i2c-digital-thermometer-tc74/) (I2C)

User submitted hacks and demos

- Wii Motion Plus controller (http://dangerousprototypes.com/2010/10/15/bus-pirate-wii-motion-plus-demo/) (I2C)
- Nokia 3310 LCD (http://dangerousprototypes.com/2009/10/19/bus-pirate-nokia-3310-lcd/) (binary SPI)
- Hack the MD80 firmware to remove the date display (http://dangerousprototypes.com/2010/11/01/hack-the-md80-firmware-to-remove-the-date/) (SPI sniffer)
- Program STM32 (http://dangerousprototypes.com/2010/10/05/program-stm32-with-the-bus-pirate/) (raw2wire)
- I2C magnetic compass sensor (http://dangerousprototypes.com/2010/10/04/i2c-magnetic-compass-sensor-demo/)
 (I2C)
- TK-1315LA GPS (http://dangerousprototypes.com/2010/09/03/tk-1315la-gps-connection-guide/) (serial UART)
- SparkFun LED Matrix (http://dangerousprototypes.com/2010/06/04/bus-pirate-led-matrix/)
- MAX147 ADC (http://dangerousprototypes.com/2010/05/19/bus-pirate-max147-adc-spi-demo/) (SPI)
- MMA7456L three-axis accelerometer (http://dangerousprototypes.com/2010/05/11/bus-pirate-mma7456l-three-axis-accelerometer/) (I2C/SPI)
- FSA03 GPS (http://dangerousprototypes.com/2010/04/03/bus-pirate-and-fsa03-gps-how-to/) (serial UART)
- Wavecom GSM modem (http://dangerousprototypes.com/2010/02/09/bus-pirate-uart-bridge-with-wavecom-gsm-modem/) (serial UART bridge)
- Sure 8×32 LED matrix marquee (http://dangerousprototypes.com/2010/02/02/bus-pirate-script-for-8x32-led-matrix/)
- Zeno zit zapper tip reset (http://dangerousprototypes.com/2009/11/28/zeno-zit-zapper-tip-reset/) (I2C)
- 1-Wire iButton (http://dangerousprototypes.com/2009/11/23/chips-1-wire-ibutton-overview/) (1-Wire)
- Bus Pirate high voltage power supply (http://dangerousprototypes.com/2009/11/22/bus-pirate-high-voltage-power-supply/) (PWM)
- Sniffing between smartcard & terminal (http://wiki.yobi.be/wiki/Bus_Pirate#7816-3 T.3D0 at arbitrary baudrate) (serial UART at arbitrary baudrate)
- Bus Pirate BlinkM demo

- AK8975 magnetometer (http://dangerousprototypes.com/2011/07/18/read-the-ak8975-magnetometer-with-a-bus-pirate/) (I2C)
- TMP102 temperature sensor (http://dangerousprototypes.com/2011/07/17/bus-pirate-tmp102-i2c-temperature-sensor/) (I2C)
- MMA8453Q accelerometer (http://dangerousprototypes.com/2011/07/12/bus-pirate-interfacing-mma8453q-accelerometer/) (I2C)
- Thinkpad T30 password reset (http://dangerousprototypes.com/2011/06/10/ibm-thinkpad-t30-bios-password-reset-with-the-bus-pirate/) (I2C)
- BPv4 HD66717 I2C demo (I2C)
- Unbricking the Bullet2 (http://dclavijo.blogspot.com/2011/01/unbricking-bullet2english.html)
- BPv3 blink MODE LED demo
- BPv3 ADC demo
- BPv3 FTDI UART demo
- BPv3 RFM12B radio data module demo (http://dangerousprototypes.com/2012/08/27/bus-pirate-spi-mini-tutorial-using-an-rfm12b/)

Bus Pirate Education Kit

mentioned in Workshop Video 52 (http://dangerousprototypes.com/2013/01/17/workshop-video-52-bus-pirate-educational-kit-vfd-tube-module/), and a preliminary DIP and nylon breadboard friendly parts manifest (http://dangerousprototypes.com/2013/02/13/bus-pirate-education-kit-parts/), pending a formal webpage with lab experiment write-ups (http://dangerousprototypes.com/docs/Bus Pirate Edu kit partlist)

Firmware upgrades

Instructions for changing the firmware in a Bus Pirate over USB.

- Download the latest firmware package
- Bus Pirate firmware change history

Updating Bus Pirate v4

- Bus Pirate v4 and pirate loader (only firmware updates)
- Bus Pirate v4 and Pickit2 (bootloader and firmware updates)

Updating Bus Pirate v3

```
With firmware v5.9 we ran out of space in hardware v3.
We can't fit all the new goodness in one firmware.
Firmware release v5.9+ includes a bonus firmware with some extra modes.
Bootload the BPv3-Firmware-vx.x-extras.hex file to try the new modes.
Currently: digital IO mode (DIO), JTAG, PC keyboard, more
```

Download the latest firmware package

This section is for **Bus Pirate v3.x** with bootloader version 4+. See the next section for more bootloader information.

- Pirate-Loader console upgrade application (Linux, Mac, Windows) (original (http://dangerousprototypes.com/2010/01/22/how-to-firmware-upgrades-with-the-linux-mac-windows-console/))
- ds30 Loader GUI (original (http://dangerousprototypes.com/2010/02/19/bus-pirate-firmware-updates-with-ds30-loader/)) (Mono/.NET)

Follow these instructions to load new firmware, older firmware (v4+ only), the extras firmware, or the XSVF player firmware.

Bus Pirate v3 bootloader upgrades

The bootloader is a small program in the Bus Pirate that installs the firmware upgrade. The bootloader itself can also be upgraded with a small amount of effort. This is not normally required.

Bus Pirate v3 bootloader upgrades

Program with a programmer

How to put the bootloader in a new chip or replace a damaged bootloader.

• Program the bootloader with a PICkit or ICD programmer

Hardware documentation

Bus Pirate v3.x



v3.6 is a minor update to v3 available from June 2012. All changes are cosmetic, v3.6 works exactly the same as previous v3s.

• v3.6 hardware overview

v3a, v3b, and v3.5 were available from October 2009 to June 2012. A similar version (http://dangerousprototypes.com/2009/12/22/sparkfuns-bus-pirate/) is sold by SparkFun.

- v3.5 hardware overview
- v3b hardware overview (original (http://dangerousprototypes.com/2010/04/29/prototype-bus-pirate-v3b/))
- v3a hardware overview (original (http://dangerousprototypes.com/2009/10/04/prototype-bus-pirate-v3/))
- v3a and v3b schematic and partlist

v3.x is based on the original v2go circuit using a PIC24FJ64GA002 microcontroller.

- PIC24FJ64GA002 datasheet (http://www.microchip.com/wwwproducts/Devices.aspx?dDocName=en026374)
- PIC24FJ family reference manual (http://www.microchip.com/stellent/idcplg? IdcService=SS GET PAGE&nodeId=2575)

Bus Pirate v4.x



■ Bus Pirate v4 vs v3 comparison

v4 is currently in development. The first batch is available, but the hardware is still experimental.

- Bus Pirate v4 design overview
- See it come to life, v4 development history and prototypes

Legacy hardware

Bus Pirate v2go (http://hackaday.com/2009/06/25/how-to-the-bus-pirate-v2-with-usb/) was the first USB Bus Pirate. Around 1000 were sold as a fundraiser for Hack a Day. The PCB is black.

v1a (http://hackaday.com/2009/01/22/how-to-bus-pirate-v1-improved-universal-serial-interface) and v0a (http://hackaday.com/2008/11/19/how-to-the-bus-pirate-universal-serial-interface/) were serial port Bus Pirate prototypes. Fundamental Logic briefly sold a through-hole kit for v1a.

Mods

- User created cases (http://dangerousprototypes.com/category/cases/)
- 2K ohm MOSI pull-up resistor mod (http://dangerousprototypes.com/2009/09/02/bus-pirate-2k-pull-up-resistor-mod/)
- Modify the FTDI EEPROM, change serial name on OSX (http://dangerousprototypes.com/2010/01/27/pirate-rename-get-a-nicely-named-serial-device/)
- Create /dev/bus_pirate on Linux (http://wiki.countercaster.com/The_Bus_Pirate_on_Linux:_From_/dev/ttyUSB%3F_to_/dev/bus_pirate)

Known Defects

- Bus Pirate v3 preorder 2 & 3 dead MODE LED fix (http://dangerousprototypes.com/2010/01/14/bus-pirate-v3-preorder-2-broken-led-fix/)
- Bus Pirate v2go preorder 2 shipped with v2 bootloader only (http://dangerousprototypes.com/2009/09/10/bus-pirate-preorder-2-new-bootloader-no-firmware/)

Manufacturing resources

We maintain a combined bootloader/firmware flash image to assist in manufacturing.

- bpv3-BL44FW510-DUMP.hex is bootloader v4.4 with firmware v5.10
- Latest version in Git (https://github.com/DangerousPrototypes/Bus_Pirate/tree/master/manufacturing_resources/v3.6/firmware_dump)

There is also an automated self-test utility, but it is generally slower than doing it manually

Power tools (https://github.com/DangerousPrototypes/Bus Pirate/tree/master/scripts/powertools)

Cases and accessories



- LCD adapter v2 (\$9 (http://www.seeedstudio.com/depot/bus-pirate-lcd-adapter-v2-p-684.html?cPath=61 68))
- Probe cable (http://hackaday.com/2009/07/02/how-to-bus-pirate-probe-cable/) (\$4.99 (http://www.seeedstudio.com/depot/bus-pirate-probe-kit-p-526.html?cPath=178_180))
- Synthetos laser-cut acrylic case (https://www.synthetos.com/webstore/index.php/) (\$14.99)

- How-to: Assemble the Synthetos case (http://dangerousprototypes.com/2010/10/21/assemble-the-synthetos-acrylic-bus-pirate-case/)
- User created cases (http://dangerousprototypes.com/category/cases/)
- Bus Pirate LCD adapter v1 deprecated

Source

- Github repository new, community, v7.0 (https://github.com/BusPirate/Bus Pirate)
- GitHub repository old, official, v6.3 (https://github.com/DangerousPrototypes/Bus Pirate)
- SVN google repository very old, official, v5.10 (http://code.google.com/p/the-bus-pirate/downloads/list)
- Compile the Bus Pirate firmware
- Spanish, Italian translations (http://dangerousprototypes.com/2009/08/26/bus-pirate-spanish-italian-translations/)

License

- PCB art: CC-0 (http://creativecommons.org/publicdomain/zero/1.0/)
- Bootloader: GPL (http://www.gnu.org/licenses/gpl.html) (from ds30 Loader project (http://mrmackey.no-ip.org/elektronik/ds30loader/))
- Firmware: CC-0 (http://creativecommons.org/publicdomain/zero/1.0/)
- The Bus Pirate pinout, menu, and command tables are released into the public domain.

Retrieved from "http://dangerousprototypes.com/docs/Bus_Pirate" Categories: Projects | Bus Pirate

jump to

- Projects homepage
- Bus Pirate
- USB Infrared toy
- Web platform
- Bus Blaster
- Logic Sniffer
- CPLD dev boards
- Logic Shrimp
- Breakout boards
- Proto boards
- #twatch network LCD
- Flash Destroyer
- In development
- Partlist and reference
- Recent changes

projects



buy stuff

- Bus Pirate v3
- USB Infrared Toy
- web platform
- Bus Blaster v2
- Logic Sniffer

Search

Go Search

Toolbox

- What links here
- Related changes
- Special pages
- Printable version
- Permanent link

Views

- Page
- Discussion
- View source
- History

Personal tools

Log in

Site

• This page was last modified on 20 September 2016, at 18:26.

Copyright 2011 Where Labs, LLC. CC BY-SA unless otherwise noted.

Connect

- Follow us on Twitter
- Subscribe to our feed
- Subscribe to our comments
- Visit our Facebook page
- Send us an email

Projects

• Get a Bus Blaster v2

- Get a Bus Pirate v3
- Get a Flash Destroyer kit
- Get a Logic Sniffer
- Get a USB Infrared Toy

About Us

Our goal is to release a new open source hardware project every month. Copies of our projects are available through Seeed Studio and our distributors.