

Project Configuration Report

DoomMG24BLE

Target Device:

Target Part: EFR32MG24A010F1536IM48

Boards:

Target SDK

Gecko SDK Suite v4.1.0: Amazon 202012.00, Bluetooth 4.0.0, Bluetooth Mesh 3.0.0, EmberZNet 7.1.0.0, Flex 3.4.0.0, MCU 6.3.0.0, Micrium OS Kernel 5.13.00, OpenThread 2.1.0.0 (GitHub-8f92d2dc8), Platform 4.1.0.0, USB 1.0.0.0, Wi-SUN 1.3.0.0, Z-Wave SDK 7.18.0.0
SDK Version: 4.1.0

Project Generators

- Simplicity IDE Project

Installed Software Components:

- Application

- *Utility*

- Assert**

- Configurable: true

- Configs: app_assert_config.h

- Bluetooth

- *Feature*

- Connection**

- Configurable: true

- Configs: sl_bluetooth_connection_config.h

- Even Connection Scheduling Algorithm**

- Configurable: false

- Legacy Advertising**

- Configurable: false

- Scanner Base Feature**

- Configurable: false

- Scanner for legacy advertisements**

- Configurable: false

- *GATT*

Configuration

Configurable: true

Configs: gatt_configuration.btconf

- *Stack*

Bluetooth Core

Sources: sl_bt_stack_init.c, sl_bt_mbedtls_context.c

Configurable: true

Configs: sl_bluetooth_config.h

GATT Client

Configurable: false

GATT Server

Configurable: false

Security Manager

Configurable: false

System

Configurable: false

• Platform

- *Device*

• EFR32MG24

EFR32MG24A010F1536IM48

Sources: startup_efr32mg24.c, system_efr32mg24.c

Configurable: true

Configs: device.yaml

- *Peripheral*

EUSART

Sources: em_eusart.c

Configurable: false

I2C

Sources: em_i2c.c

Configurable: false

LDMA

Sources: em_ldma.c

Configurable: false

TIMER

Sources: em_timer.c

Configurable: false

USART

Sources: em_usart.c
Configurable: false

VDAC

Sources: em_vdac.c
Configurable: false

- Radio

RAIL Utility, PTI

Sources: sl_rail_util_pti.c
Configurable: true
Configs: sl_rail_util_pti_config.h

- Utilities

Component Catalog

Configurable: false

• Services

Simple MPU

Sources: sl_mpu.c
Configurable: false

- Device Initialization

Device Init: DPLL

Sources: sl_device_init_dpll_s2.c
Configurable: true
Configs: sl_device_init_dpll_config.h

Pins:

Pin #	Pin Name	Function	Custom Pin Name	Software Component
26	PA00			
27	PA01			
28	PA02			
29	PA03			
30	PA04			
31	PA05			
32	PA06			
33	PA07			
34	PA08			
35	PA09			
23	PB00			
22	PB01			
21	PB02			

20	PB03			
19	PB04			
18	PB05			
1	PC00			
2	PC01			
3	PC02			
4	PC03			
5	PC04			
6	PC05			
7	PC06			
8	PC07			
9	PC08			
10	PC09			
48	PD00			
47	PD01			
46	PD02			
45	PD03			
44	PD04			
43	PD05			

Peripherals:

Peripheral	Software Component	Custom Peripheral Name
ACMP0		
ACMP1		
CMU		
EUSART0		
EUSART1		
GPIO		
HFXO0		
I2C0		
I2C1		
KEYSCAN		
LETIMER0		
LFXO		
MODEM		
PCNT0		
PRS Channel ASYNCH0		
PRS Channel ASYNCH1		
PRS Channel ASYNCH10		
PRS Channel ASYNCH11		
PRS Channel ASYNCH12		
PRS Channel ASYNCH13		
PRS Channel ASYNCH14		
PRS Channel ASYNCH15		
PRS Channel ASYNCH2		
PRS Channel ASYNCH3		
PRS Channel ASYNCH4		
PRS Channel ASYNCH5		
PRS Channel ASYNCH6		

PRS Channel ASYNCH7		
PRS Channel ASYNCH8		
PRS Channel ASYNCH9		
PRS Channel SYNCH0		
PRS Channel SYNCH1		
PRS Channel SYNCH2		
PRS Channel SYNCH3		
PTI		
TIMER0		
TIMER1		
TIMER2		
TIMER3		
TIMER4		
USART0		
VDAC0		
VDAC1		