



Table 3.1 ADDR_SEL Resistor Setting

RESISTOR (Ω)	SMBUS ADDRESS	RESISTOR (Ω)	SMBUS ADDRESS
0	1001_1000(nw)	1600	0101_0000(nw)
100	1001_1010(nw)	2000	0101_0010(nw)
180	1001_1100(nw)	2700	0101_0100(nw)
300	1001_1110(nw)	3600	0101_0110(nw)
430	1001_0000(nw)	5600	0101_1000(nw)
560	1001_0010(nw)	9100	0101_1010(nw)
750	1001_0100(nw)	20000	0101_1100(nw)
1270	1001_0110(nw)	Open	0001_0000(nw)

Sampling freq 2.5ms 80ms(default)
R= 0.1 ohm ADC bits= 6 11
mV range A range Resolution
10 +/- 0.1 A 1.587301587 0.04885197851 mA
20 +/- 0.2 A 3.174603175 0.09770395701 mA
40 +/- 0.4 A 6.349206349 0.195407914 mA
80 +/- 0.8 A 12.6984127 0.390815828 mA

Sampling freq 2.5ms 80ms(default)
R= 1 ohm ADC bits= 6 11
mV range A range Resolution
10 +/- 0.01 A 0.1587301587 0.004885197851 mA
20 +/- 0.02 A 0.3174603175 0.009770395701 mA
40 +/- 0.04 A 0.6349206349 0.0195407914 mA
80 +/- 0.08 A 1.26984127 0.0390815828 mA

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