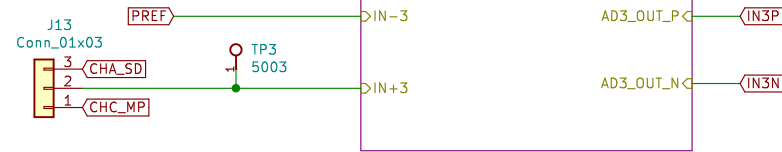
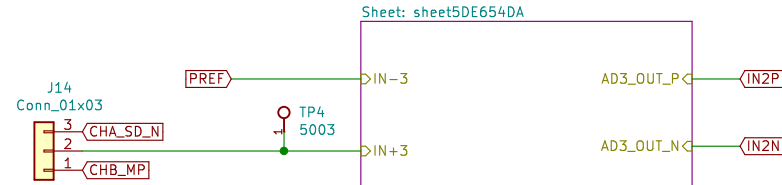


AFE circuits designs

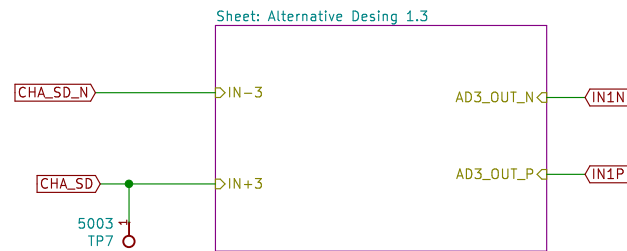


File: Input2.sch

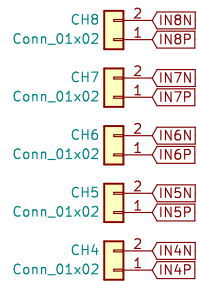


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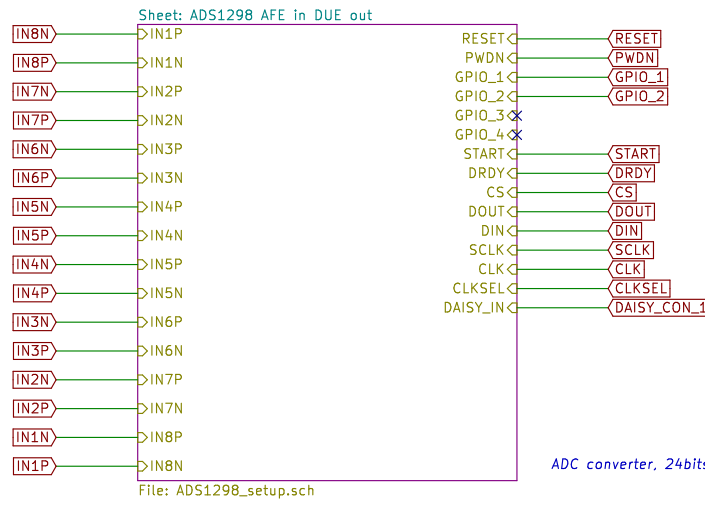
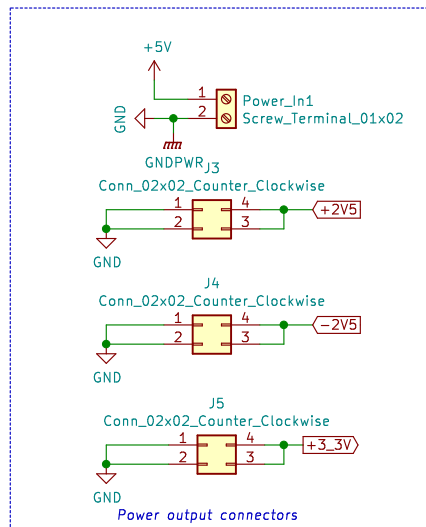
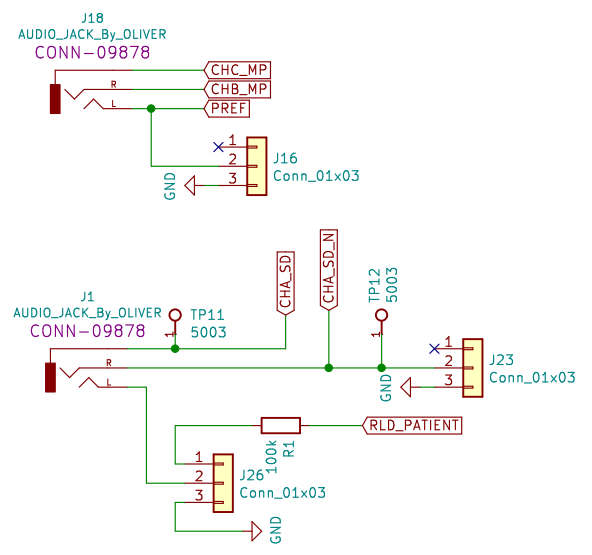
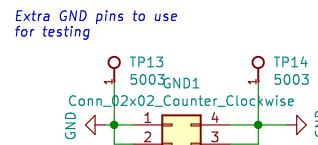
Selection to choose between
If all the amplifier are connected to on
input (test if SD if same as 2MP signals) otherwise
it can be used as MP channels



File: AlternativeDesign3.sch



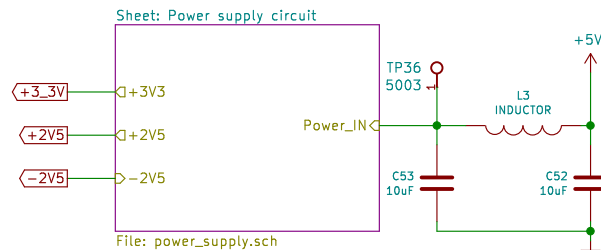
Free inputs
for testing of external
AFE circuit



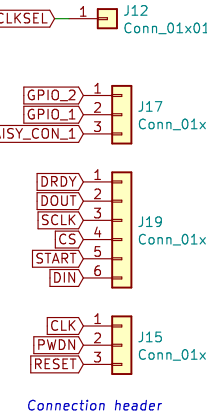
ADC converter, 24bits

Power converters

Input 5V, output +/-2.5V and +3.3V

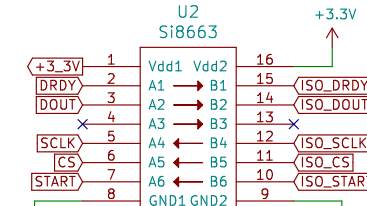


File: power_supply.sch

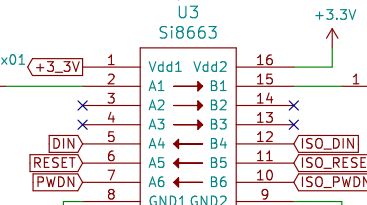


Isolated Side Not isolated side

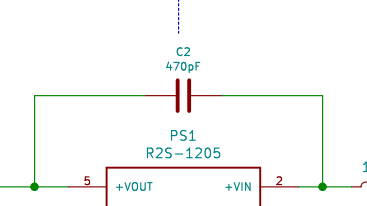
Optical Isolation barrier



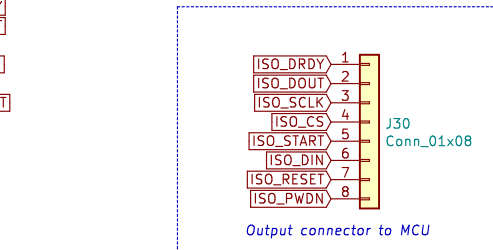
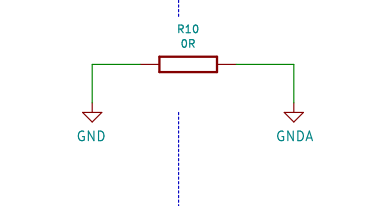
Optical Isolation barrier



Optical Isolation barrier



Optical Isolation barrier



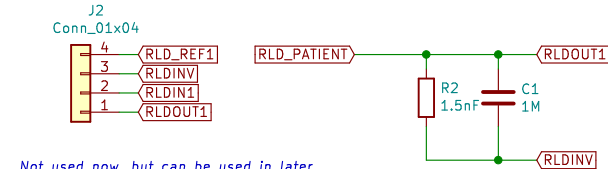
Test pin to verify the isolation barrier

Power supply isolated (medical 1kV), 12V in, 5V out and EMC filter

DC power jack connector 2.1mm

Additional ground and power supply

Driven Right leg



Not used now, but can be used in later
stage for testing

HD-SEMG system based on INA333

Oliver Kersten Engineering

Sheet: /

File: HDSEMG.sch

Title: HDSEMG system

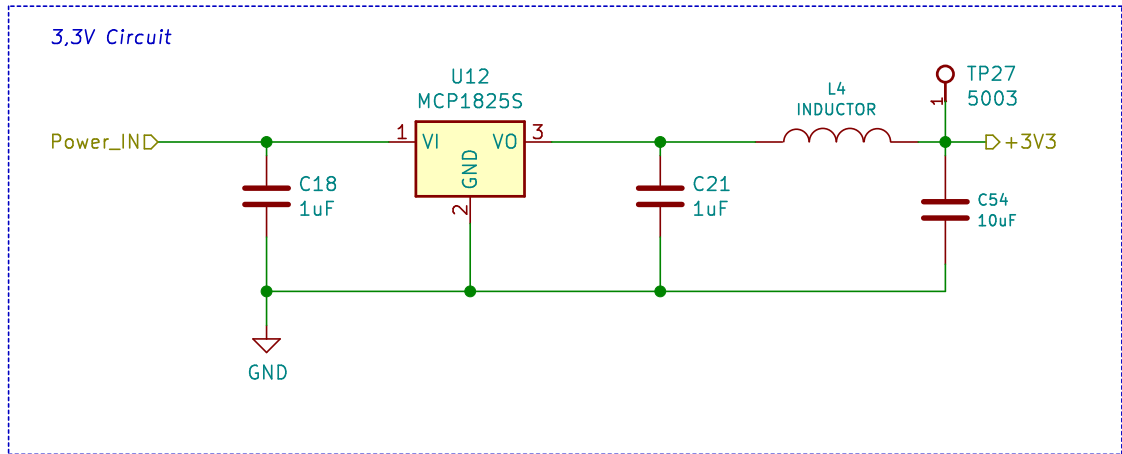
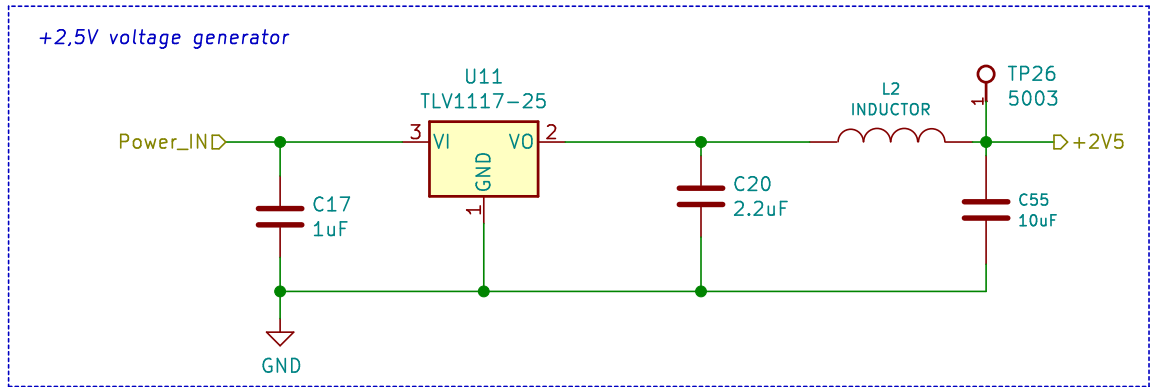
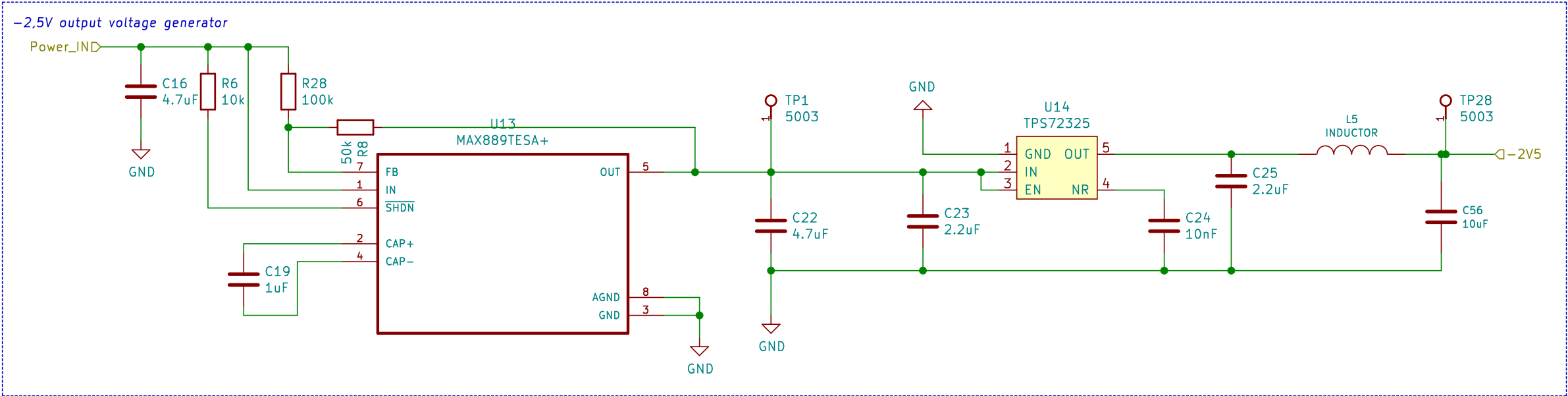
Size: A3

Date: 2019-09-30

Rev: V2

KiCad E.D.A. kicad (5.1.4)-1

Id: 1/6



Sheet: /Power supply circuit/
File: power_supply.sch

Title:

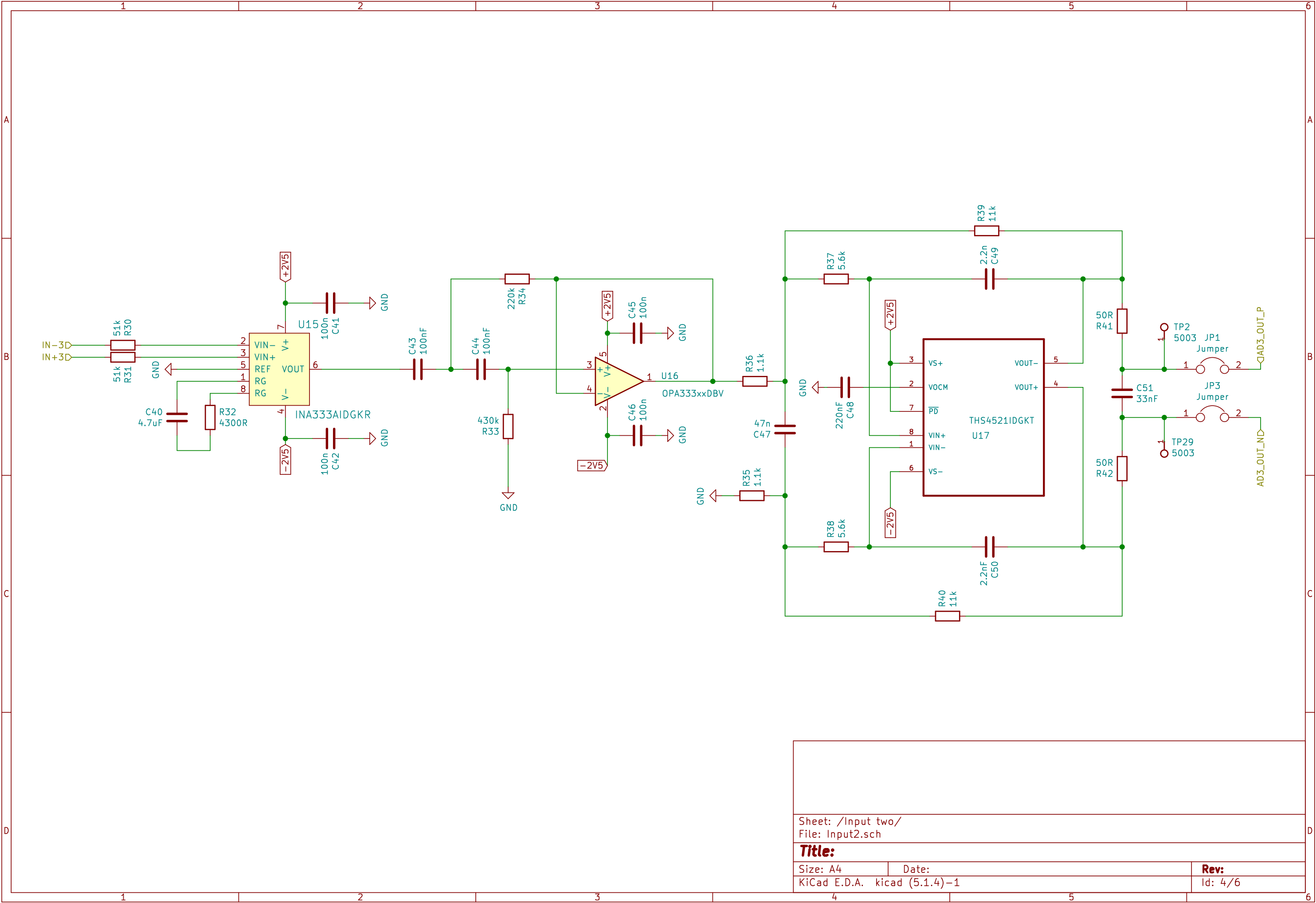
Size: A4

Date:

KiCad E.D.A. kicad (5.1.4)-1

Rev:

Id: 2/6



Sheet: /Input two/
File: Input2.sch

Title:

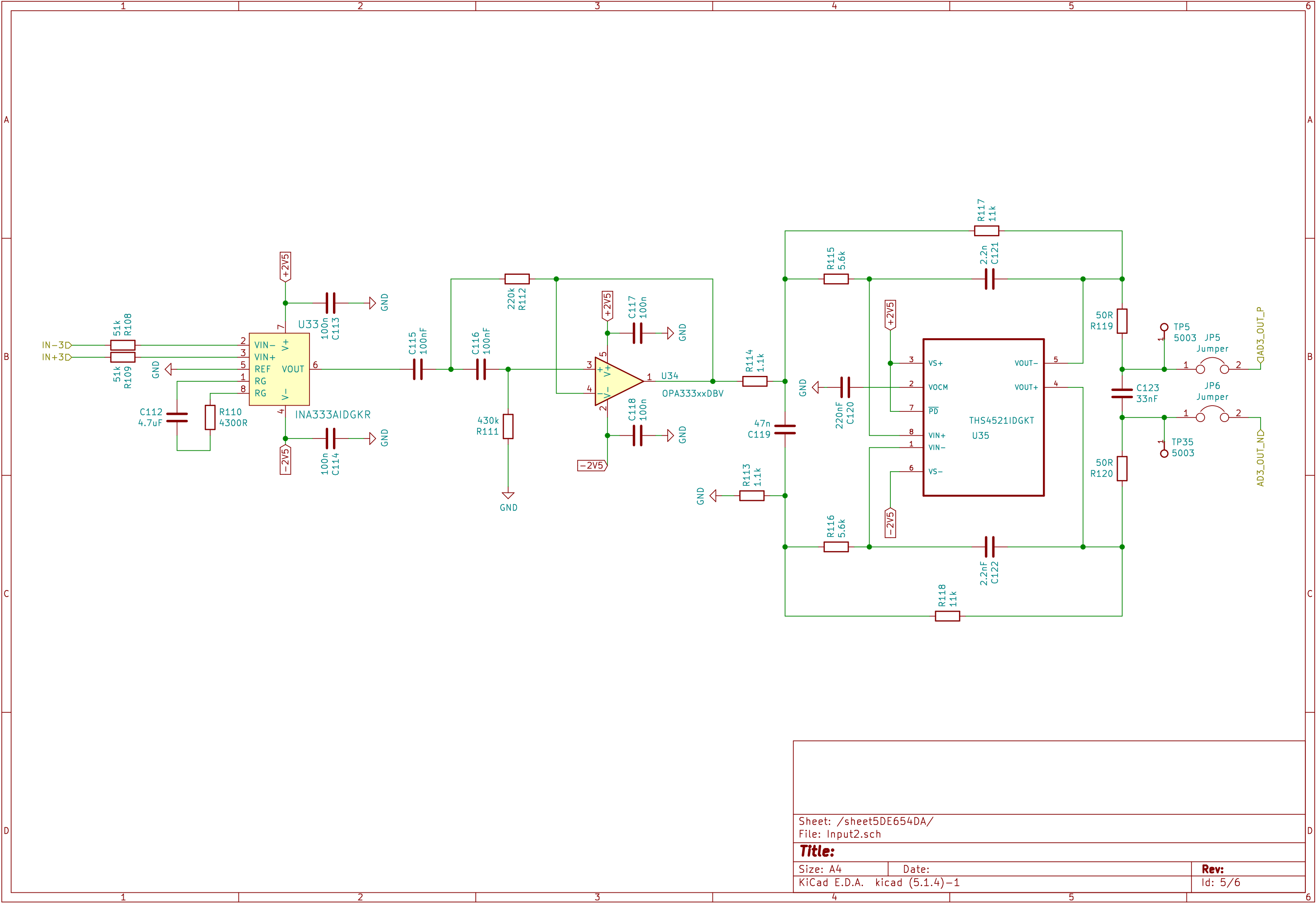
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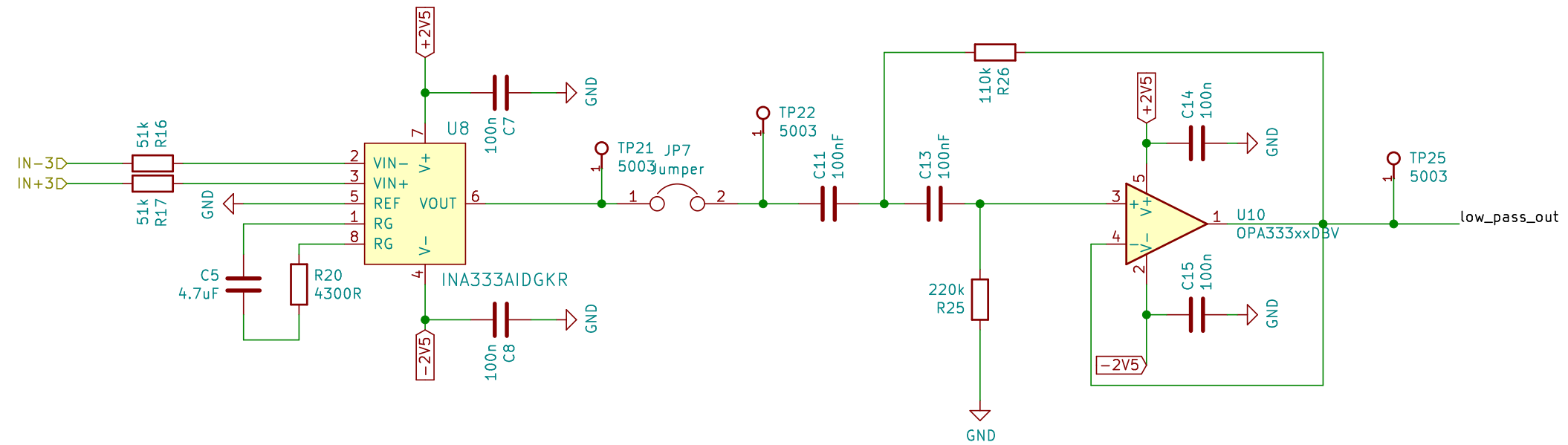
KiCad E.D.A. kicad (5.1.4)-1

Rev:

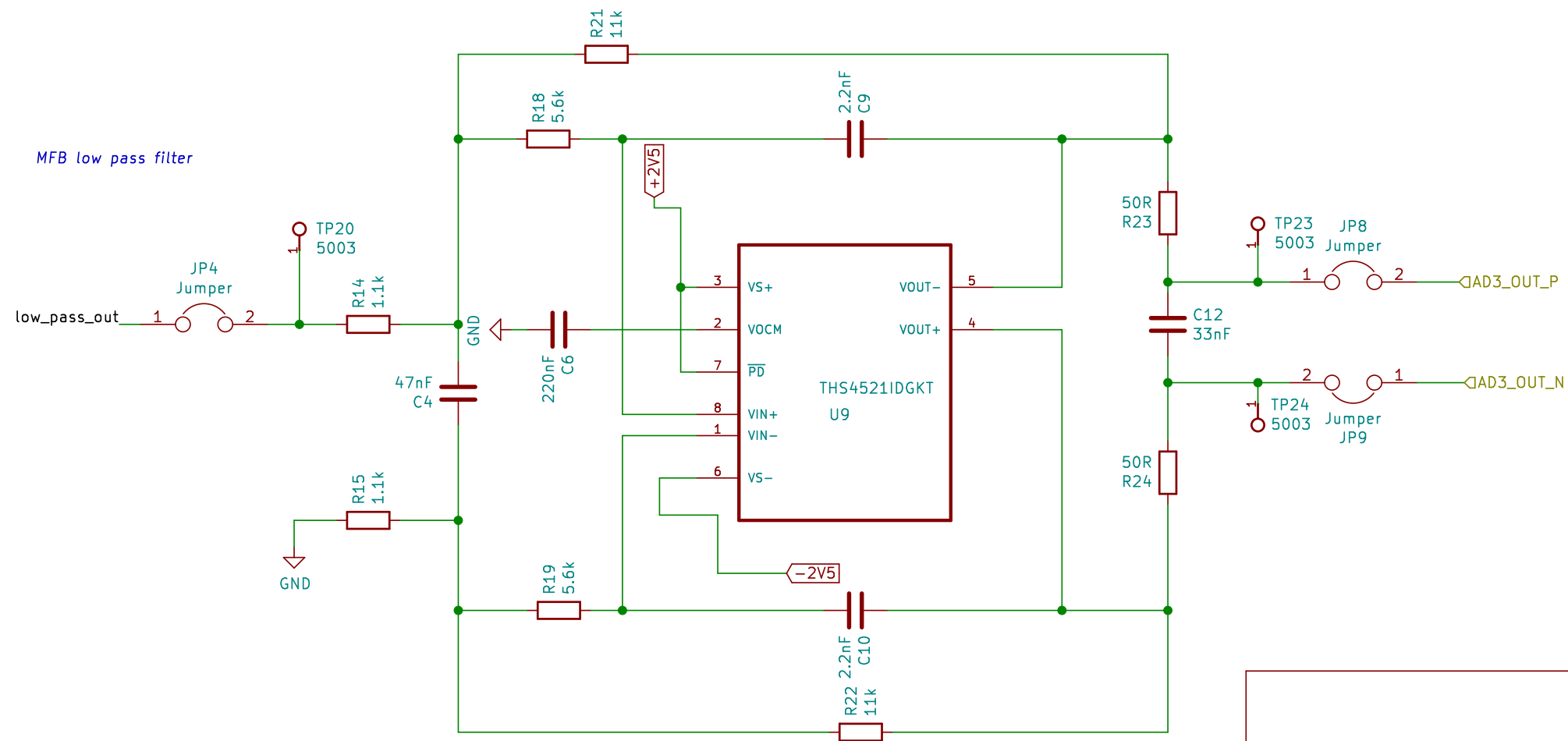
Id: 4/6



Test circuit 1



MFB low pass filter



Sheet: /Alternative Desing 1.3/
File: AlternativeDesign3.sch

Title:

Size: A4

Date:

Rev:

KiCad E.D.A. kicad (5.1.4)-1

Id: 6/6