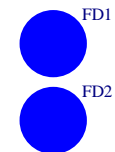
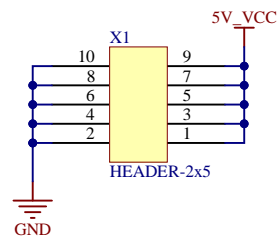
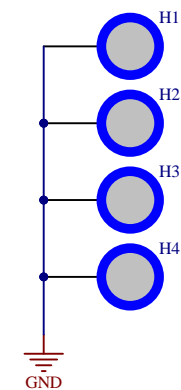


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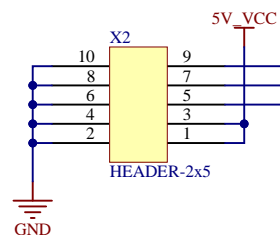


ESP32
esp32.SchDoc

SW_SELECT
SW_PLUS
SW_MIN

Buttons
buttons.SchDoc

SW_SELECT
SW_PLUS
SW_MIN



MOSI
SCK
CS

RTC
rtc.SchDoc


SDA
SCL

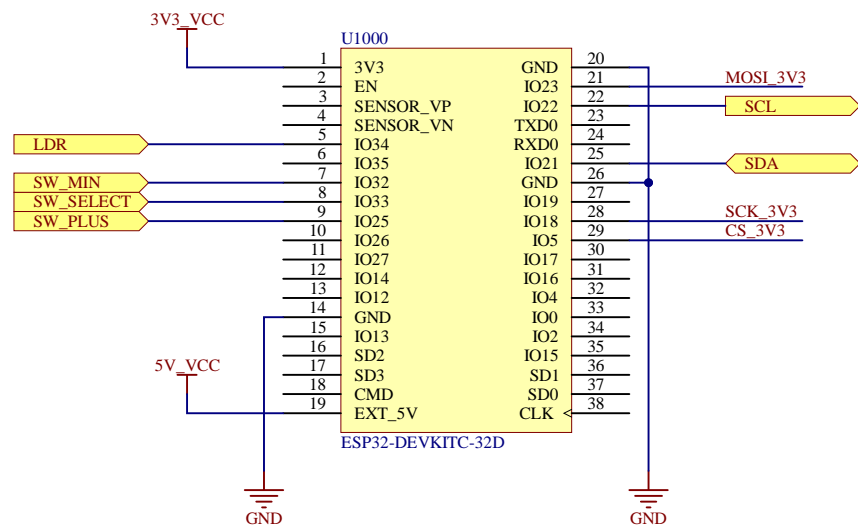
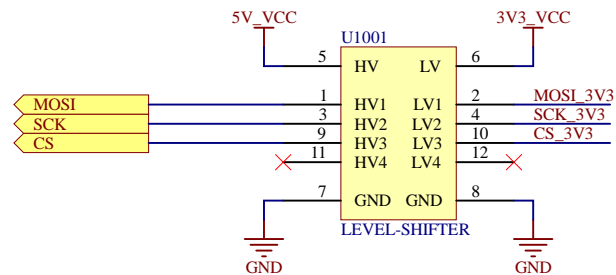
SDA
SCL

LDR
ldr.SchDoc

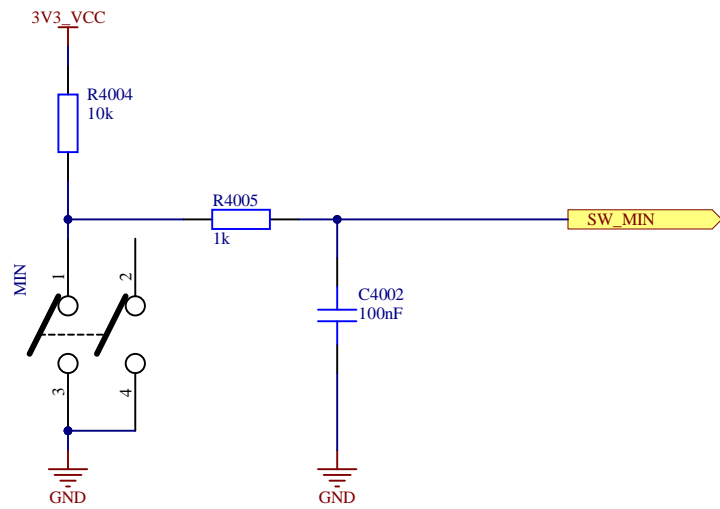
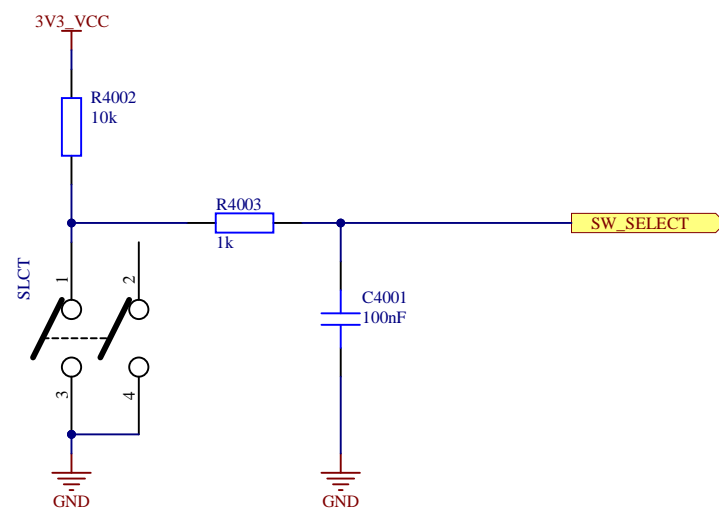
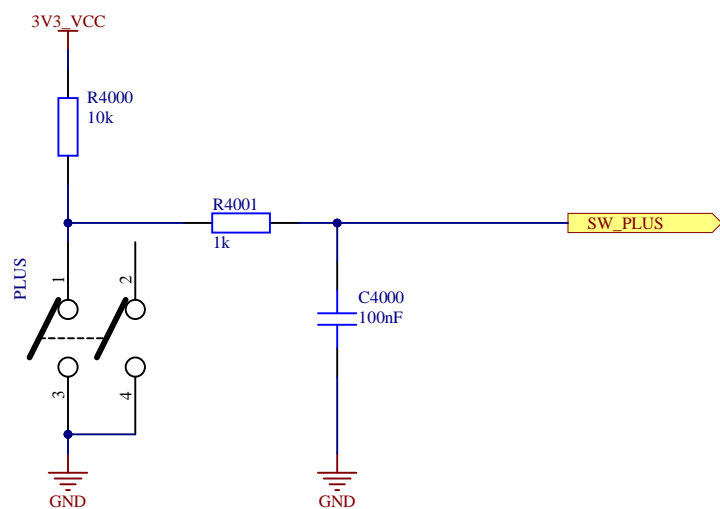
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LDR

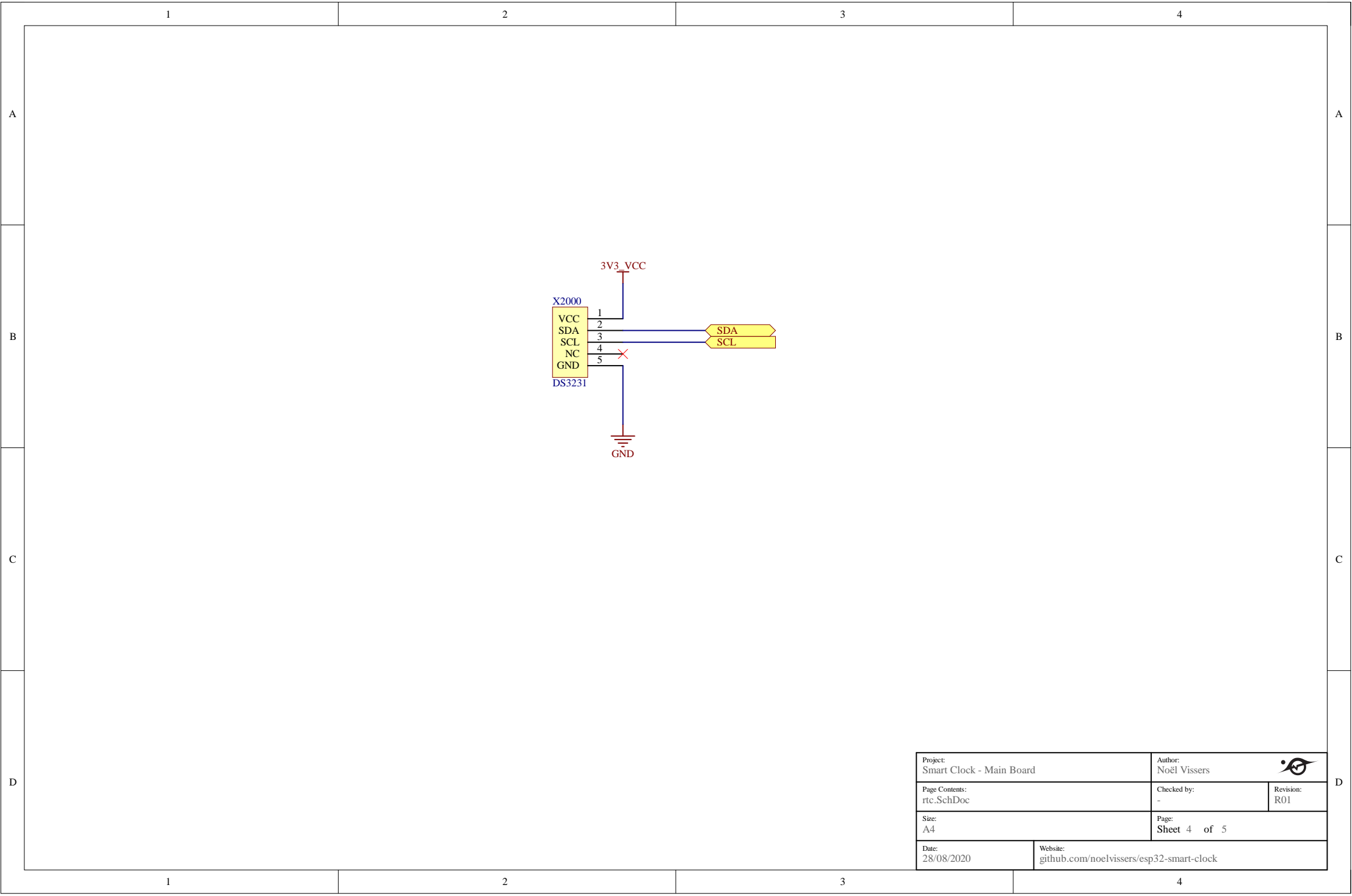
Project: Smart Clock - Main Board		Author: Noël Visser			
Page Contents: main-board.SchDoc		Checked by: -		Revision: R01	
Size: A4		Page: Sheet 1 of 5			
Date: 28/08/2020		Website: github.com/noelvisser/esp32-smart-clock			

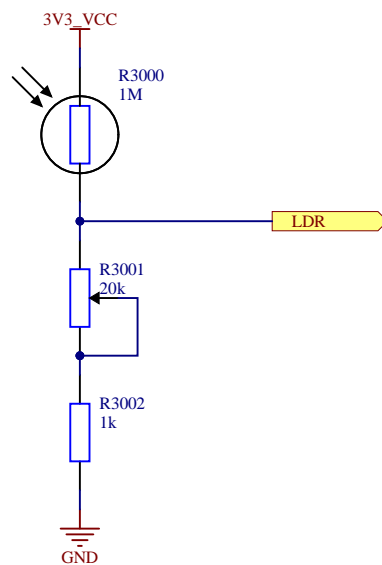



Project: Smart Clock - Main Board		Author: Noël Visser	
Page Contents: esp32.SchDoc		Checked by: -	Revision: R01
Size: A4		Page: Sheet 2 of 5	
Date: 28/08/2020		Website: github.com/noelvisser/esp32-smart-clock	

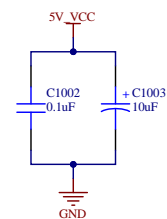
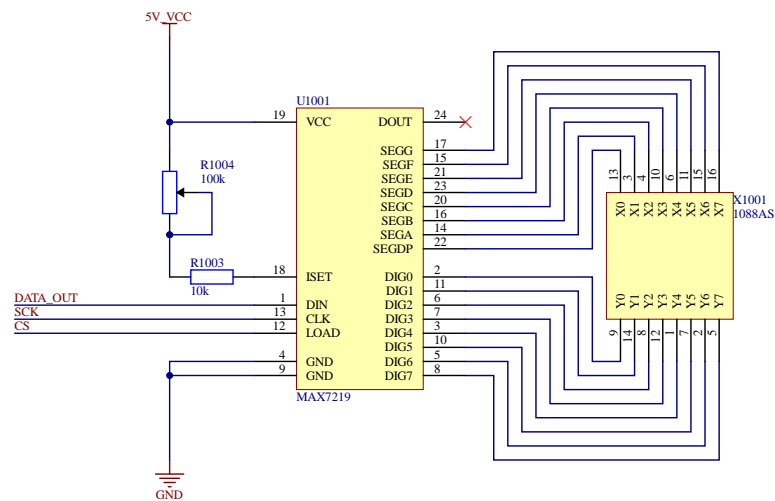
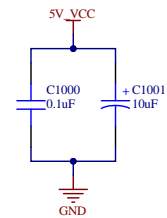
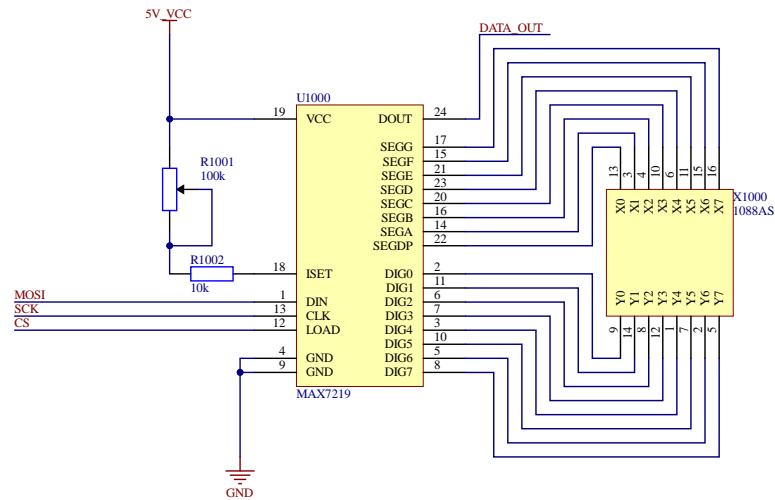
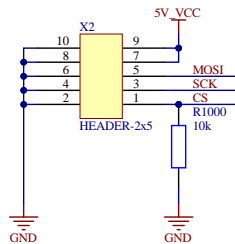
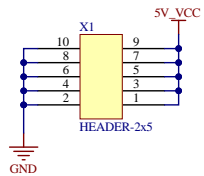


Project: Smart Clock - Main Board		Author: Noël Visser		
Page Contents: buttons.SchDoc		Checked by: -	Revision: R01	
Size: A4		Page: Sheet 3 of 5		
Date: 28/08/2020	Website: github.com/noelvisser/esp32-smart-clock			





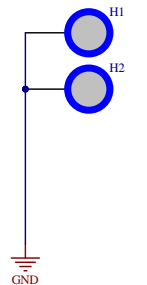
Project: Smart Clock - Main Board		Author: Noël Visser			
Page Contents: ldr.SchDoc		Checked by: -			Revision: R01
Size: A4		Page: Sheet 5 of 5			
Date: 28/08/2020		Website: github.com/noelvisser/esp32-smart-clock			




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Project: Smart Clock - Display	Author: Noël Visser	
Page Contents: display.SchDoc	Checked by: -	Revision: R01
Size: A3	Page: Sheet 1 of 1	
Date: 28/08/2020	Website: github.com/noelvisser/esp32-smart-clock	