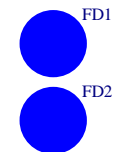
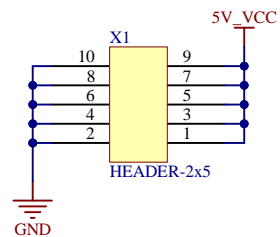
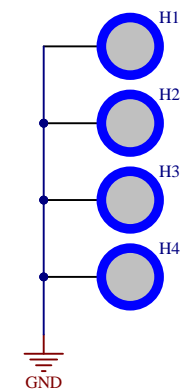


FIDUCIALS



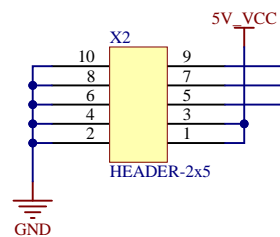
MOUNTING HOLES



ESP32
esp32.SchDoc

SW_SELECT
SW_PLUS
SW_MIN

Buttons
buttons.SchDoc
SW_SELECT
SW_PLUS
SW_MIN




MOSI
SCK
CS

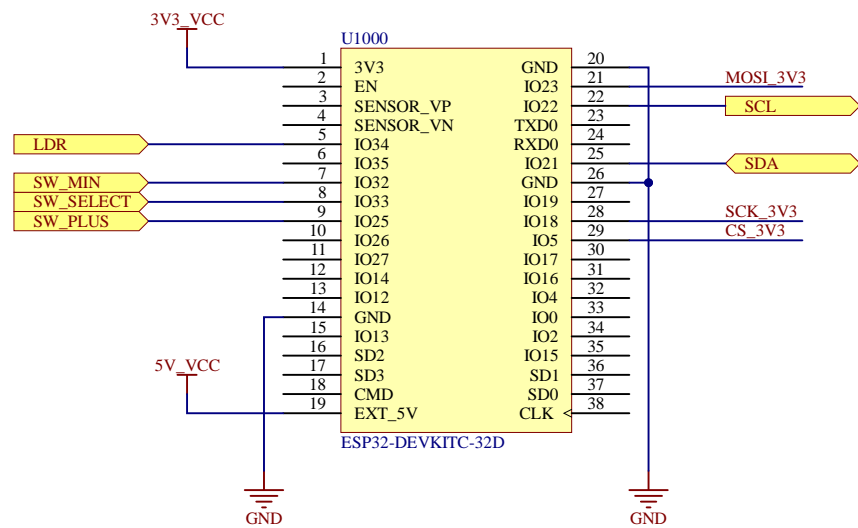
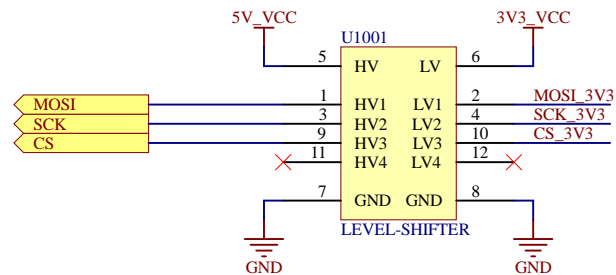
SDA
SCL

RTC
rtc.SchDoc
SDA
SCL

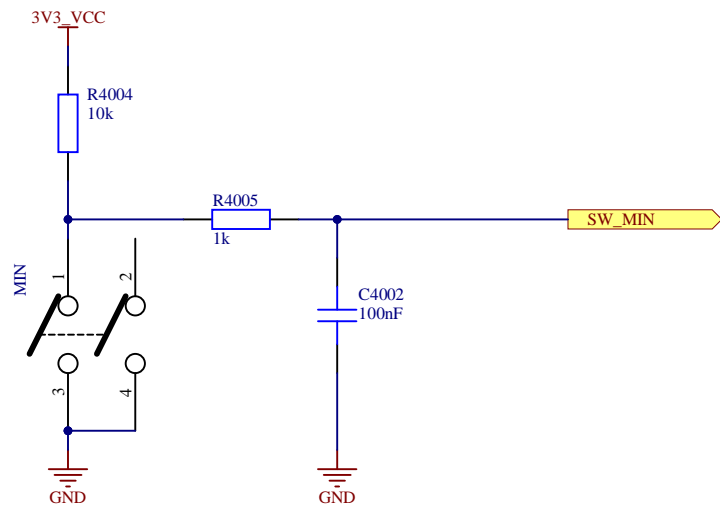
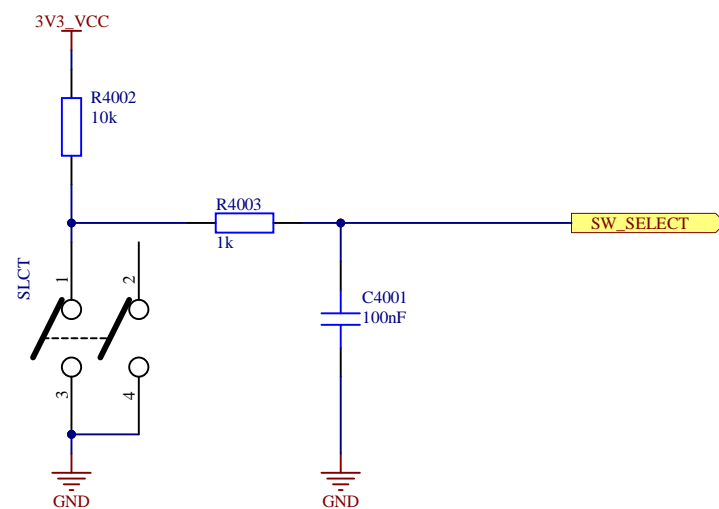
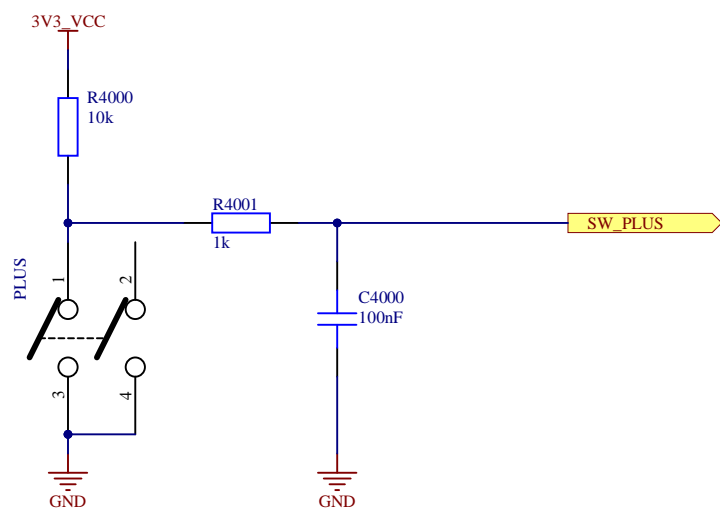
LDR


LDR
ldr.SchDoc
LDR

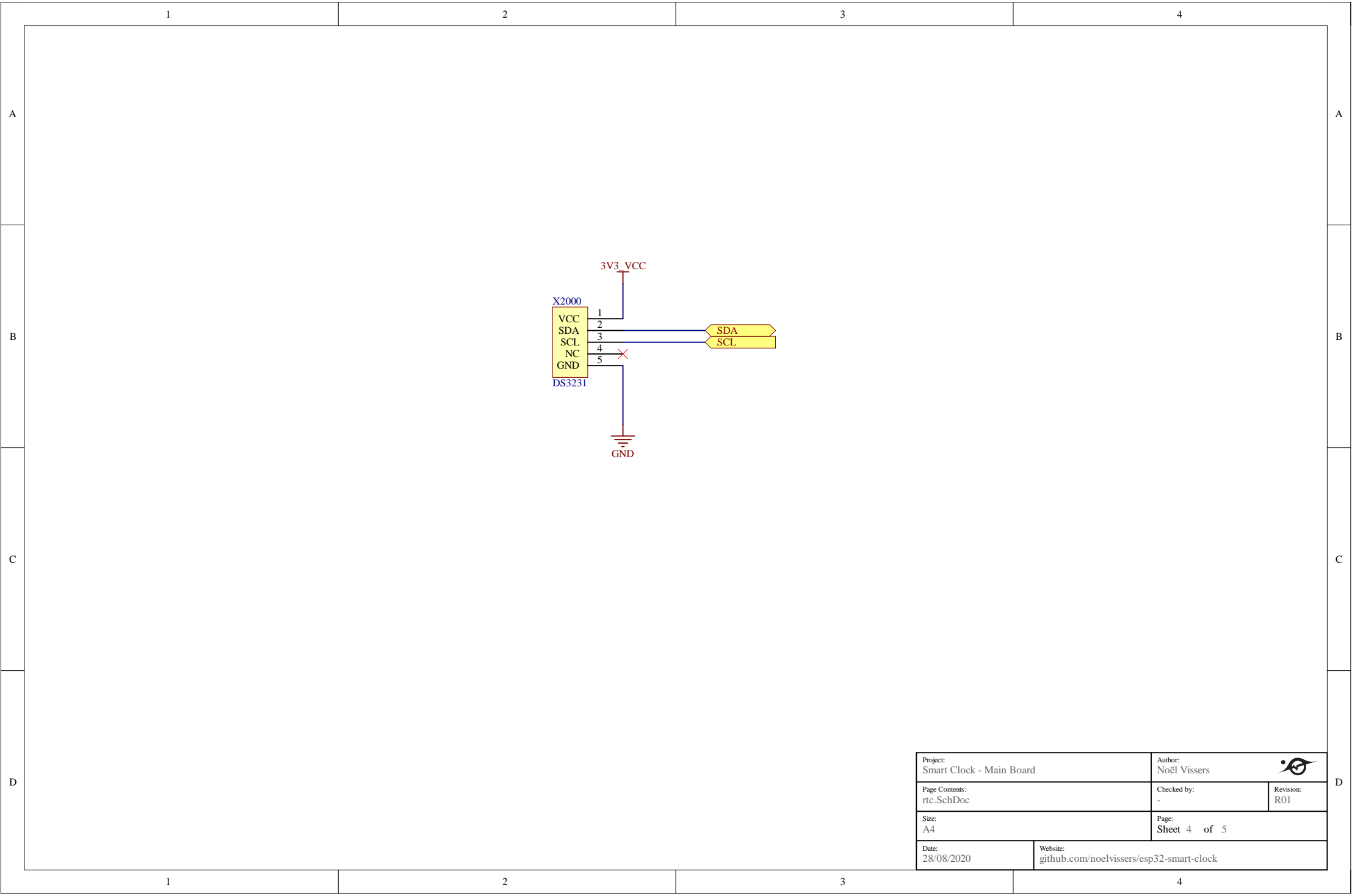
Project: Smart Clock - Main Board		Author: Noël Visser		
Page Contents: main-board.SchDoc		Checked by: -	Revision: R01	
Size: A4		Page: Sheet 1 of 5		
Date: 28/08/2020	Website: github.com/noelvisser/esp32-smart-clock			

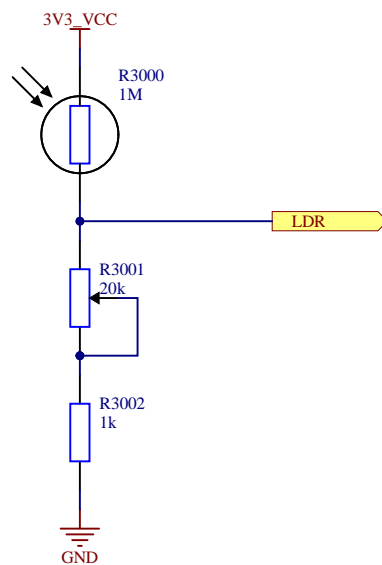



Project: Smart Clock - Main Board		Author: Noël Visser	
Page Contents: esp32.SchDoc		Checked by: -	Revision: R01
Size: A4		Page: Sheet 2 of 5	
Date: 28/08/2020		Website: github.com/noelvisser/esp32-smart-clock	



Project: Smart Clock - Main Board		Author: Noël Visser		
Page Contents: buttons.SchDoc		Checked by: -	Revision: R01	
Size: A4		Page: Sheet 3 of 5		
Date: 28/08/2020	Website: github.com/noelvisser/esp32-smart-clock			





Project: Smart Clock - Main Board		Author: Noël Visser			
Page Contents: ldr.SchDoc		Checked by: -			Revision: R01
Size: A4		Page: Sheet 5 of 5			
Date: 28/08/2020		Website: github.com/noelvisser/esp32-smart-clock			