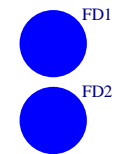
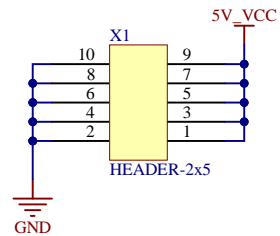
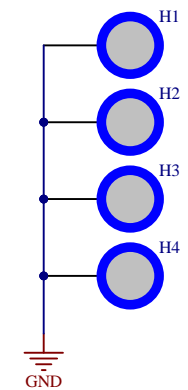


## FIDUCIALS



## MOUNTING HOLES

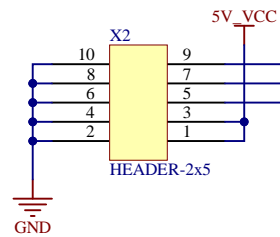


ESP32  
esp32.SchDoc

SW\_SELECT  
SW\_PLUS  
SW\_MIN

Buttons  
buttons.SchDoc

SW\_SELECT  
SW\_PLUS  
SW\_MIN



MOSI  
SCK  
CS

RTC  
rtc.SchDoc


SDA  
SCL

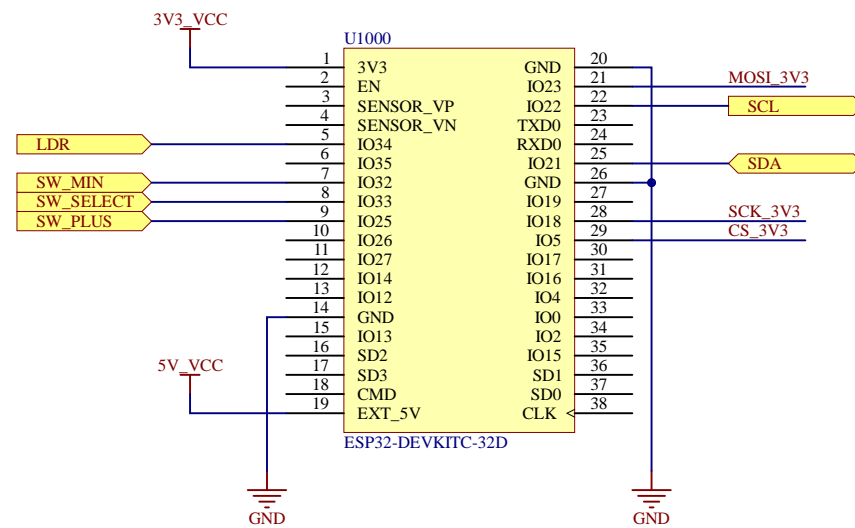
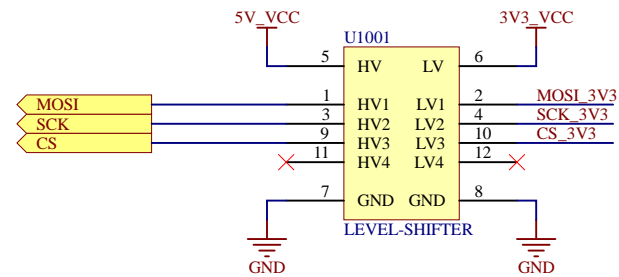
SDA  
SCL


LDR  
ldr.SchDoc

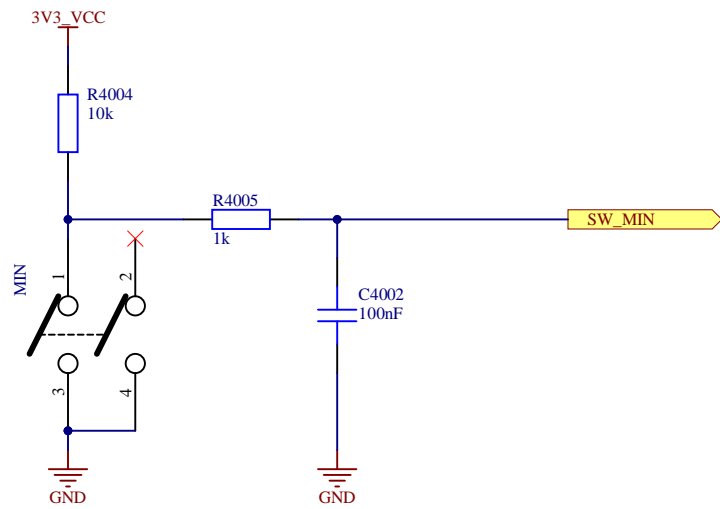
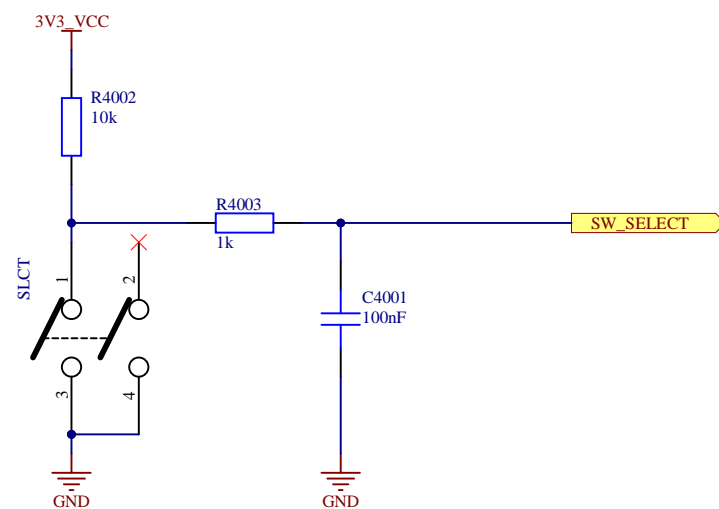
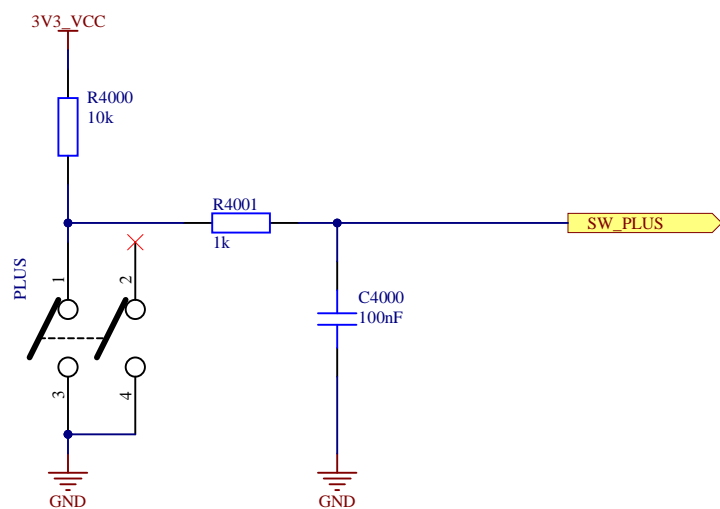
LDR


LDR

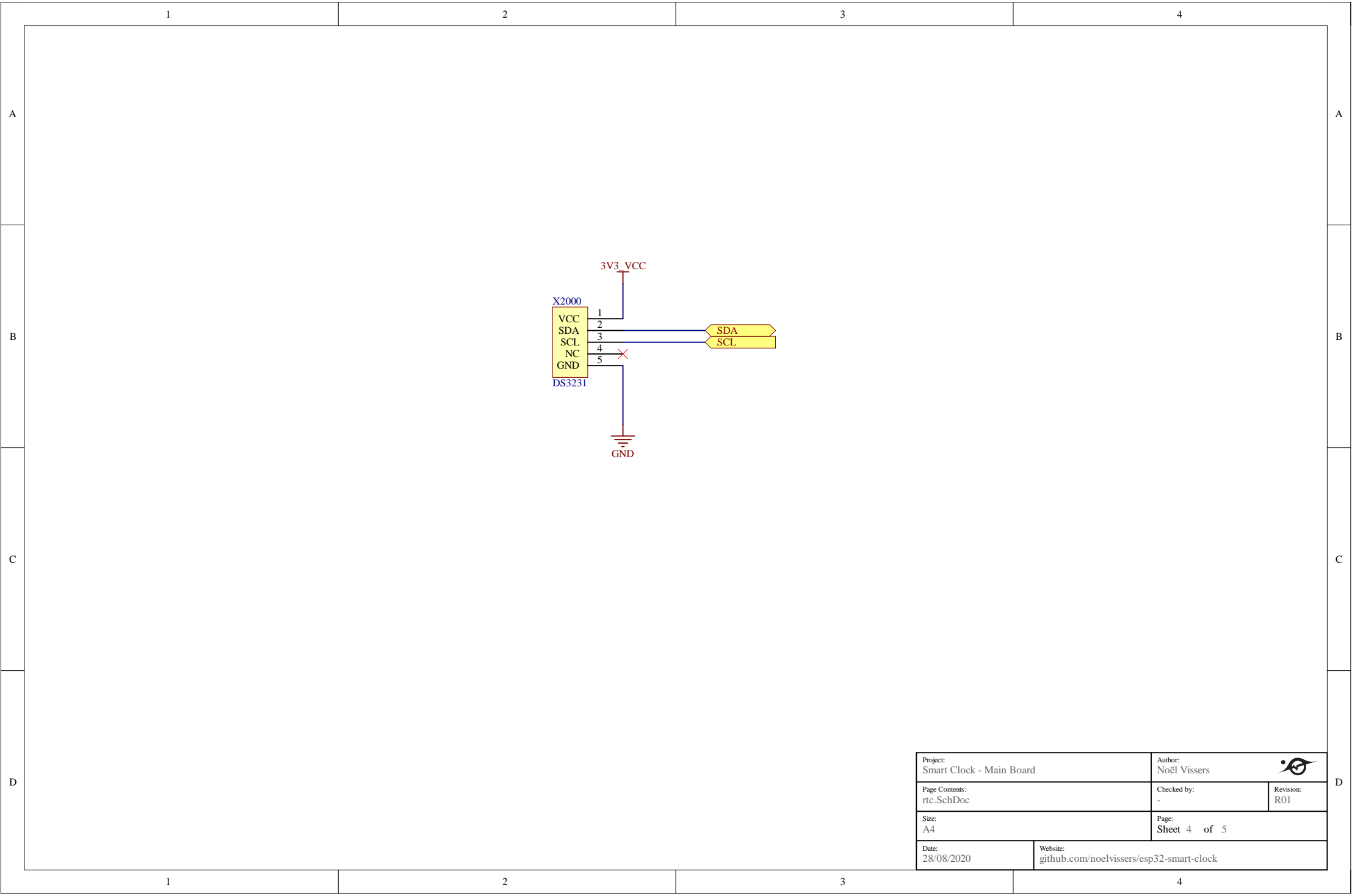
Project: Smart Clock - Main Board		Author: Noël Visser			
Page Contents: main-board.SchDoc		Checked by: -		Revision: R01	
Size: A4		Page: Sheet 1 of 5			
Date: 28/08/2020		Website: <a href="https://github.com/noelvisser/esp32-smart-clock">github.com/noelvisser/esp32-smart-clock</a>			




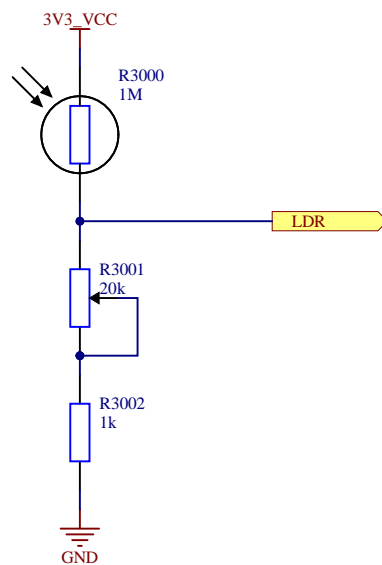
Project: Smart Clock - Main Board		Author: Noël Visser			
Page Contents: esp32.SchDoc		Checked by: -		Revision: R01	
Size: A4		Page: Sheet 2 of 5			
Date: 28/08/2020		Website: <a href="https://github.com/noelvisser/esp32-smart-clock">github.com/noelvisser/esp32-smart-clock</a>			




Project: Smart Clock - Main Board		Author: Noël Visser		
Page Contents: buttons.SchDoc		Checked by: -	Revision: R01	
Size: A4		Page: Sheet 3 of 5		
Date: 28/08/2020	Website: <a href="https://github.com/noelvisser/esp32-smart-clock">github.com/noelvisser/esp32-smart-clock</a>			



Project: Smart Clock - Main Board		Author: Noël Visser <span></span>	
Page Contents: rtc.SchDoc		Checked by: -	Revision: R01
Size: A4		Page: Sheet 4 of 5	
Date: 28/08/2020		Website: <a href="https://github.com/noelvisser/esp32-smart-clock">github.com/noelvisser/esp32-smart-clock</a>	



Project: Smart Clock - Main Board		Author: Noël Visser			
Page Contents: ldr.SchDoc		Checked by: -			Revision: R01
Size: A4		Page: Sheet 5 of 5			
Date: 28/08/2020		Website: <a href="https://github.com/noelvisser/esp32-smart-clock">github.com/noelvisser/esp32-smart-clock</a>			