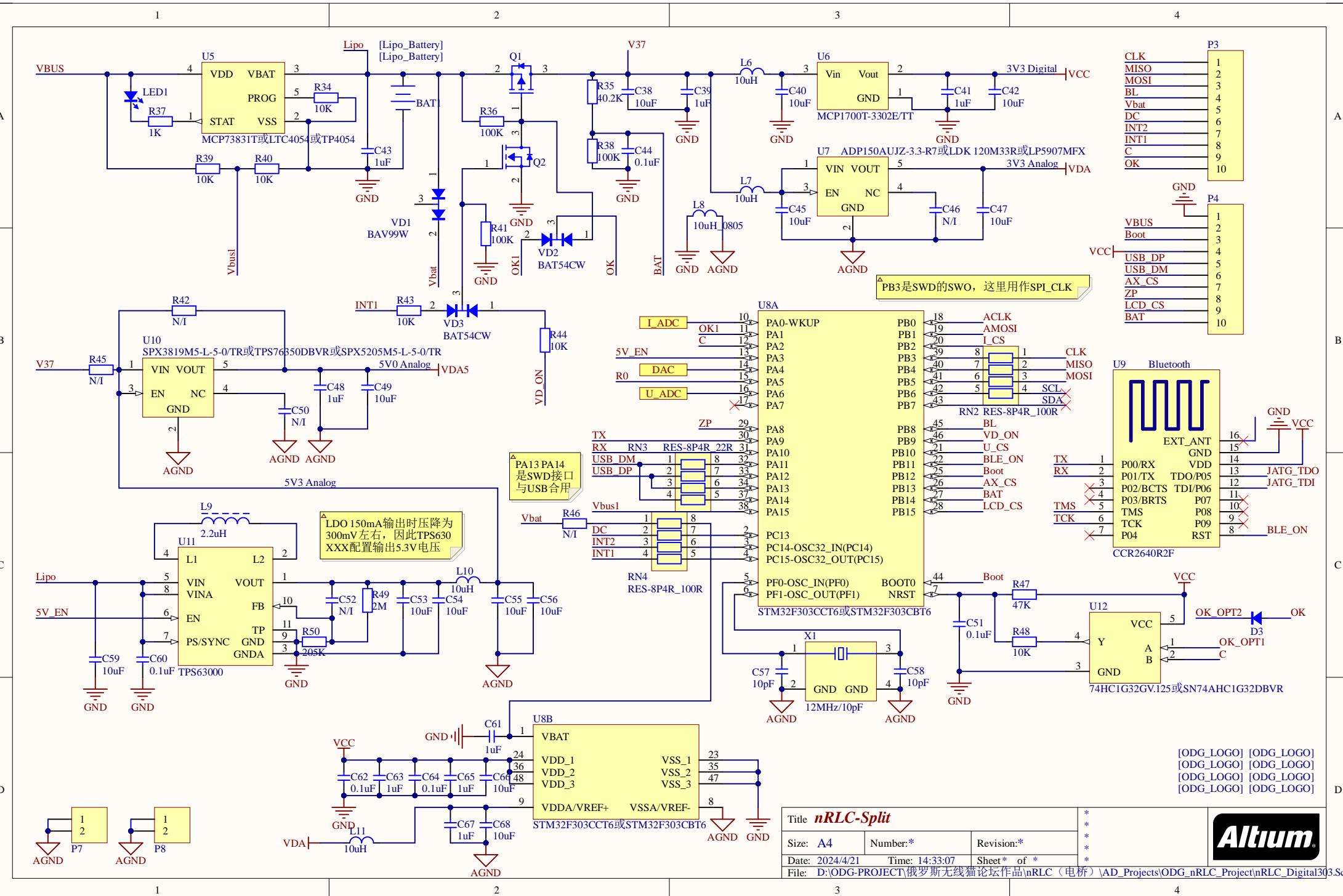


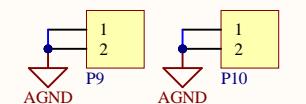
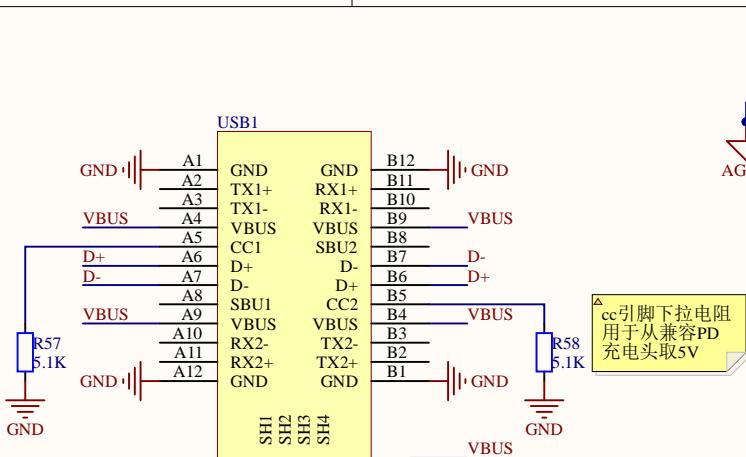
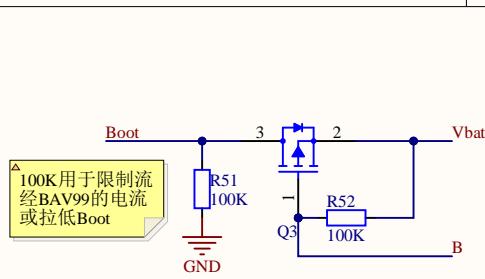
Title *nRLC-Split*

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Date: 2024/4/21	Time: 14:33:06	Sheet * of *	*
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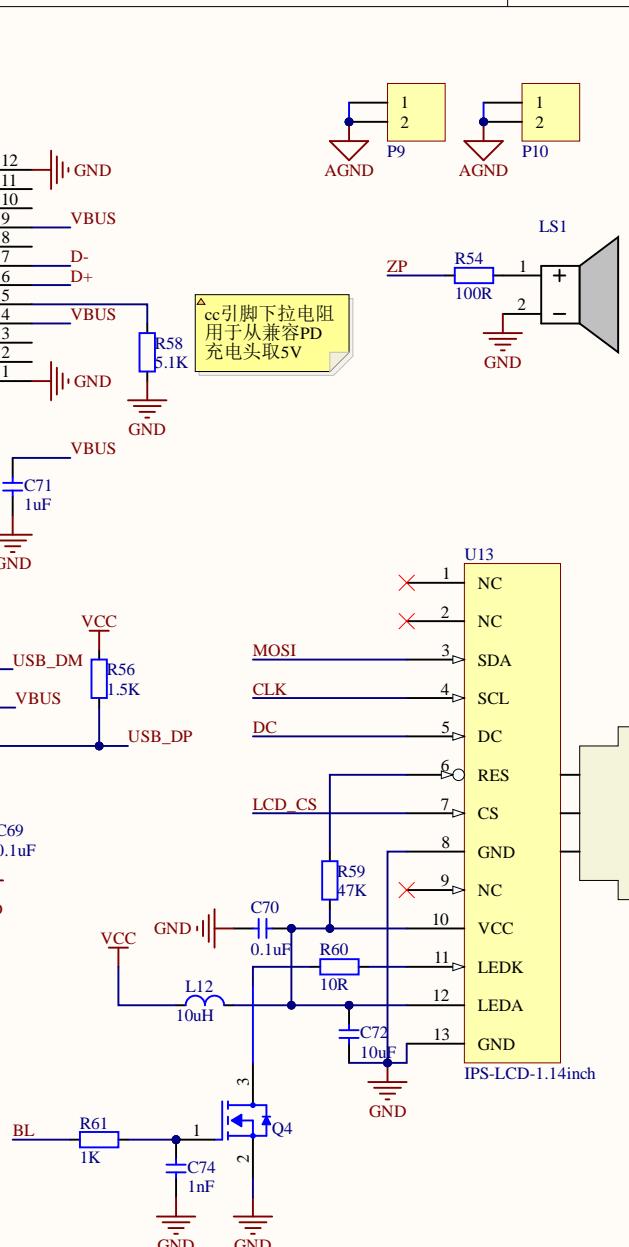
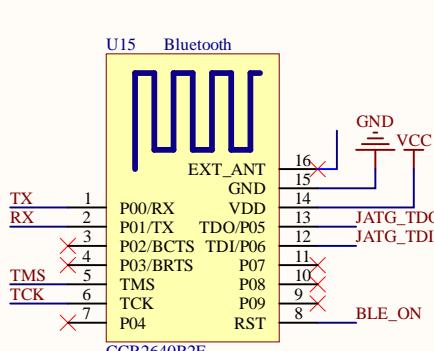
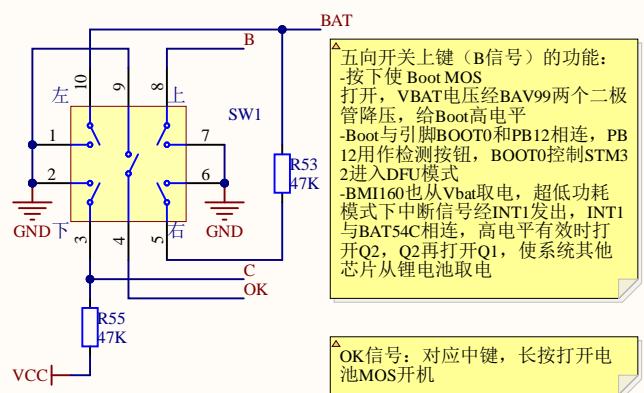




1 2 3 4



P5
CLK
MISO
MOSI
BL
Vbat
DC
INT2
INT1
C
OK
10



P6
VBUS
Boot
USB_DP
USB_DM
ZP
LCD_CS
BAT
10

△ 显示屏外围电路参考:
中景园电子1.14LCD显示屏技术资料中的1.14寸IPS LCD原理图.pdf
BL背光控制线, 默认可以不接, 低电平关闭背光

Title: nRLC-Split

Size: A4 Number: * Revision: *

Date: 2024/4/21 Time: 14:33:08 Sheet: * of *

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1 2 3 4