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# Xinyi Wang
# Advanced Operating Systems
# Programming Project #3: Logical and Vector Clocks
# Project 3_1: logical clocks
# Date Assigned: Thursday, March 28, 2013
# Due Date: Due Date: Thursday, April 11, 2013 by 9:30 a.m.
# Instructor: Dr. Ajay K. Katangur
```

input file:

```
3
exec 1
send 1 2 "silly message"
end
```

```
mpirun -np 4 aosproj3_1 input1.txt
```

