WE HAVE TO MAKE THIS
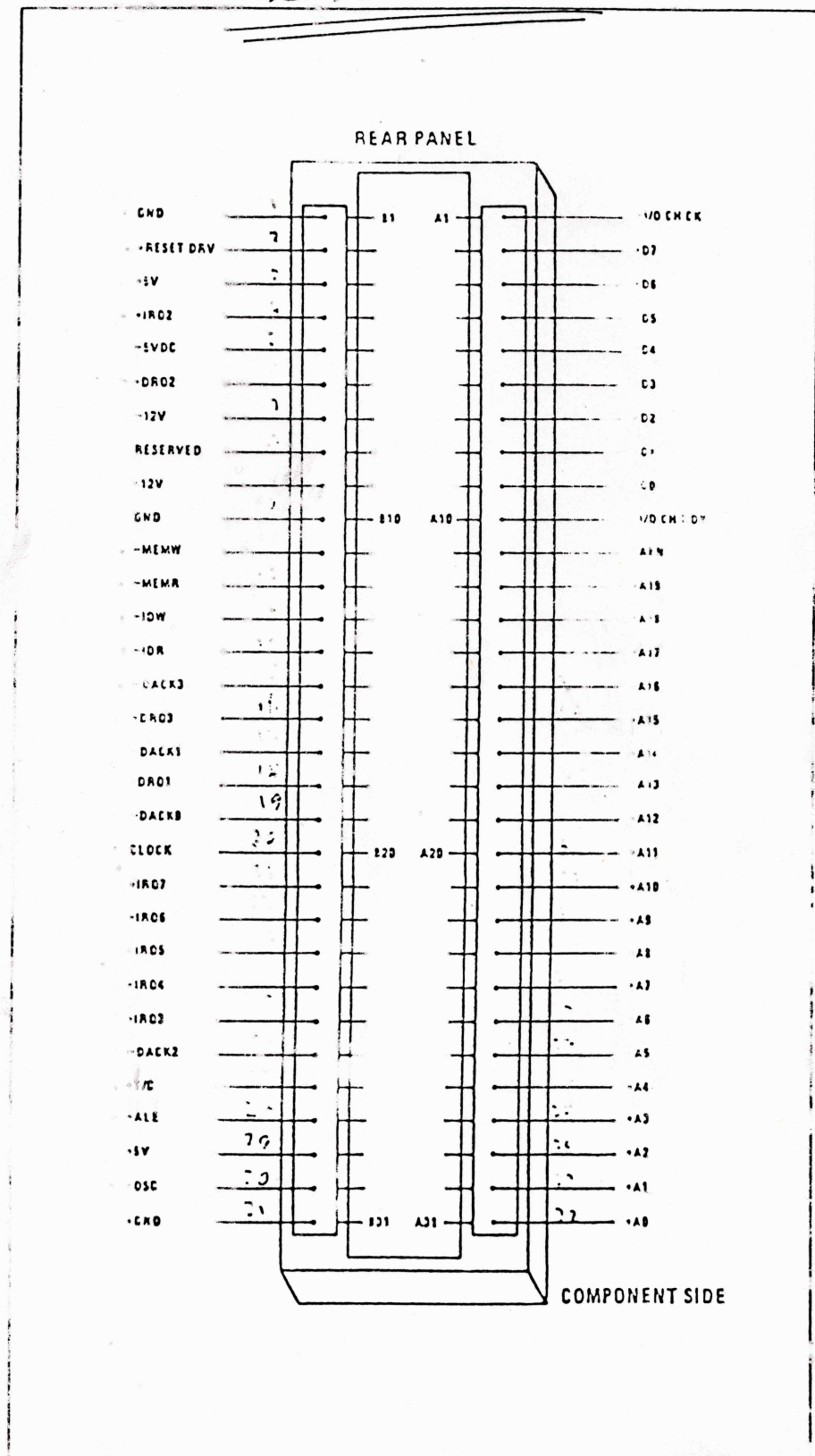
te behomen :
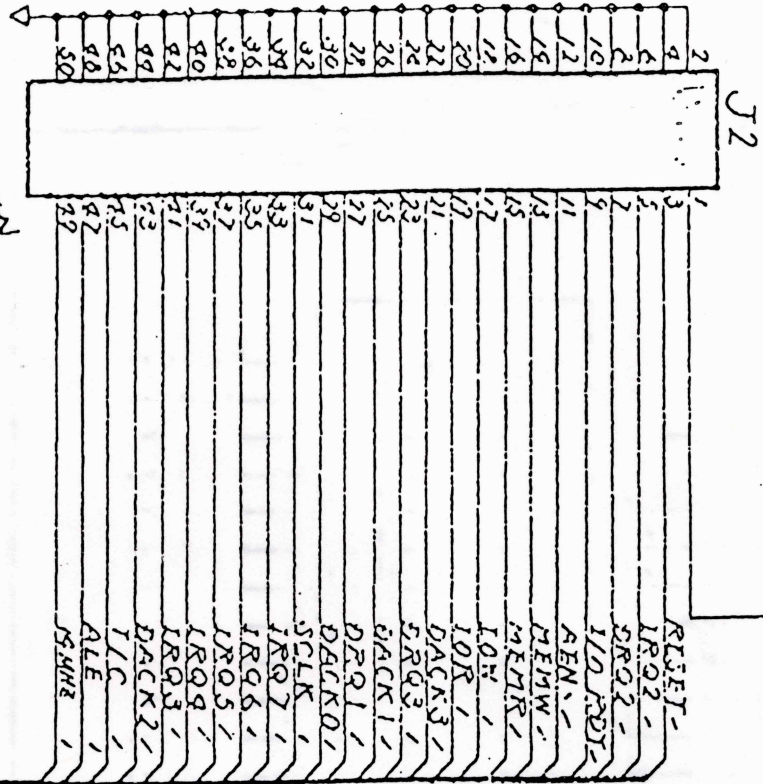

Figuur 9/2.1: De aansluitgegevens van een PC-slot

(1)

FR: ART MORGAN
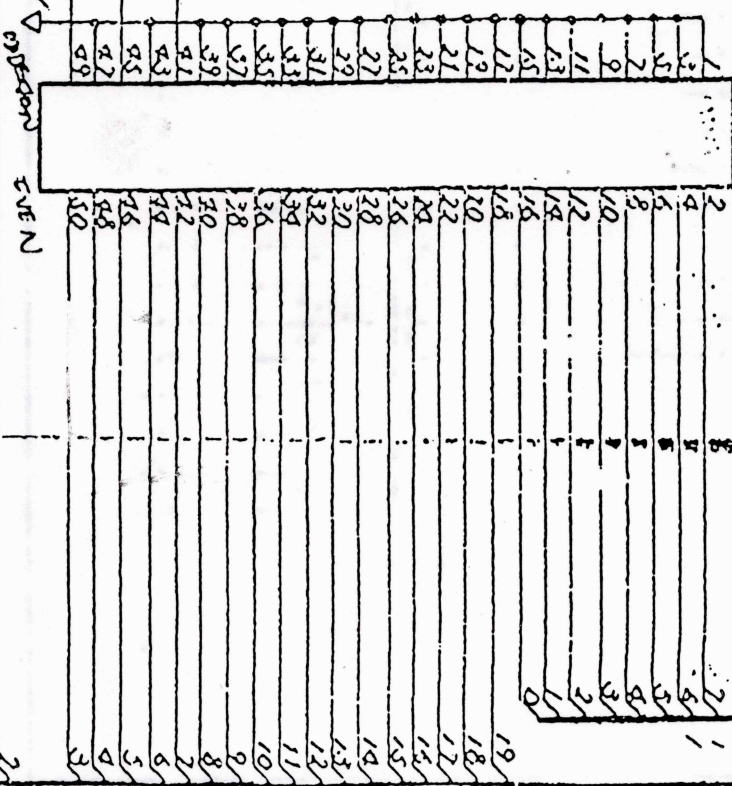**RE: PC EXPANSION**

HOW TO WIRE
ATARI PC
&
8BIT ISA

J2

RP2

RESET-
IRQ2-
DRQ2-
I/O RDY
MEMW-
MEMR-
IOW-
IOR-
DACK3-
DRQ3
IRQ9-
IRQ5-
IRQ6-
IRQ7-
SCLK-
DACKO1
DRQ1
DACK1-
BACK3-
IRQ3
BACK2-
T/C
ALE
OSC14M

U62
74LS145
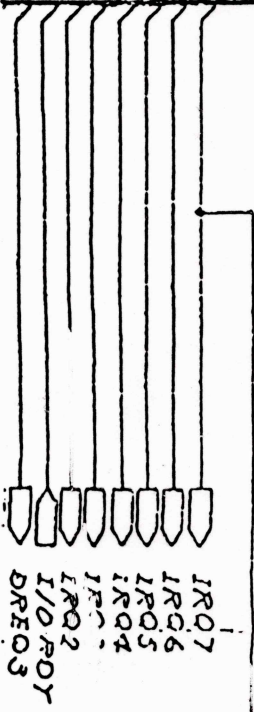
IRQ7
IRQ6
IRQ5
IRQ4
IRQ3
I/O RDY
DRQ2
DREQ3

(2)

ATARI PC Expansion Connectors

J2	PC	Name		J8	PC	Name
1		245DIR (N.C.)		2	A2	D7
3	B2	RESET		4	A3	D6
5	B4	IRQ2		6	A4	D5
7	B6	DRQ2		8	A5	D4
9	A10	I/O CH RDY		10	A6	D3
11	A11	AEN		12	A7	D2
13	B11	xMEMW		14	A8	D1
15	B12	xMEMR		16	A9	D0
17	B13	xIOW		18	A12	A19
19	B14	xIOR		20	A13	A18
21	B15	xDACK3		22	A14	A17
23	B16	DRQ3		24	A15	A16
25	B17	xDACK1		26	A16	A15
27	B18	DRQ1		28	A17	A14
29	B19	xDACK0		30	A18	A13
31	B20	CLK		32	A19	A12
33	B21	IRQ7		34	A20	A11
35	B22	IRQ6		36	A21	A10
37	B23	IRQ5		38	A22	A9
39	B24	IRQ4		40	A23	A8
41	B25	IRQ3		42	A24	A7
43	B26	xDACK2		44	A25	A6
45	B27	T/C		46	A26	A5
47	B28	ALE		48	A27	A4
49	B30	OSC (14.31818 MHz)		50	A28	A3
				41	A31	A0
				45	A30	A1
				49	A29	A2

all even pins GROUND

all other odd pins GROUND

All signals correspond to the expansion bus of the IBM PC. The xIO CH CK signal is not implemented in the ATARI PC. The connectors do not provide any of the power supply voltages. Also note that the CLK signal is the processor clock, and so in the ATARI PC may be either 4.77 MHz or 8.0 MHz depending upon the "turbo" mode bit.

Atari Corporation   25 August 1987   Engineering Specification

(3)