

Fahd Ouassarni

Embedded Software Developer.

Projects

OpenWRT On WiSoC

Telecom, OpenWRT, Linux, MIPS

June 2019 — Ongoing

- OpenWRT/LEDE building system hands-on
- iptables and simple rooting usage
- Adding OpenVPN and its corresponding uci configuration

SoC on FPGA / ENSICAEN

C, VHDL, Zynq, SPI, LCD

December 2018 — February 2019

- Baremetal programing on ARM
- VHDL synthesized SPI peripheral

Evaluation of TCI6638K2K / THALES AIR SYSTEMS SAS SystemTap, Linux, ARM October 2018 — February 2019

- Listing of linux profiling and debugging tools
- Profiling linux boot time with bootchart and bootgraph.pl
- Demonstration of SystemTap and other tools

Job Experience

Trainee Engineer / Hyptra Tailleville, France

April 2019 - September 2019

- Embedded software developpement for 8051.
- PCB design using Altium Designer.
- FPGA synthesis on PicoZed.

Special Research Student / Kumamoto University Kumamoto, Japan May 2018 - August 2018

- Laboratory: Human Interface and Cyber Communication Laboratory.
- Research: Multichannel speech segregation using FDBM on a SBC. [cfdbm]

Education

Electronic Engineering Master Degree / ENSICAEN Caen, France

September 2016 - March 2020

- Speciality: Electronics and applied Physics.
- Major: Signal, regulation for embedded systems and telecommunication.

Interests

Technology

Culture

Travel

Video Games

math Date of birth 17/09/1996

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▲ Assets

Curious

Motivated

Determined

Teamworker

Autonomous

Languages

English

Fluent - TOEIC 855

Arabic

Mother tongue

French

Fluent - C1

Technical Skills

Architectures

x86, ARM, PIC32, STM32, 8051.

Communication

Programming

UART, SPI, I2C, DSI, USB.

Assembly, C, C++.

Scripting

Shell, Python, m4.

Debugging

Scope & LA, gdb, strace, valgrind.

Embedded firmware

FreeRTOS, CubeMX.

Embedded linux

buildroot, Yocto, OpenWRT.

OrangePi Zero, TL-MR3020, QEMU.

Tools

Git, Make, SSH

Links

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