



# Fahd Ouassarni

Embedded software development,  
Embedded Linux.

📅 Date of birth 17/09/1996  
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## 🔑 Workflow

MPLABX for PIC projects. Vivado for FPGA projects.  
Eagle and LTSpice for PCB projects. VIM and GCC for Linux projects.  
Yocto or Buildroot for embedded linux projects.  
Basically a toolchain and a good text editor.

## 👛 Job Experience

**Trainee Engineer / Hyptra** TAILLEVILLE, France  
April 2019 — September 2019

- Embedded software development for 8051.
- PCB design using Altium Designer.
- FPGA synthesis on PicoZed.

**Technical Team Leader / SERI (ENSICAEN)** Caen, France  
September 2018

- A two weeks start-up immersion introduced by ENSICAEN.
- Mission: Wireless Communication on Embedded Linux.
- Product: Space Inspection Rover [Video]

**Special Research Student / Kumamoto University** Kumamoto, Japan  
May 2018 — August 2018

- Laboratory: Human Interface and Cyber Communication Laboratory.
- Research: Multichannel speech segregation using FDBM on a SBC. [cfdbm]

## 🎓 Education

**Electronic Engineering Master Degree / ENSICAEN** Caen, France  
September 2016 — March 2020

- Speciality: Electronics and applied Physics.
- Major: Signal, regulation for embedded and telecom.

**Higher School Preparatory Classes / Jean Moulin High School** Forbach, France  
September 2014 — May 2016

- They consist of two very intensive years with the main goal of training undergraduate students for enrollment in one of the grandes écoles.
- Speciality: Mathematics, Physics and Engineering Science.
- Results: Admission at ENSICAEN via Concours Commun Polytechnique.

**Baccalaureate / Salman Al Farissi High School** Salé, Morocco  
September 2013 — June 2014

- Speciality: Mathematics and Engineering Science.

## 🔧 Projects

**Xen on ARM**

XEN, LINUX, FREERTOS, ARM

March 2019

- Introduction to Xen and other different hypervisors in a quest to enhance both my Linux and ARM architecture skills.

**SoC on FPGA / ENSICAEN**

C, VHDL, ZYNQ, FPGA

December 2018 — February 2019

- Introduction to Zynq Processing System alongside VHDL synthesized peripherals.

**Evaluation of TCI6638K2K / THALES AIR SYSTEMS SAS**

LINUX, C, ARM, DSP

October 2018 — February 2019

- Introduction to Embedded Linux and Realtime DSP applications.

**cfdbm / Kumamoto University**

C, ALSA, ARM, SBC

June 2018 — August 2018

- Introduction to ALSA for a realtime speech separation application.

**Mobile Robot Race / ENSICAEN**

LTSPICE, EAGLE, PCB

October 2017 — April 2018

- A race of motorized machines guided on a line of electromagnetic field.

## 💡 Life Motto

「学びは生涯の宝」

## 📁 Assets

Curious

Motivated

Determined

Teamworker

Autonomous

## 💬 Languages

Arabic

Mother tongue

French

Fluent - C1

English

Fluent - TOEIC 855

Japanese

Beginner - N4

## ⚙️ Technical Skills

Architectures

x86, ARM, PIC, 8051.

Communication

UART, SPI, I2C, CAN, USB.

Programming

Assembly, C, C++, Java, Javascript.

Networking

iptables, tftp, nfs, hostapd.

Frameworks

FreeRTOS, Yocto, NodeJS.

Simulation

MATLAB, Simulink.

Scripting

Shell, Python, Perl, Lua.

Markup

HTML/CSS,  $\LaTeX$ , Markdown.

Tools

Git, SSH, Makefile, Wireshark.

HDL

VHDL.

## ⚽ Interests

Technology

Culture

Travel

Video Games

## 🌐 Links

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