

# Pedro Oliveira



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April 10<sup>th</sup>, 1989



Software Engineer

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## Education

University of Minho

Portugal

M.E. Electronics and Computers Engineering

2008 – 2013

- Major in Embedded Systems.
- Major in Power Electronics.

## Work Experience

Synopsys

Porto, Portugal

Embedded Software Engineer

2017 – Present

- Worked on trusted execution environments.
- HDCP protection for HDMI and DisplayPort interfaces.

CEiiA

Porto, Portugal

Technical Lead & Software Engineer

2014 – 2017

- Board bring up and system design of mobility devices for control and tracking.
- Responsible for training, leading, mentoring and recruiting within the team.

Coreworks

Lisbon, Portugal

FPGA / ASIC Design Engineer

Feb. 2014 – Aug. 2014

- Design and verification of real-time multi-core systems for audio codecs.
- Responsible for converting the SoC bus and IPs from AMBA AXI to Avalon.

## Skills

### Programming:

- C, C++, Python, Shell scripting
- HTML/CSS
- Java, JavaScript

### Tools:

- GNU Toolchains, Makefile, Autotools, vim, gdb, Valgrind.
- Git, SVN.

### Embedded Linux and RTOS:

- Buildroot, Yocto/OpenEmbedded
- U-Boot, BusyBox, Crosstool-NG
- MQX, FreeRTOS, Xenomai
- Device Driver Development, IPCs

### Other:

- iptables, ARP
- GoogleTest, CppUTest
- Cryptography
- Verilog, VHDL
- SQLite

### Language:

- **Portuguese:** Mother tongue.
- **English:** Fluent.
- **German:** Good.