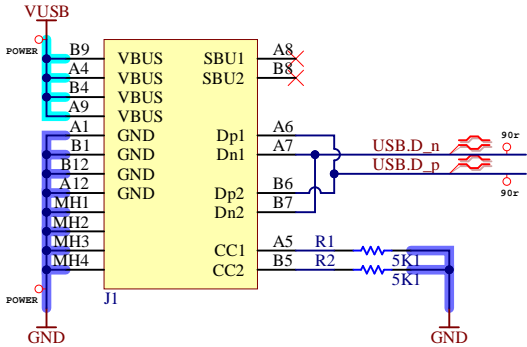


Rev. 00 : Initial release
Rev. 01 : Extra GND Pin

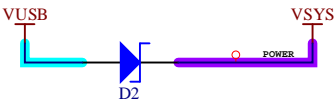
Lana TNY

CH32V203 Stamp

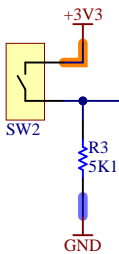
USB-C Interface



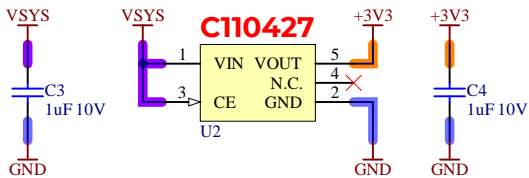
Power Protection



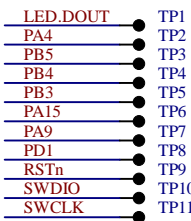
Boot0 internal PD, button to 3V3
External PD suggested in packages
where BOOT0 is combined with PB8



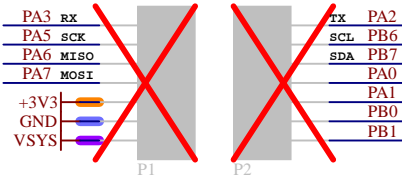
+3V3 Power Supply



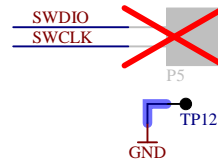
Extra SMD



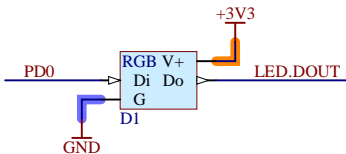
QT Py / XIAO Compatible Connector



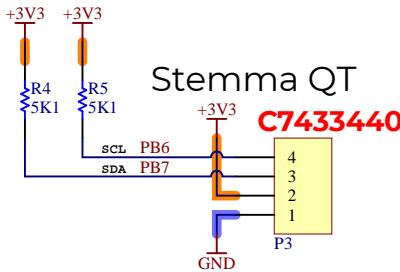
Program Connector



WS2812b LED



Stemma QT



Phyx

Title
Lana TNY


Date
05/11/2023

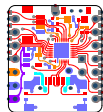
Drawn By
Wim Van Gool

Size
A3

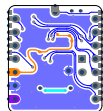
Sheet
1 of 1



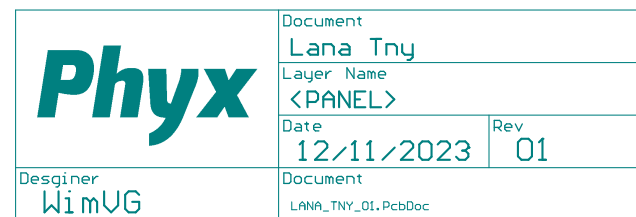
	Document	
	Lana Tny	
	Layer Name	
	<PANEL>	
Desginer	Date	Rev
	12/11/2023	01
WimUG		Document
		LANA_TNY_01.PcbDoc



<div>Phyx</div>	Document	
	Lana Tny	
	Layer Name	
	<PANEL>	
	Date	Rev
	12/11/2023	01
Desginer	Document	
WimUG	LANA_TNY_01.PcbDoc	

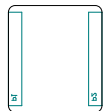


Phyx	Document	
	Lana Tny	
	Layer Name	
	<PANEL>	
	Date	Rev
	12/11/2023	01
Desginer	Document	
WimUG	LANA_TNY_01.PcbDoc	





<div>Phyx</div>	Document	
	Lana Tny	
	Layer Name	
	<PANEL>	
Desginer	Date	Rev
	12/11/2023	01
WimUG		Document
		LANA_TNY_01.PcbDoc



Phyx	Document	
	Lana Tny	
	Layer Name	
	<PANEL>	
	Date	Rev
	12/11/2023	01
Desginer	Document	
WimUG	LANA_TNY_01.PcbDoc	

