# **Emacs support for Make Files**

Emacs support for Make Files						
Description	Keystroke	Function	<u>Note</u>			
Make support	<ul> <li>Emacs natively supports several Make dialect modes as listed below.</li> <li>PEL adds several commands and user-options that add control to the editing behaviour. See:</li> <li>pel-modes-activating-superword-mode: PEL automatically activates super-word-mode for make files. Use <f11> t <f2> to access the customization group.</f2></f11></li> </ul>					
Open this PDF file. See also: <u>∑ Help/</u> Info	<f11> SPC M <f1><f12> <f1>&gt;</f1></f12></f1></f11>	(pel-help-pdf &optional OPEN-WEB-PAGE)	Open the <u>\mathbb{N}I - Make</u> local PDF. If the prefix argument (like <b>C-u</b> or <b>M</b> ) is used, then it opens the remote GitHub hosted raw PDF instead. If the <b>pel-flip-help-pdf-arg</b> user-option is set it's the other way around.			
<u> al - Make</u>	<f11> SPC M <f3> <f12> <f3></f3></f12></f3></f11>	(pel-customize-library & optional OTHER-WINDOW)	Customize Emacs makefile support: makefile.  • If OTHER-WINDOW is non-nil (use <b>C-u</b> ), display in another window.			
Select Make dialect mode  See also:  Customize	Emacs supports several dialects of <a href="make">make</a> . It automatically selects the dialect when a file is visited using the mode and file specification association identified in the <a href="make-mode">automate-mode</a> . The following make file dialect modes are supported:  • makefile-mode (the based mode upon which all following modes are derived):  • makefile-automake-mode : .am  • makefile-bsdmake-mode : [Mm]akefile, .mk, .make  • makefile-gmake-mode : GNUmakefile  • makefile-imake-mode : Imakefile					
•   File/Directory  Variables	With PEL, set up the asso     You can access the reli     Its also possible to use file v	:.mak PEL implements the makefile-n extension for their makefile (the <u>dmd project</u> fo ciation using the <b>pel-auto-mode-alist</b> user-op	tion.  by using PEL <b><f11> <f2> p</f2></f11></b> key sequence. See <u>Customize</u> ode: write something like this on the first line: -*- mode: makefile-gmake; -*-			
Activate automake mode	• C-c RET C-a • C-c C-m C-a	(makefile-automake-mode)	Activates the <u>automake</u> mode  • The mode-line lighter is : Makefile.am			
Activate BSD make mode	• C-c RET C-b • C-c C-m C-b	(makefile-bsdmake-mode)	Activates the <u>BSD make</u> mode.  • BSD Make is the default make on macOS and BSD OS systems.  • The mode-line lighter is: BSDmakefile			
Activate <u>GNU</u> <u>make</u> mode	• C-c RET C-g • C-c C-m C-g	(makefile-gmake-mode)	Activates the <b>GNU make</b> mode.  • The mode-line lighter is: GNUmakefile  A Because this key sequence ends with <b>C-g</b> , type the <b>Esc</b> key 3 times to escape from the C-c C-m prefix. You can also use a key not in the list.			
Activate <u>imake</u> mode	• C-c RET <tab> • C-c C-m C-i</tab>	(makefile-imake-mode)	Activate the imake mode The mode-line lighter is: Imakefile			
Activate standard make mode	• C-c RET RET • C-c C-m C-m	(makefile-mode)	Activates the major mode for editing standard Makefiles.  • The mode-line lighter is : Makefile			
Activate <u>makepp</u> mode	• C-c RET C-p • C-c C-m C-p	(makefile-makepp-mode)	Activates the <u>makepp</u> mode. Also called <u>make++</u> • makepp is written in Perl. It is mostly useful for writing C++ specific make files, as it expands GNU Make and removes the requirement of using recursive make.  • The mode-line lighter is: Makeppfile			
Activate <u>NMAKE</u> mode	• C-c RET C-n • C-c C-m C-n	(makefile-nmake-mode)	Activates the nmake mode, supporting Microsoft's NMAKE makefile syntax.  • The mode-line lighter is: Nmake			
Navigate	The standard Emacs make-mo navigate across the macro defi		rigate across make target/dependency statements. PEL complements this with commands to			
Move point forward to next target/ dependency	• M-n • <f12> <down> • <m-f12> <down> <f11> SPC M <down></down></f11></down></m-f12></down></f12>	(makefile-next-dependency)	Move point to the beginning of the next dependency line.  • Skips comments and macro definitions.			
Move point backward to previous target/ dependency	• M-p • <f12> <up> • <m-f12> <up> <f11> SPC M <up></up></f11></up></m-f12></up></f12>	(makefile-previous-dependency)	Move point to the beginning of the previous dependency line.  • Skips comments and macro definitions.			
Move point forward to next macro definition statement	• <f12> <m-down> • <m-f12> <m-down> <f11> SPC M <m-down></m-down></f11></m-down></m-f12></m-down></f12>	(pel-make-next-macro &optional N SILENT DONT-PUSH-MARK	Move to the beginning of next N make file macro definition statement.  The function skips over comments.  In o valid form is found, don't move point, issue an error describing the failure unless SILENT is non-nil, in which case the function returns nil on error and non-nil on success.  The error message states the number of instanced searched, the regexp used and the number of instances found.  On success, the function push original position on the mark ring unless DONT-PUSH-MARK is non-nil.			
Move point backward to previous macro definition statement	• <f12> <m-up> • <m-f12> <m-up> <f11> SPC M <m-up></m-up></f11></m-up></m-f12></m-up></f12>	(pel-make-previous-macro &optional N SILENT DONT-PUSH-MARK)	Move to the beginning of previous N make file macro definition statement.  The function skips over comments.  If no valid form is found, don't move point, issue an error describing the failure unless SILENT is non-nil, in which case the function returns nil on error and non-nil on success.  The error message states the number of instanced searched, the regexp used and the number of instances found.  On success, the function push original position on the mark ring unless DONT-PUSH-MARK is non-nil.  The command support shift-marking.			
Insert & Edit	The following commands help	the editing of the makefile contents.				
Insert <u>GNU make</u> <u>function statement</u>	• C-c Tab • C-c C-i	(makefile-insert-gmake-function)	Insert a GNU make function call.  • Asks for the name of the function to use (with completion).  • Then prompts for all required parameters.			
Insert target at point	C-c :	(makefile-insert-target-ref TARGET-NAME)	Complete on a list of known targets, then insert TARGET-NAME at point.			
Add/remove line continuation trailing backslashes	C-c C-\	(makefile-backslash-region FROM TO DELETE-FLAG)	Insert, align, or delete end-of-line backslashes on the lines in the region.  • With no argument, inserts backslashes and aligns existing backslashes.  • With an argument, deletes the backslashes.  This function does not modify the last line of the region if the region ends right at the start of the following line; it does not modify blank lines at the start of the region. So you can put the region around an entire macro definition and conveniently use this command.			
Perform completion at point	C-M-i <f12> . <f6> .</f6></f12>	(completion-at-point)	Perform completion on the text around point. The completion method is determined by 'completion-at-point-functions'.  The C-M-i key sequence is also often bound to flyspell command.  Use <f12> . instead.</f12>			
Electric Insert	When the makefile-mode make	efile-electric-keys user-option is turned on (it is	off by default), the characters \$ : = and . have special behaviour, described below.			
Insert macro reference	\$	(makefile-insert-macro-ref MACRO-NAME)	Complete on a list of known macros, then insert complete ref at point.			

Description	Keystroke	Function	<u>Note</u>		
Insert new target	:	(makefile-electric-colon ARG)	Prompt for name of new target.  Prompting only happens at beginning of line.  Anywhere else just self-inserts.		
Insert macro defintion	=	(makefile-electric-equal ARG)	Prompt for name of a macro to insert.  Only does prompting if point is at beginning of line.  Anywhere else just self-inserts.		
Insert special target		(makefile-electric-dot ARG)	Prompt for the name of a special target to insert. Supports tab completion.  Only does electric insertion at beginning of line.  Anywhere else just self-inserts.		
Indenting	A CONTRACTOR OF THE CONTRACTOR		uish the tab character from multiple space characters. rk well in makefile. Use the other 3 commands.		
Insert a tab character	<tab></tab>	(indent-for-tab-command &optional ARG)	Inserts a tab character in a makefile.		
Indent line(s) rigidly	• <f6> <tab> • <f11> <tab> c</tab></f11></tab></f6>	(pel-indent-lines & optional $\underline{\mathbb{N}}$ )	Indent current or marked lines by N indentation levels. Each level uses a tab character.  • Works with point anywhere on the line.  • All lines touched by the region are indented.  • A special argument N can specify more than one indentation level. It defaults to 1.  • If a negative number is specified, 'pel-unindent-lines' is used.  • If a region is marked, the function does not deactivate it to allow repeated execution of the command. It also modifies the region to include all characters in all affected lines.  • Use C-g to de-activate the region.		
Un-indent line(s) rigidly	• <backtab> • <f6> <backtab> • <f11> <tab> C</tab></f11></backtab></f6></backtab>	(pel-unindent-lines &optional N)	<ul> <li>Un-indent current line or marked lines by N indentation levels.</li> <li>Works with point is anywhere on the line.</li> <li>All lines touched by the region are un-indented.</li> <li>If region was marked, the function does not deactivate it to allow repeated execution of the command.</li> <li>If a region was marked, the function does not deactivate it to allow repeated execution of the command. It also modifies the region to include all characters in all affected lines</li> <li>Use C-g to de-activate the region.</li> </ul>		
Indent expression	С-М-q	(prog-indent-sexp &optional DEFUN)	Indent the expression after point.  • When interactively called with prefix, indent the enclosing defun instead.  A This command does not work well in makefiles.		
Comment control	Although the make file modes provide the comment-region command, it's best to use comment-dwim as it works much better.				
Comment/un- comment  See also:∑ Comments	M-;	(comment-dwim ARG)	Comment or un-comment line or region.  When no marked region and no comment:  On empty line: insert comment starter at the proper indentation level. Typed again: move it toward end of line.  On line with code: insert comment starter after the code for an end-of-line comment.  With marked un-commented region:  Comment region (each line is commented)  With marked commented region:  removes the comment.  Call the comment command you want (Do What I Mean).  If the region is active and 'transient-mark-mode' is on, call 'comment-region' (unless it only consists of comments, in which case it calls 'uncomment-region'). Else, if the current line is empty, call 'comment-insert-comment-function' if it is defined, otherwise insert a comment and indent it. Else if a prefix ARG is specified, call 'comment-kill'.		
	С-с С-с	(comment-region BEG END &optional ARG)	Comment or uncomment each line in the region.  With just C-u prefix arg, uncomment each line in region BEG END.  Numeric prefix ARG means use ARG comment characters.  If ARG is negative, delete that many comment characters instead.  The strings used as comment starts are built from 'comment-start' and 'comment-padding'; the strings used as comment ends are built from 'comment-end' and 'comment-padding'.  By default, the 'comment-start' markers are inserted at the current indentation of the region, and comments are terminated on each line (even for syntaxes in which newline does not end the comment and blank lines do not get comments). This can be changed with 'comment-style'.		
Analyze	The following commands analyze the content of the make file or the file system.				
Scan current directory files, checking for targets	C-c C-f	(makefile-pickup-filenames-as-targets)	Scan the current directory for filenames to use as targets.  • Checks each filename against 'makefile-ignored-files-in-pickup-regex' and adds all qualifying names to the list of known targets.		
Scan current buffer for makefile content	С-с С-р	(makefile-pickup-everything ARG)	Notice names of all macros and targets in Makefile.  • Prefix arg means force pickups to be redone.  Use this to refresh the list of macros and targets located in the makefile before executing another action on those.		
Update scan with latest makefile buffer content	C-c C-u	(makefile-create-up-to-date-overview)	Create a buffer containing an overview of the state of all known targets.  Known targets are targets that are explicitly defined in that makefile; in other words, all targets that appear on the left hand side of a dependency in the makefile.		
List macros and targets in dedicated buffer	С-с С-ь	(makefile-switch-to-browser)	Open a *Macros and Target* buffer that only lists them.  It operates in Fundamental mode and aside listing the macros and targets provides nothing more.		

#### Emacs & Makefile - References

Document	Notes
Make tools	
GNU Make Manuals	GNU Make manual. See:  • GNU Make Top page  • GNU Make - Appendix A - Quick Reference
Makepp home page	Makepp, also called make++ is a GNU Make replacement, written in Perl. It addresses the recursive make problem.
Make generic information	
Recursive Make Considered Harmful - Steve Miller	PDF paper (from the wayback machine archive) written by Steve Miller in 1997 describing the concept of recursive make technique showing why it causes several problems and what can be done to avoid them.

### **GNU Make Rules**

		GNO Wake	Tuics				
	ı	GNU Make Rules					
Торіс	Rule syntax format	Description					
Rule Syntax			Multiple line recipe, the on mostly used.     The recipe lines must start with a <b>TAB</b> character (or the string identified by the .RECIPEPREFIX pseudo-variable.				
	targets : prerequisite recipe	s; recipe	from them by a	ble to to identify a r a semicolon. vriting a single-line	•	e line as the prereq	luisites, separated
Wildcards	Wildcards can be us	*	All files, like "*.c"				
		d in target and prerequisites anded in variable definitions: examples	?	Expand to characters			
	But <u>wildcard fu</u>	unctions can be use to expand in variable definition as	[]				
	III. Objects :	== \$(wildcard *.o)	~	At beginning of pa	ath name, like ~/b	in expands to your	home bin director
			~user	Expands the the home directory of specific user			
Searching directories	The value of the VPATH make variable specifies a list of directories that make should search.  • Each directory in the list can be separated by space or :  • On MS-DOS, Windows: space or ;						
Selective search	vpath directive	Same as VPATH but more selective: only applies to a particular class of file names. The path statement format is one of the 3 forms. The last 2 clear search path for the specified scope (file patter or all):  • vpath pattern directories  • vpath pattern  • vpath  • vpath			file name pattern,		
Link Libraries	Note: that make treats prerequisites of the form -lname as library names. The -lname is expanded to the full path of the library name with starts with the 'lib' prefix.  For example:  foo: foo.c -lcurses cc \$^-o \$@  will cause the following command to be executed if needed: cc foo.c /usr/lib/libcurses.a -o foo  This behaviour is customizable by the .LIBPATTERNS special variable.						
Phony Targets See also: • Rules without Recipes or Prerequisites • Empty target files to record events	<ul> <li>A phone target is a target that is not really the name of a file, it's just a name for a recipe to be executed when you make an explicit request.</li> <li>Use it to avoid a conflict with the name of a file, and to improve performance: implicit rule search is skipped for .PHONY targets.</li> <li>Example:</li></ul>						
Special Built-in Targets		S .DEFAULT .PRECIOUS .INTERMEDIATE .SECO N_TIME .SILENT .EXPORT_ALL_VARIABLES .NOT			ELETE_ON_ERRO	OR .IGNORE	
		GNU Make Implicit Rules					
Implicit Rule Topic	Description						
Using Implicit Rules	<ul> <li>To use therm refrain from writing the recipe for a kind of target.</li> <li>Each implicit rule has a target and prerequisite patterns.</li> <li>Write a rule to identify extra prerequisites like header files prerequisites to an object file.</li> <li>There may be several implicit rules for the same target (for example a rule to generate object file from C files, another rule to generate object file from C++ files).</li> <li>See the <u>catalogue of built-in-rules</u>. It is possible to <u>cancel an implicit rule</u>.</li> <li>Make searches for implicit rules for: <ul> <li>each target that has no recipe,</li> <li>each double-colon rule that has no recipe,</li> <li>a file that is only mentioned as a prerequisite.</li> </ul> </li> <li>The <u>Implicit Rule Search Algorithm</u> describes how the search for an implicit rule is done.</li> <li>A <u>chain of implicit rules</u> can be used to make the target from a prerequisite. But only one instance of an implicit rule can only be used in the chain.</li> <li>It's possible to define <u>last-resort default rules</u> to <u>override part of another makefile</u>.</li> </ul>						
		licit rule to apply to a specific target create an empty re	oipe ioi illat talge	, L.			

Variables u	sed in	<u>Implicit</u>	Rules
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Variable Name	Description	Default value	Flag Variable	Description and default value (if any)	
AR	Archive-maintaining program	ar	ARFLAGS	Flags to give the archive-maintaining program; default 'rv'	
AS	Program for compiling assembly files	as	ASFLAGS	Extra flags to give to the assembler (when explicitly invoked on a '.s' or '.S' file)	
СС	Program for compiling C files	СС	CFLAGS	Extra flags to give to the C compiler.	
схх	Program for compiling C++ files	g++	CXXFLAGS	Extra flags to give to the C++ compiler.	
СРР	Program for running the C preprocessor, with results to standard output	\$(CC) -E	CPPFLAGS	Extra flags to give to the C preprocessor and programs that use it (the C and Fortran compilers).	
FC	Program for compiling or preprocessing Fortran and	f77	FFLAGS	Extra flags to give to the Fortran compiler.	
	Ratfor files		RFLAGS	Extra flags to give to the Fortran compiler for Ratfor files.	
M2C	Program to compile Modula-2 files	m2c			
PC	Program to compile Pascal files	рс	PFLAGS	Extra flags to give to the Pascal compiler.	
со	Program for extracting a file from RCS	со	COFLAGS	Extra flags to give to the RCS co program.	
GET	Program for extracting a file from SCCS	get	GFLAGS	Extra flags to give to the SCCS get program.	
LEX	Program to use to turn Lex grammars into source code	lex	LFLAGS	Extra flags to give to Lex.	
YACC	Program to use to turn Yacc grammars into source code	yacc	YFLAGS	Extra flags to give to Yacc.	
LINT	Program to use to run lint on source code	lint	LINTFLAGS	Extra flags to give to lint.	
MAKEINFO	Program to convert a Texinfo source file into an Info file	makeinfo			
TEX	Program to make TeX DVI files from TeX source	tex			
TEXI2DVI	Program to make TeX DVI files from Texinfo source	texi2dvi			
WEAVE	Program to translate Web into TeX	weave			
CWEAVE	Program to translate C Web into TeX	weave			
TANGLE	Program to translate Web into Pascal	tangle			
CTANGLE	Program to translate C Web into C	tangle			
RM	Command to remove a file	rm -f			

		LDFLAGS	Extra flags to give to compilers when they are supposed to invoke the linker, 'ld', such as -L. Libraries (-lfoo) should be added to the LDLIBS variable instead.	
		LDLIBS	Library flags or names given to compilers when they are supposed to invoke the linker, 'ld'. Non-library linker flags, such as -L, should go in the LDFLAGS variable.	
		LOADLIBES	Deprecated (but still supported) alternative to LDLIBS.	
Automatic Variable	Expands to	Notes and exa	mples	
\$@	File name of the target. For archive(member): name or archive.			
\$(@D)	The directory part of the target	If the target is ju	ust a file name, then the value of \$(@D) is .	
\$(@F)	The file name (with extension) of the target			
\$%	File name of target archive member			
\$(%D)	The directory part of the target archive member			
\$(%F)	The file name (with extension) of the target archive member			
\$<	Name of the first prerequisite			
\$( <d)< td=""><td>The directory part of the prerequisite</td><td></td><td></td></d)<>	The directory part of the prerequisite			
\$( <f)< td=""><td>The file name (with extension) of the prerequisite</td><td></td><td></td></f)<>	The file name (with extension) of the prerequisite			
\$?	Names of all prerequisites newer than target with spaces between them.  • For archive(member), only contain the member.	Also useful in explicit rules when the receipt must operate on only the prerequisites that have changed.		
\$(?D)	List of the directory part of all prerequisites newer than target			
\$(?F)	List of the <b>file name</b> (with extension) of all prerequisites newer than target			
\$^	The names of all prerequisites with spaces between them.  For archive(member), only contain the member.  No duplicates in the list	Does not conta	in order-only prerequisites.	
\$(^D)	List of the directory part of all prerequisites (no duplicates)			
\$(^F)	Lis of the <b>file name</b> (with extension) of all prerequisites (no duplicates)			
\$+	The names of all prerequisites with spaces between them.  • For archive(member), only contain the member.  • Duplicates are allowed in the list in the same order as received	Useful when lin	king where it might be required to repeat the name of a library	
\$(+D)	List of the <b>directory</b> part of all prerequisites (with duplicates)			
\$(+F)	List of the <b>file name</b> (with extension) of all prerequisites (with duplicates)			
\$	The names of all order-only prerequisites with spaces between them.			
\$*	For implicit rule: the <b>stem</b> which an implicit rule matches.     For explicit rule, there is no <i>stem</i> : expands to the target name minus the suffix.		if target is $dir/a.foo.b$ and the target pattern is $a.\%.b$ then the stem is $dir/foo$ If target is $foo.c$ , then $$^*$ expands to $foo$ .	
\$(*D)	The directory part of the stem			
\$(*F)	The file name (with extension) of the stem			
		-		

## Suffix Rules - Obsolete Old-fashioned Suffix Rules

Kinds of old-fashioned suffix rule	Example of suffix rule	Corresponding pattern rule	Description
double-suffix	.c.o	%.o: %.c	Matches any file whose name ends with the target suffix.
single-suffix	.c	%:%.c	Matches any file name, and the corresponding implicit prerequisite name is made by appending the source suffix
	Suffix rules cannot have	oned suffix rules are obsolete because the pattern rules are more general and clearer. cannot have any prerequisites of their own. without recipe are meaningless.	

#### **Assignment operators**

ОР	Description	Example		
	Rules			
:		non-terminal		
::	Makes the rule terminal: it's prerequisite may not be an intermediate file.			
	Variables			
=	Non-terminal recursively expanded variable assignment. See: • The two-flavours of Variables • Setting Variables	The following will echo Huh?:  foo = \$(bar) bar = \$(ugh) ugh = Huh?  all:;echo \$(foo)		
:=	Simply expanded variables See:  The two-flavours of Variables	The following:		

ОР	Description	Example
::=	Simply expanded variables - 2012 POSIX standard compliant. See: • The two-flavours of Variables	The following:  x ::= foo y ::= \$(x) bar x ::= later
		<pre>is equivalent to:     y ::= foo bar     x ::= later</pre>
?=	Set variable if it is not already set. See: • Setting Variables	The following:  FOO ?= bar  is equivalent to:  ifeq (\$(origin FOO), undefined)  FOO = bar endif
!=	Shell assignment operator: used to execute a shell script and set a variable to its output. See:  • Setting Variables	For example, if you don't expect a \$ character to be part of the output string:  hash != printf '\043' file_list != findname '*.c'  If you expect \$ character(s) to be part of the output, then it's better to use another form:  hash := \$(shell printf '\043') var := \$(shell findname "*.c")