

Speedbar - Major Modes Compatibility 🚧

Language	PEL PDF Table	Emacs Support file	Feature name	Major Mode Name	Modeline Lighter	Description
Speedbar and SR-Speedbar	The table describes the capabilities and compatibility of Speedbar and Speedbar-SR with various major modes. It complements the information on Speedbar and Speedbar-SR available in the ⌘ Speedbar table. <ul style="list-style-type: none"><li>This is an early version. More information will be included as more Emacs major modes are tested with Speedbar.</li></ul>					
Ada		ada-mode.el				
Assembler		asm-mode.el				
Asciidoc						
Batch files		bat-mode.el				
C	⌘ - C	cc-mode.el				
C++	⌘ - C++	C++				
C#						
Ceylon						
Chapel						
CLU						
Clojure						
CMake						
Crystal						
Curl						
CWEB						
D	⌘ - D	D				
Dart						
Eiffel						
Elixir	⌘ - Elixir					
Elm						
Emacs Lisp	⌘⌘ - Emacs Lisp	elisp-mode.el				<ul style="list-style-type: none"><li>defun top-level &amp; indented forms ✓</li><li>defmacro top-level forms ✓ (Note: does not distinguish from defun)</li><li>defsubst top-level forms</li><li>defvar top-level and indented forms ✓</li><li>defconst top-level forms</li><li>defcustom top-level forms</li><li>defgroup top-level forms</li><li>defhydra indented ✗</li></ul>
Erlang	⌘ - Erlang					
F#						
F*						
Forth	⌘ - Forth					
Fortran F-90 and F-95		f90.el				
Fortran F-77, F-90		fortran.el				
Go						
Groovy						
Haskell						
Hop						
HTML						
Hy						
Icon		icon.el				
IDL		idlwave.el				
Java						
Javascript		js.el				
Julia	⌘ - Julia					
Kotlin						
LaTeX						
Ld - GNU Linker		ld-script.el				
Limbo						
Lisp - Common Lisp	⌘ - Common Lisp					
LiveScript						
LFE						
Lua						
M4		m4-mode.el				
Make		make-mode.el	<ul style="list-style-type: none"><li>makefile-bsdmake-mode</li></ul>	<ul style="list-style-type: none"><li>BSDmakefile</li></ul>	<ul style="list-style-type: none"><li>Macro assignments ✓</li><li>Dependencies ✓<ul style="list-style-type: none"><li>✗ Includes extra/invalid dependencies in operations when the line includes a colon. Seen on Emacs 26.3. However <b>makefile-previous-dependency</b> does work properly. Since this is what’s used, need investigation. 🚧</li></ul></li></ul>	
Markdown						
ML						
Modula 2		modula2.el			1	
Modula 3						

Language	PEL PDF Table	Emacs Support file	Feature name	Major Mode Name	Modeline Lighter	Description
Objective-C						
OCaml						
Octave		octave.el				
OpenCL						
Opa						
Org Mode						
Oxygene						
Pascal		pascal.el				
Perl		perl-mode.el				
Pike						
Postscript		ps-mode.el				
Prolog		prolog.el				
PureScript						
Python	ꝑꝢ - Python	python.el		python-mode	Python	<ul style="list-style-type: none"> <li>• class definitions ✓</li> <li>• class method definitions ✓</li> <li>• function definitions ✓</li> <li>• ✗ functions defined inside logical statements (like if statements) and therefore indented are no always reported. 🚧 More investigation needed.</li> </ul>
		<a href="#">python-mode.el</a>		python-mode	Py	
		<p>⚠ Both files use the <b>same</b> major mode name. python.el is distributed with Emacs, python-mode.el was written before but is not part of the standard distribution and is quite large (1677 functions in version 20201230.2132). Once python-mode.el is installed it normally overrides python.el by being identified before python.el in Emacs load-path. It is not possible to use both and select one dynamically.</p> <p>🚧 Although this was written first, at this point I'd recommend to rename the mode to py-mode (it would match the mode-line lighter).</p>				
Racket						
ReasonML						
reStructuredText						
Rexx	ꝑꝢ - REXX					
Rebol						
Red						
Ruby		ruby-mode.el				
Rust						
Sather						
Scala						
Scheme		scheme.el				
MIT Scheme		xscheme.el				
Self						
Shell Script		sh-script.el				
Shen						
Simula		simula.el				
Smalltalk						
Solidity						
SQL		sql.el				
Swift						
Tcl		tcl.el	tcl			
TeX						
TypeScript						
V						
Vala						
Vera, OpenVera		vera-mode.el	vera-mode			
<ul style="list-style-type: none"> <li>• Verilog</li> <li>• SystemVerilog</li> </ul>		verilog-mode.el				
VHDL		vhdl-mode.el				
XML						
Zig						