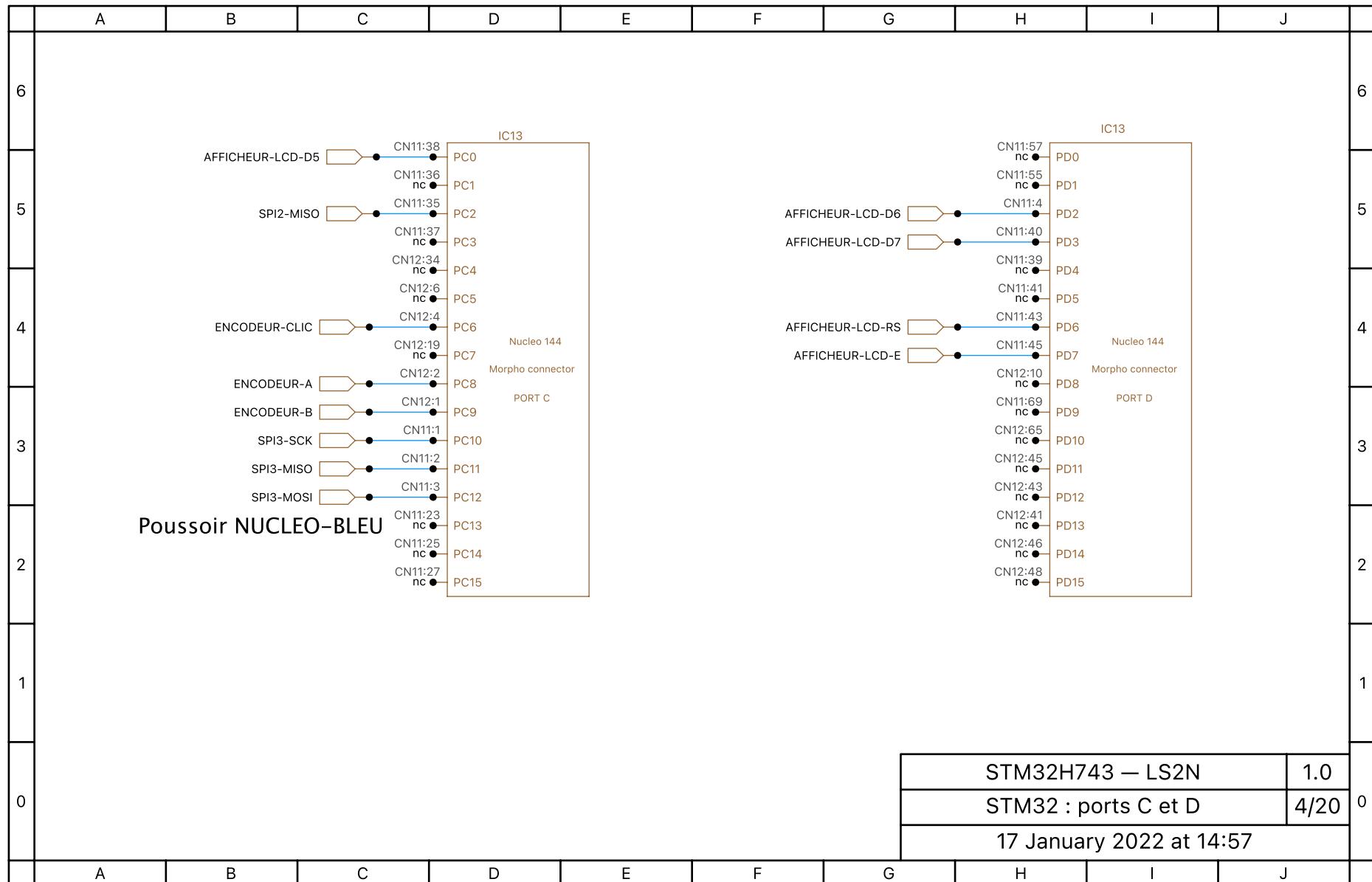
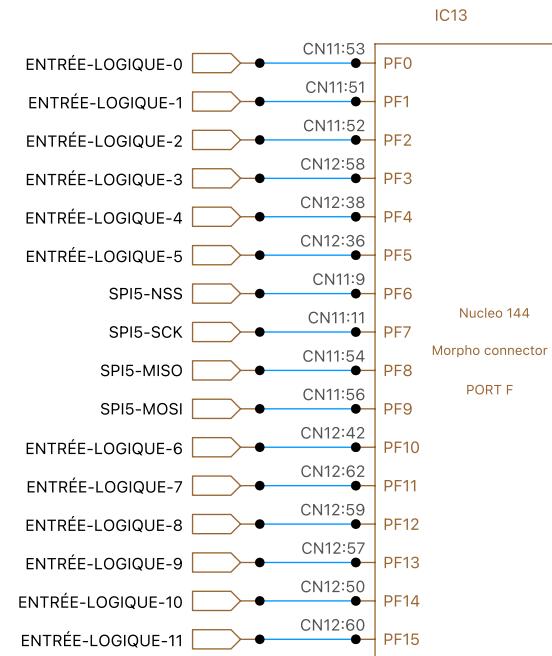
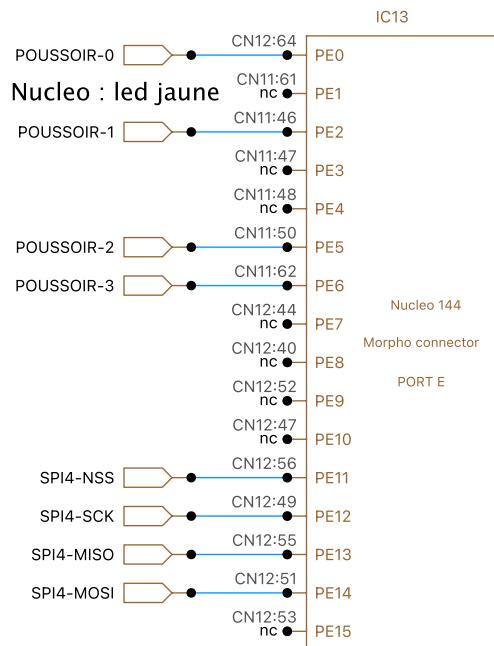


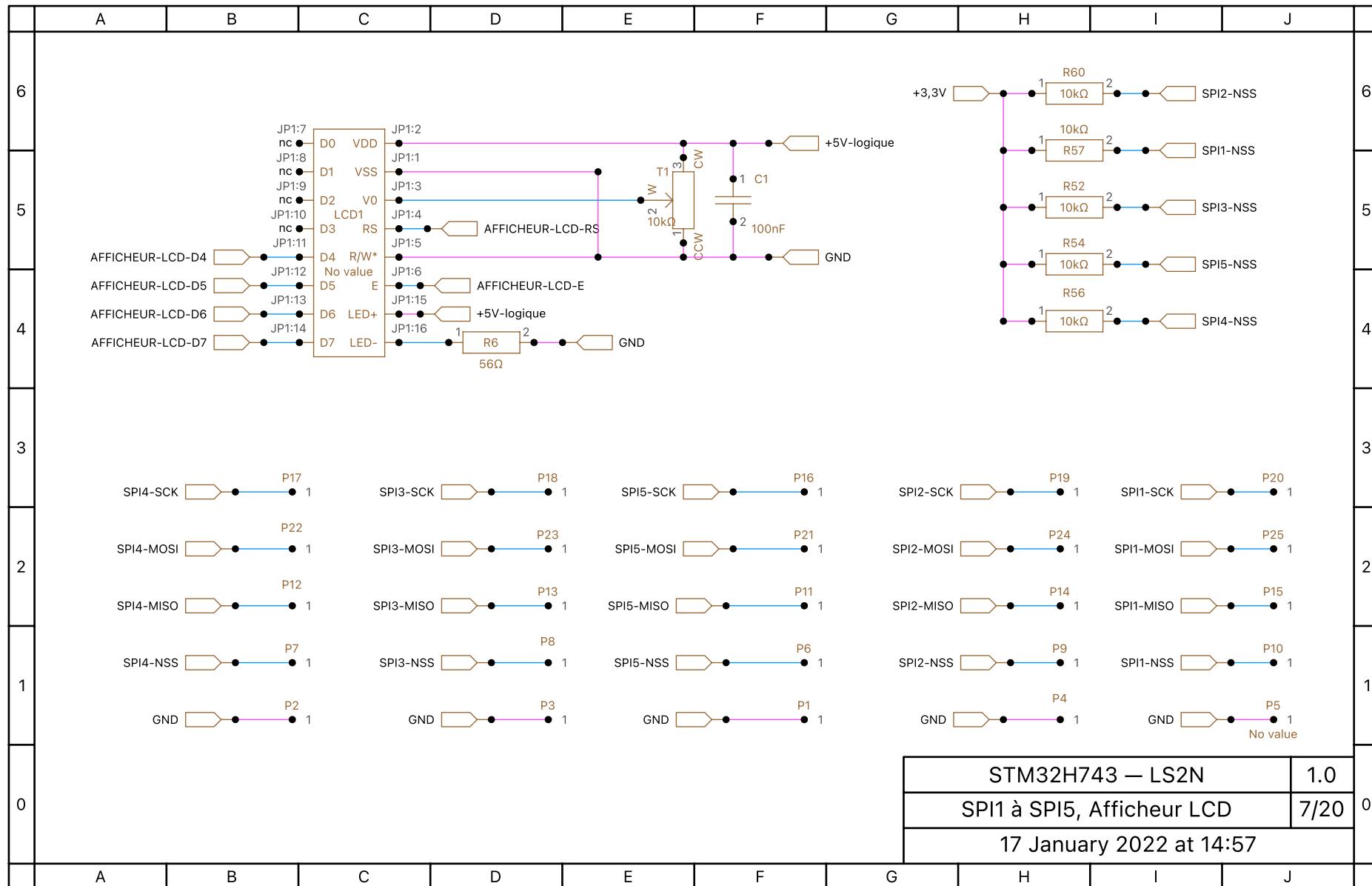
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6																				
5	<p>Diagram illustrating the pinout mapping for the Nucleo 144 board. The connections are as follows:</p> <ul style="list-style-type: none"> LED-0 connects to CN11:28 (PA0). CN11:30 (nc) connects to PA1. CN12:35 (nc) connects to PA2. LED-1 connects to CN12:37 (PA3). SORTIE-ANALOGIQUE-STM32 connects to CN11:32 (PA4). SPI1-SCK connects to CN12:11 (PA5). CN12:13 (nc) connects to PA6. CN12:15 (nc) connects to PA7. CN12:23 (nc) connects to PA8. CN12:21 (nc) connects to PA9. CN12:33 (nc) connects to PA10. CN12:14 (nc) connects to PA11. CN12:12 (nc) connects to PA12. CN11:13 (nc) connects to PA13. CN11:15 (nc) connects to PA14. CN11:17 connects to PA15. SPI3-NSS connects to CN11:28 (PA0). 																			
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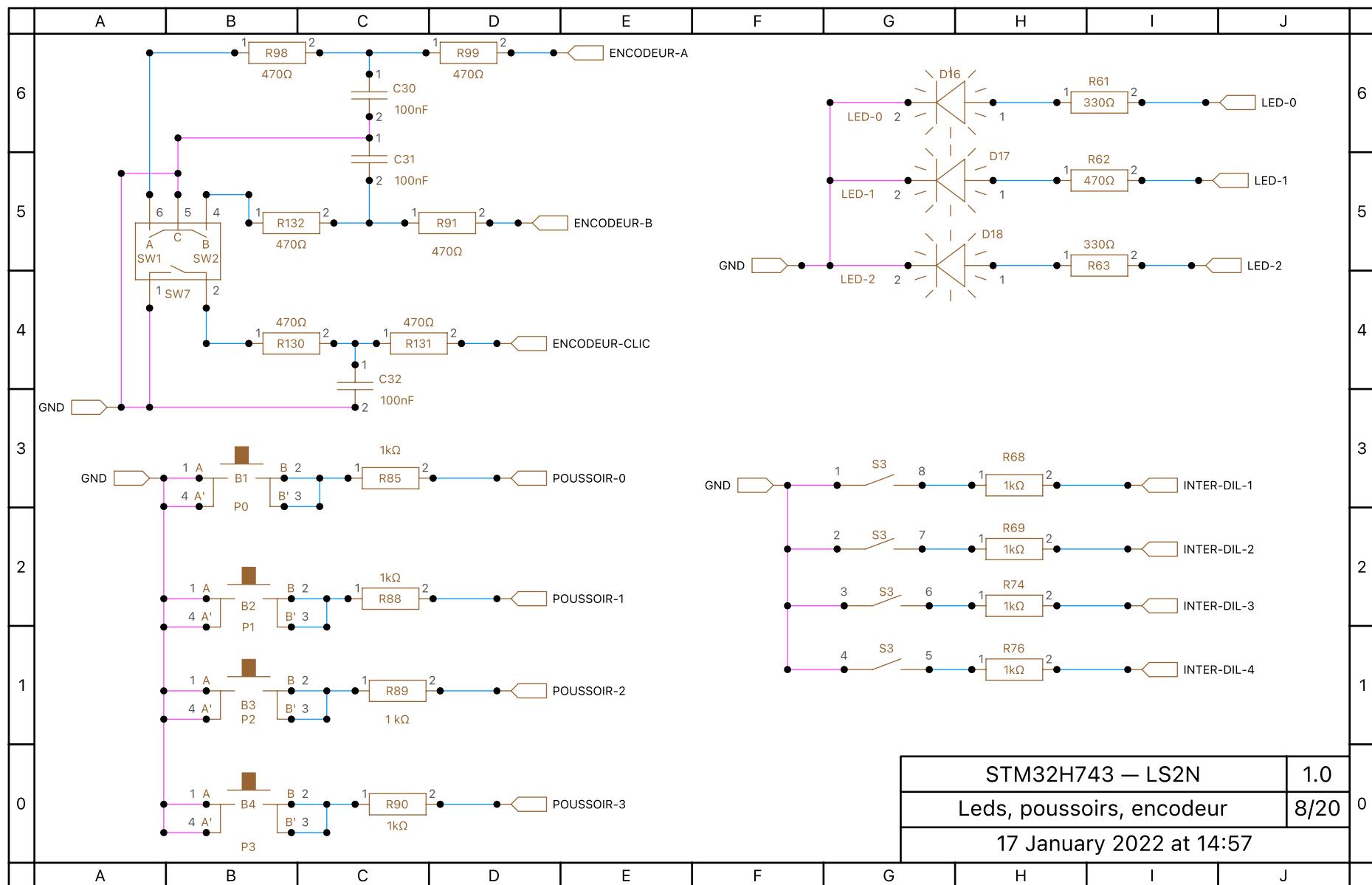


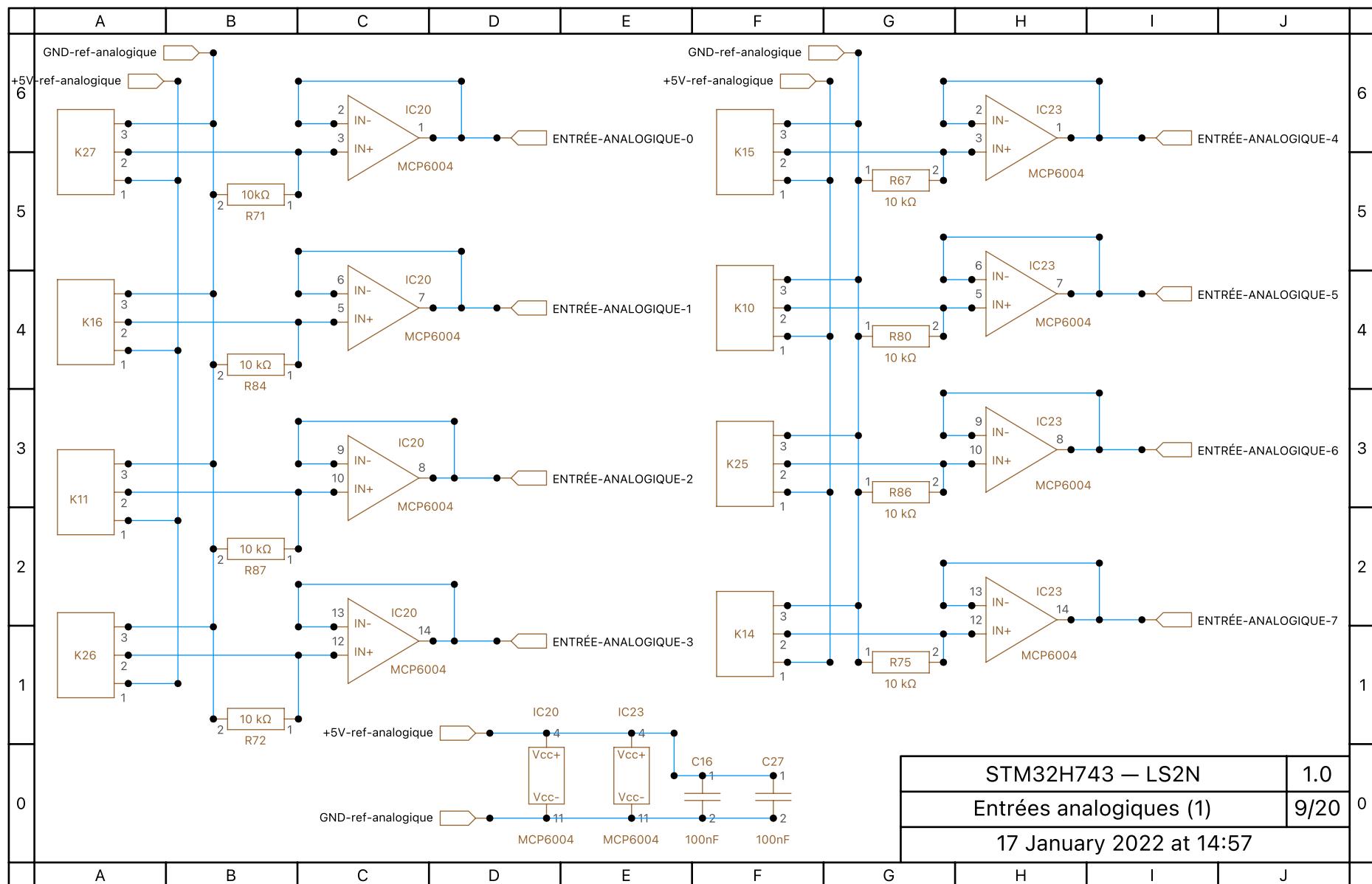
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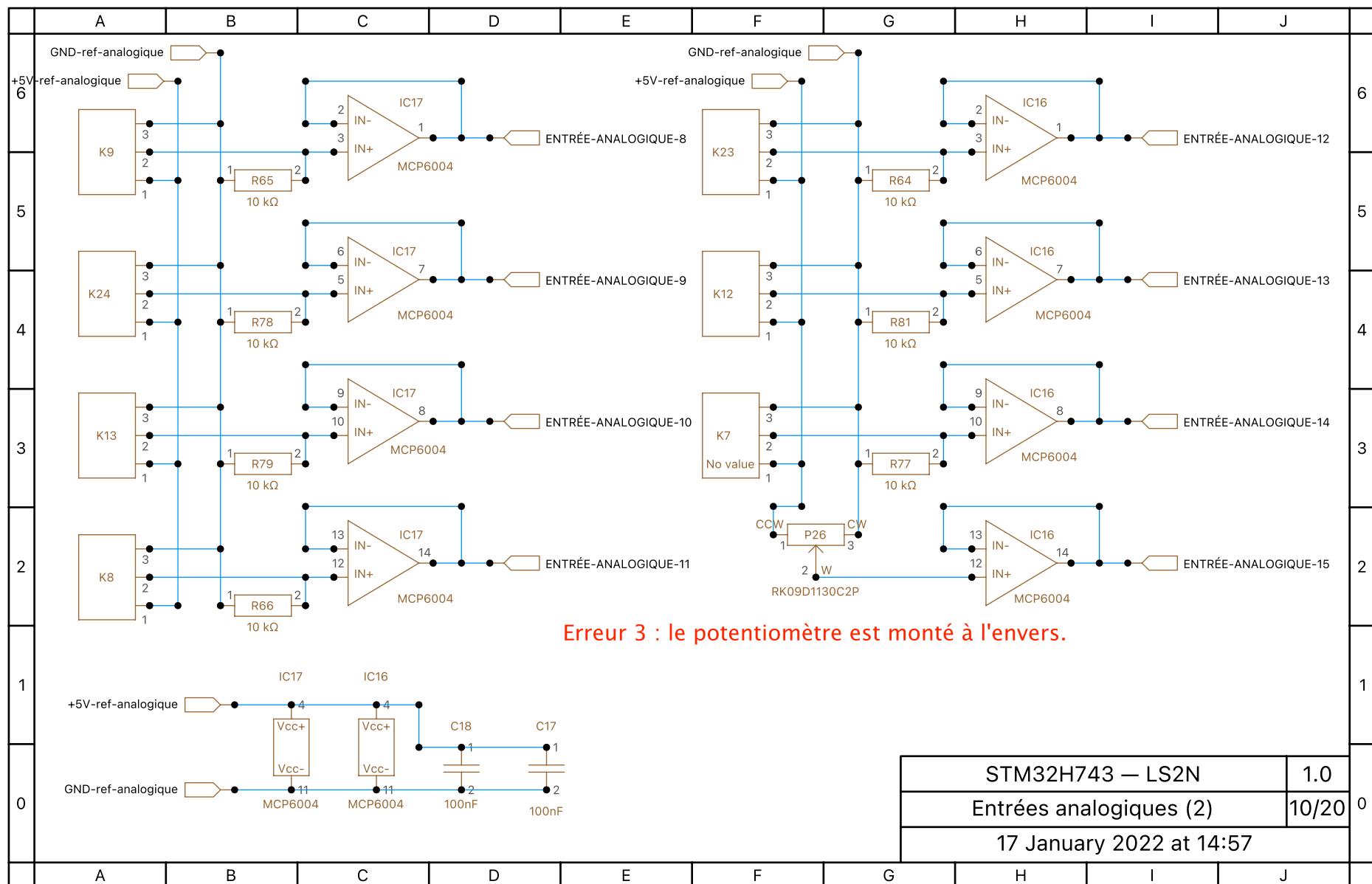


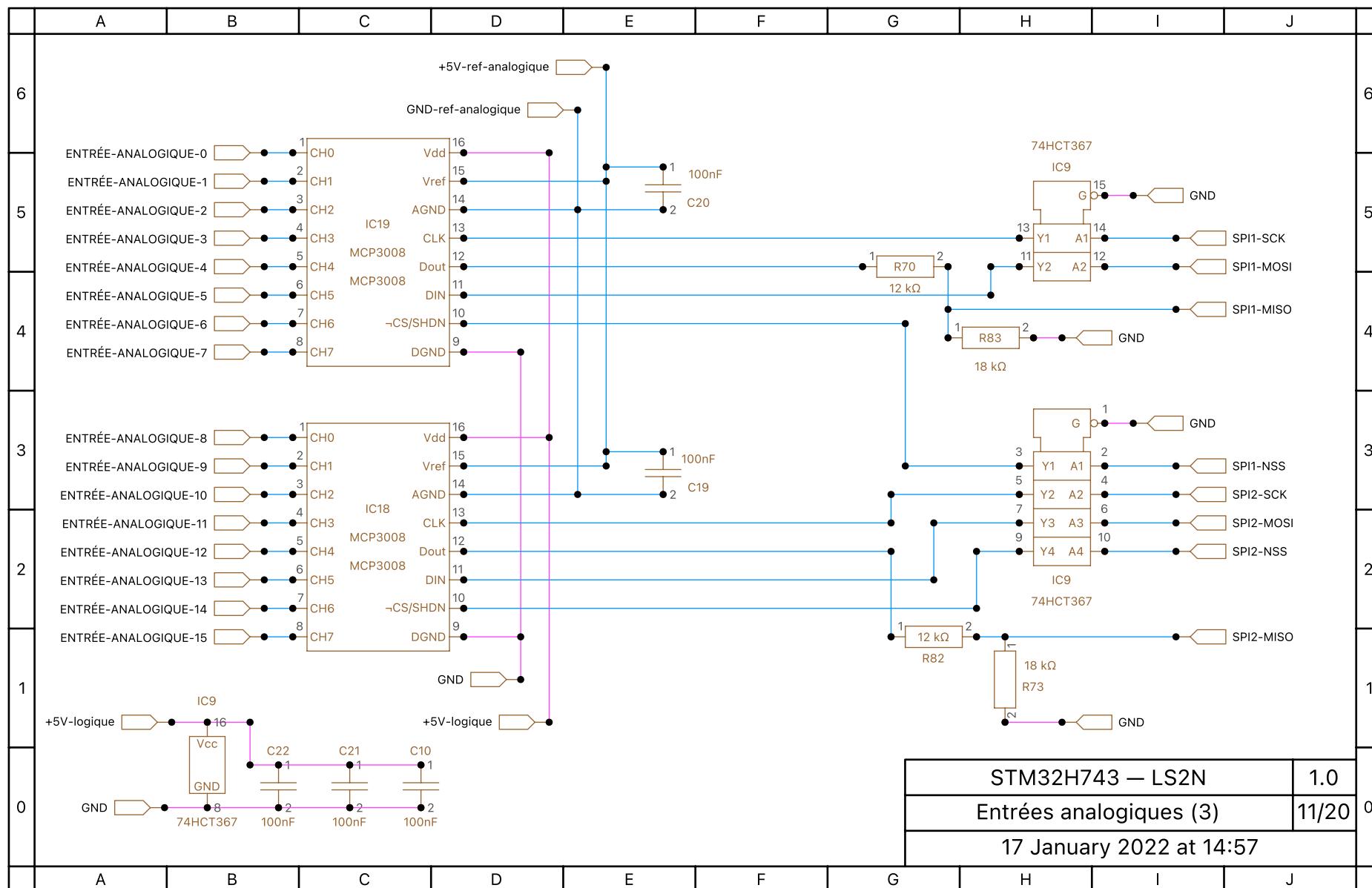
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5	RETOUR-LOGIQUE-VANNE-2 RETOUR-LOGIQUE-VANNE-0 RETOUR-LOGIQUE-VANNE-1										5																																
4	<p>AFFICHEUR-LCD-D4 → PG6 SPI1-NSS → PG10 INTER-DIL-1 → PG14</p> <table border="1"> <tr><td>CN11:59 nc</td><td>PG0</td></tr> <tr><td>CN11:58 nc</td><td>PG1</td></tr> <tr><td>CN11:42 nc</td><td>PG2</td></tr> <tr><td>CN11:44 nc</td><td>PG3</td></tr> <tr><td>CN12:69 nc</td><td>PG4</td></tr> <tr><td>CN12:68 nc</td><td>PG5</td></tr> <tr><td>CN12:70 nc</td><td>PG6</td></tr> <tr><td>CN12:67 nc</td><td>PG7</td></tr> <tr><td>CN12:66 nc</td><td>PG8</td></tr> <tr><td>CN11:63 nc</td><td>PG9</td></tr> <tr><td>CN11:66 nc</td><td>PG10</td></tr> <tr><td>CN11:70 nc</td><td>PG11</td></tr> <tr><td>CN11:65 nc</td><td>PG12</td></tr> <tr><td>CN11:68 nc</td><td>PG13</td></tr> <tr><td>CN12:61 nc</td><td>PG14</td></tr> <tr><td>CN11:64 nc</td><td>PG15</td></tr> </table>										CN11:59 nc	PG0	CN11:58 nc	PG1	CN11:42 nc	PG2	CN11:44 nc	PG3	CN12:69 nc	PG4	CN12:68 nc	PG5	CN12:70 nc	PG6	CN12:67 nc	PG7	CN12:66 nc	PG8	CN11:63 nc	PG9	CN11:66 nc	PG10	CN11:70 nc	PG11	CN11:65 nc	PG12	CN11:68 nc	PG13	CN12:61 nc	PG14	CN11:64 nc	PG15	4
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2	<p>Nucleo 144 → PG13</p> <table border="1"> <tr><td>CN11:29 nc</td><td>PH0</td></tr> <tr><td>CN11:31 nc</td><td>PH1</td></tr> </table>										CN11:29 nc	PH0	CN11:31 nc	PH1	2																												
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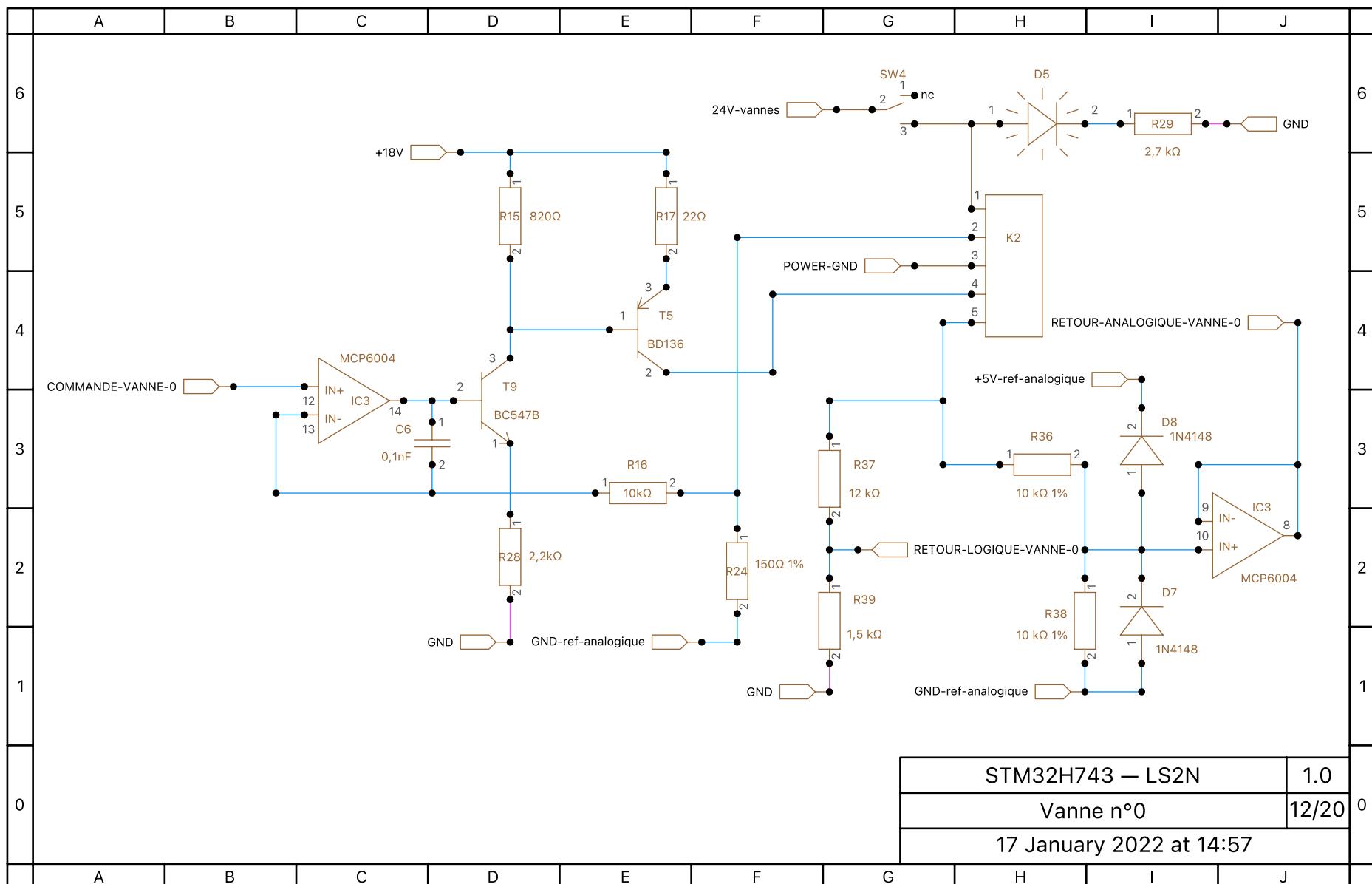


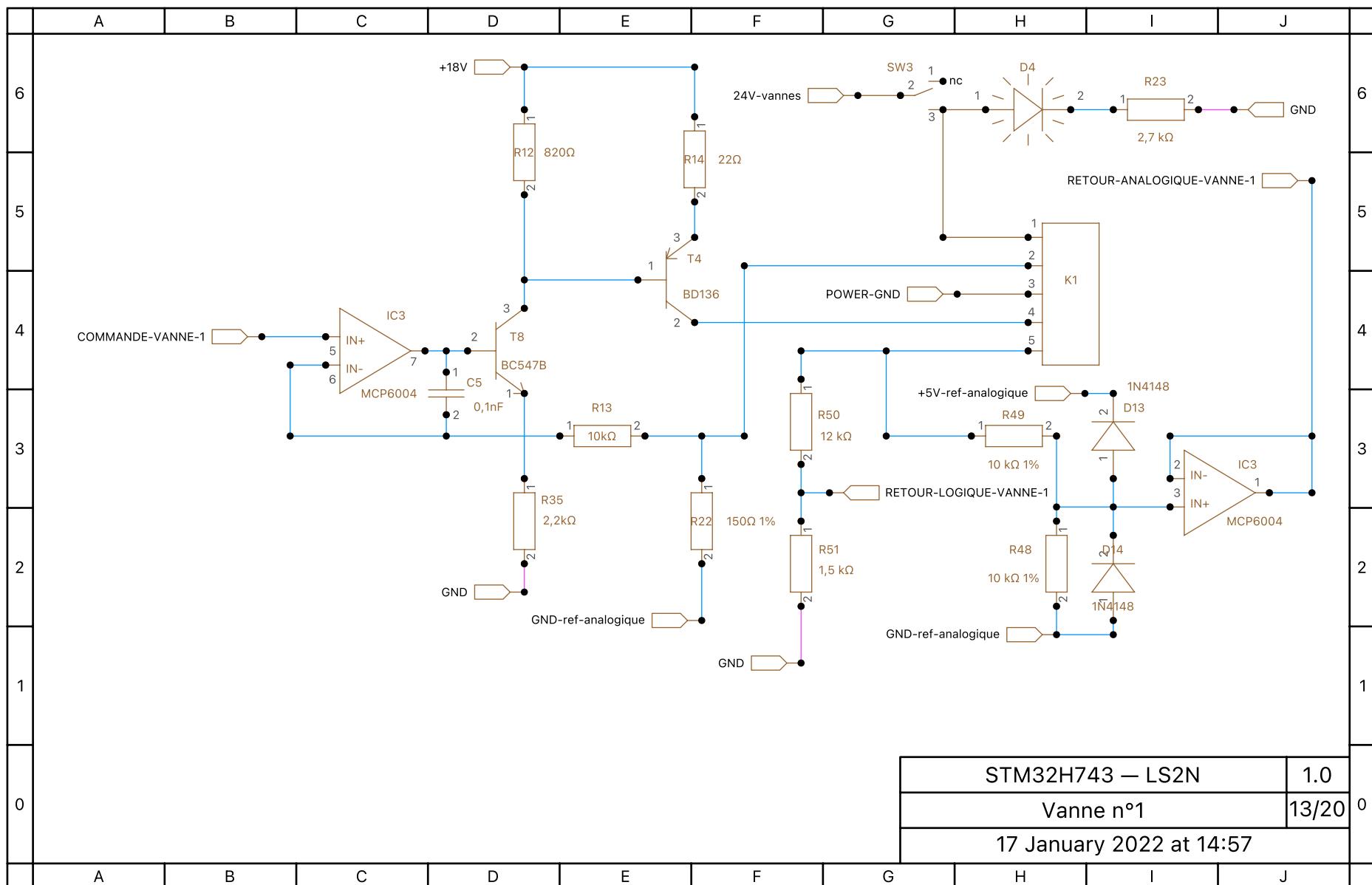


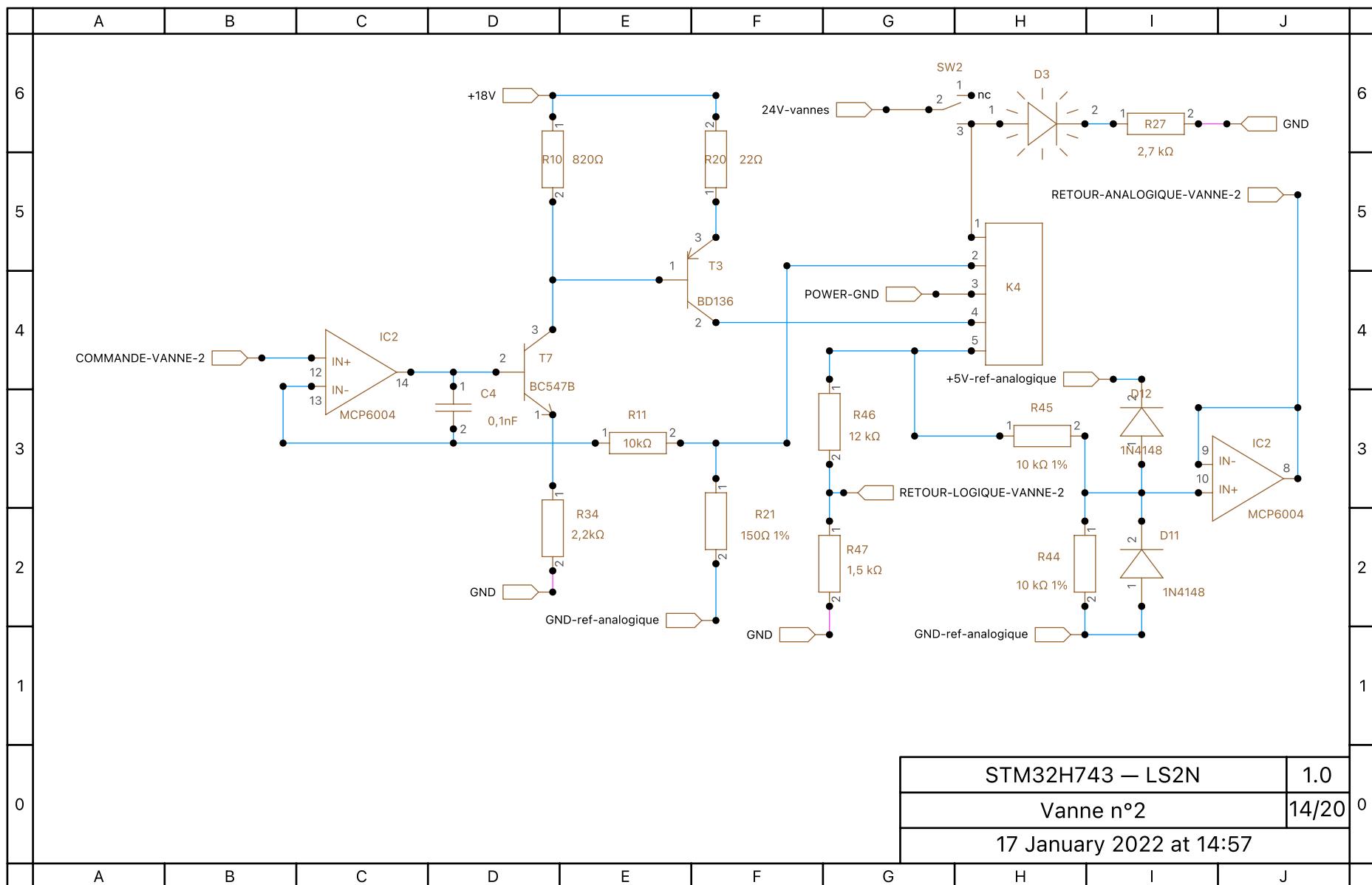












STM32H743 – LS2N	1.0
Vanne n°2	14/20
17 January 2022 at 14:57	

