More System calls and Page faults

ECE 469, Feb 15

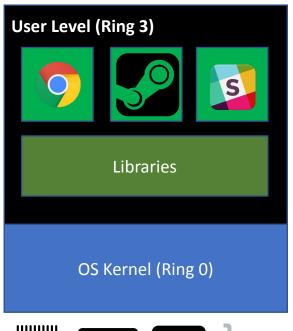
Aravind Machiry

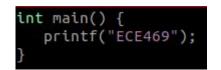


Recap: Syscalls

- An API of an OS
- User-level Application calls functions in kernel
 - Open
 - Read
 - Write
 - Exec
 - Send
 - Recv
 - Socket
 - Etc...











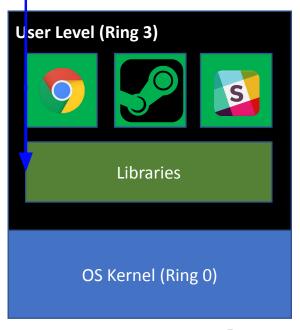






printf("ECE469")

A library call in ring 3











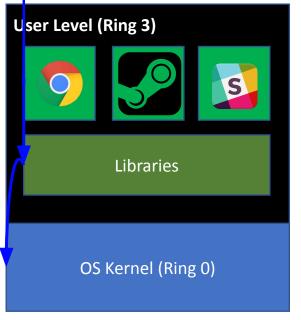




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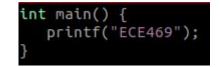
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Interrupt!, switch from ring3 to ring0















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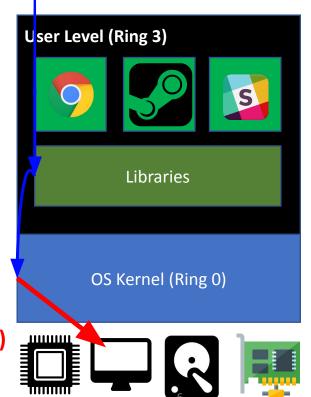
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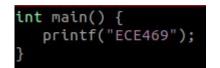
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A kernel function do_sys_write(1, "ECE469", 6)







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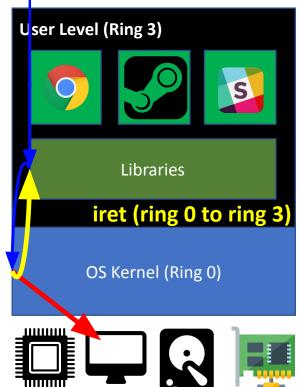
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int main() {
   printf("ECE469");
}
```



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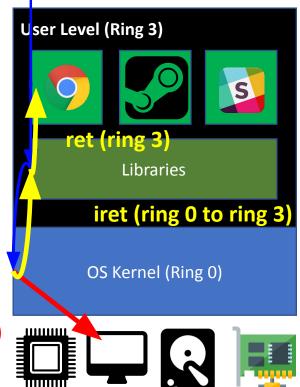
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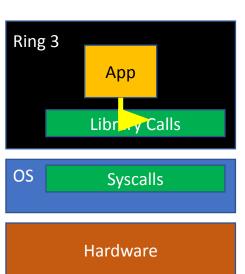


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System calls via Interrupt Handler



- Call gate
 - System call can be invoked only with trap handler
 - int \$0x30 in JOS
 - int \$0x80 in Linux (32-bit)
 - int \$0x2e in Windows (32-bit)
 - sysenter/sysexit (32-bit)
 - syscall/sysret (64-bit)
- OS performs checks if userspace is doing a right thing
 - Before performing important ring 0 operations
 - E.g., accessing hardware..

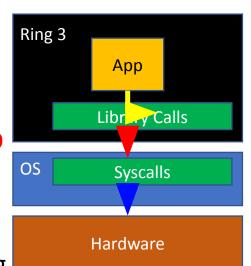


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int \$0x30
CHECK!!



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 - Before performing important ring 0 operations
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Implementing Syscalls in JOS



- See kern/syscall.c
- void sys_cputs (const char *s, size_t len)
 - Print a string in s to the console
- int sys_cgetc(void)
 - Get a character from the keyboard
- envid_t sys_getenvid(void)
 - Get the current environment ID (process ID)
- int sys env destroy(envid t)
 - Kill the current environment (process)

Implementing Syscalls in JOS



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Required for Implementing scanf, printf, etc...

- How can we pass arguments to syscalls?
 - Remember syscalls are implemented as interrupts!

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 - Remember syscalls are implemented as interrupts!

General Purpose Registers!!!



- In JOS
 - eax = system call number
 - \circ edx = 1st argument
 - \circ ecx = 2^{nd} argument
 - ebx = 3rd argument
 - edi = 4th argument
 - esi = 5th argument
- E.g., calling sys_cputs("asdf", 4);
 - \circ eax = 0
 - edx = address of "asdf"
 - \circ ecx = 4
 - ebx, edi, esi = not used
- And then
 - o Run int \$0x30

```
/* system call numbers */
enum {
    SYS_cputs = 0,
    SYS_cgetc,
    SYS_getenvid,
    SYS_env_destroy,
    NSYSCALLS
};
```



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```

Will add more as our lab implementation progresses



- In Linux x86 (32-bit)
 - eax = system call number
 - ebx = 1st argument
 - ecx = 2nd argument
 - edx = 3rd argument
 - esi = 4th argument
 - edi = 5th argument
- See table
 - https://syscalls.kernelgrok.com/ : lists 337 system calls...

0	sys_restart_syscall	0x00
1	sys_exit	0x01
2	sys_fork	0x02
3	sys_read	0x03
4	sys_write	0x04
5	sys_open	0x05
6	sys_close	0x06
7	sys_waitpid	0x07
8	sys_creat	0x08
9	sys_link	0x09
10	sys_unlink	0x0a
11	sys_execve	0x0b

Handling arguments to Syscalls



```
E.g., calling sys_cputs("asdf", 4);
eax = 0
edx = address of "asdf"
ecx = 4
ebx, edi, esi = not used
```

- And then
 - Run int \$0x30
- At interrupt handler
 - · Read syscall number from the eax of tf
 - syscall number is 0 -> calling SYS_cputs
 - Read 1st argument from the edx of tf
 - Address of "adsf"
 - Read 2nd argument from ecx of tf
 - 4
 - call sys_cputs("asdf", 4) // in kernel

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```

Invoking Syscalls

- Set all arguments in the registers
 - Order: edx ecx ebx edi esi
- int \$0x30 (in JOS)
 - Software interrupt 48
- int \$0x80 (in 32bit Linux)
 - Software interrupt 128

User calls a function



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 - Set args in the register and then
- int \$0x30

Now kernel execution starts...

```
struct Trapframe {
    struct PushRegs tf_regs;
    uint16_t tf_es;
    uint16_t tf_padding1;
    uint16_t tf_ds;
    uint16_t tf_padding2;
    uint32_t tf_trapno;
    /* below here defined by
    uint32_t tf_err;
    uintptr_t tf_eip;
    uint16_t tf_cs:
    uint16_t tf_padding3;
    uint32_t tf_eflags;
    /* below here only when
    uintptr_t tf_esp;
    uint16_t tf_ss;
    uint16_t tf_padding4;
    _attribute__((packed));
```

CPU gets software interrupt



```
struct Trapframe {
    struct PushReas tf_reas;
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- CPU gets software interrupt
- TRAPHANDLER_NOEC(T_SYSCALL...)



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 - Call syscall() using those registers
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 - Get back to the user environment!



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•env_pop_tf()

```
void
env_pop_tf(struct Trapframe *tf)
{
    asm volatile(
        "\tmovl %0,%%esp\n"
        "\tpopal\n"
        "\tpopl %%es\n"
        "\tpopl %%ds\n"
        "\taddl $0x8,%%esp\n" /* skip tf_trapno and tf_errcode */
        "\tiret\n"
        : "g" (tf) : "memory");
    panic("iret failed"); /* mostly to placate the compiler */
}
```



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- •env_pop_tf()
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- Back to Ring 3!

- Execution...
- int \$0x30
- Call trap gate
- Handle trap!
- Pop context
- iret
- Execution resumes...

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Ring 3

Ring 0



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• int \$0x30 Ring 3

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Ring 0

Ring 3

Page faults



- Occurs when paging (address translation) fails
 - !(pde&PTE_P) or !(pte&PTE_P): Present bit not set
 - Automated extension of runtime stack
 - Write access but !(pte&PTE_W): access violation
 - Access from user but !(pte&PTE_U): protection violation

Page faults Handling (2): Copy-On-Write (CoW)



- Copy-on-Write (CoW)
 - Technique to reduce memory footprint
 - Share pages read-only
 - Create a private copy when the first write access happens
- Memory Swapping
 - Use disk as extra space for physical memory
 - Limited RAM Size: 16GB?
 - We have a bigger storage: 1T SSD, Hard Disk, online storage, etc.
 - Can we store some 'currently unused but will be used later' part into the disk?
 - Then we can store only the active part of data in memory

Program in Memory

- .text
 - Code area. Read-only and executable
- .rodata
 - Data area, Read-only and not executable
- .data
 - Data area, Read/Writable (not executable)
 - Initialized by some values
- .bss (uninitialized data)
 - Data area, Read/Writable (not executable)
 - Initialized as 0



.bss (RW-)

.data (RW-)

.rodata (R--)



.bss (RW-)

.data (RW-)

.rodata (R--)



.bss (RW-)

.data (RW-)

.rodata (R--)

.text (R-X)

Process 1

.bss (RW-)

.data (RW-)

.rodata (R--)



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Process 1

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.data (RW-)

.rodata (R--)

.text (R-X)

Process 2

.bss (RW-)

.data (RW-)

.rodata (R--)



Do we need to copy the same data for each process creation?

Process 2

.bss (RW-)

.data (RW-)

.rodata (R--)

.text (R-X)

.bss (RW-)

Process 1

.data (RW-)

.rodata (R--)

.text (R-X)

.bss (RW-)

.data (RW-)

.rodata (R--)

Sharing pages by marking read-only



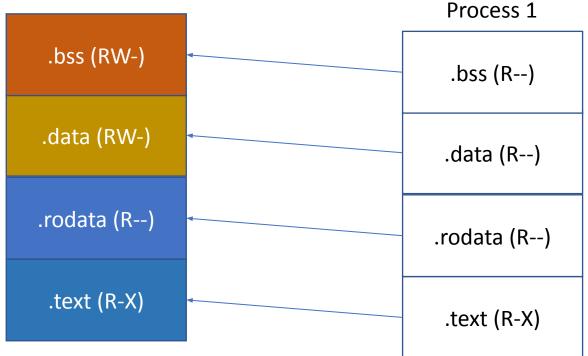
Set page table to map the same physical address to share contents

.bss (RW-) .data (RW-) .rodata (R--) .text (R-X)

Sharing pages by marking read-only



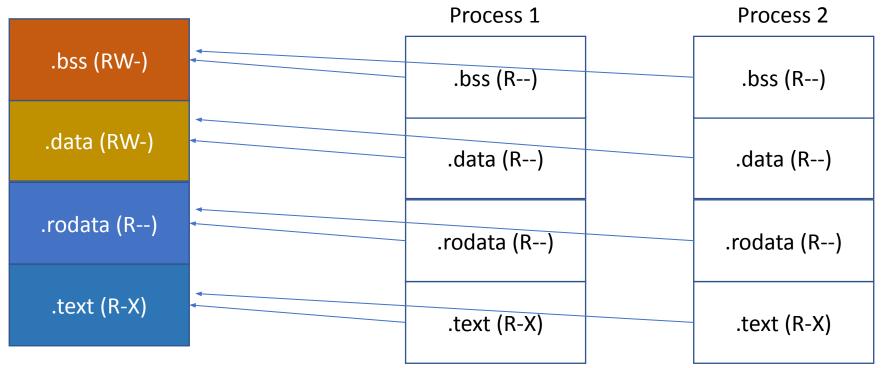
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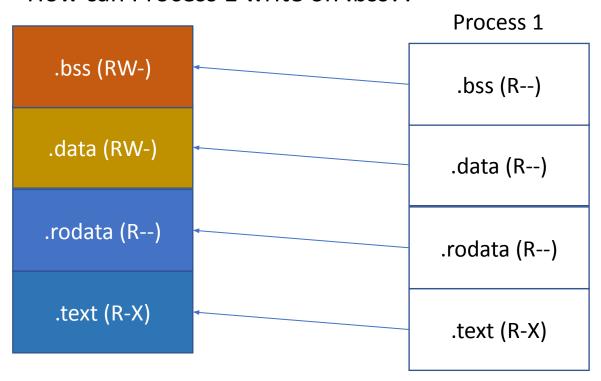
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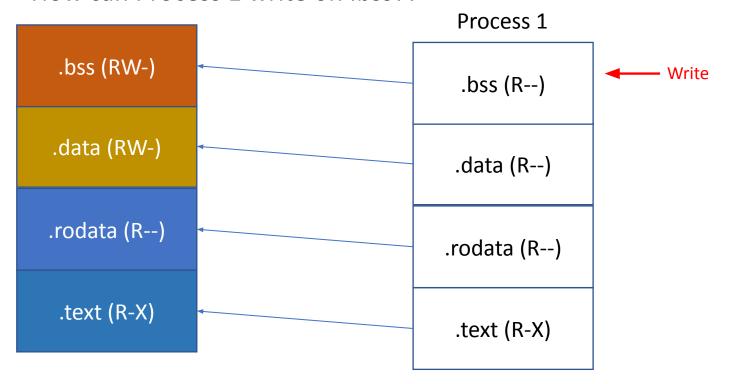


What about writes?



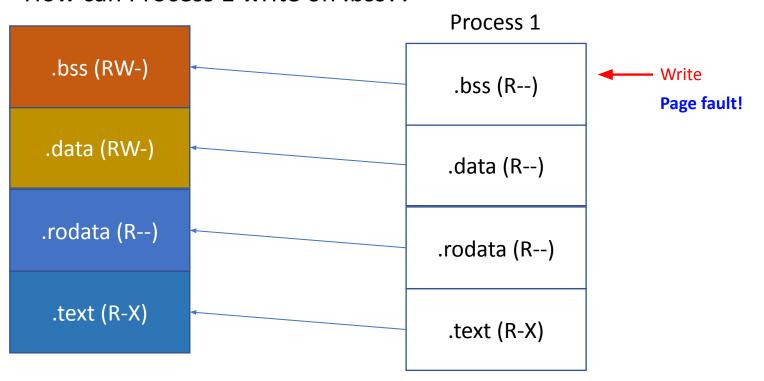
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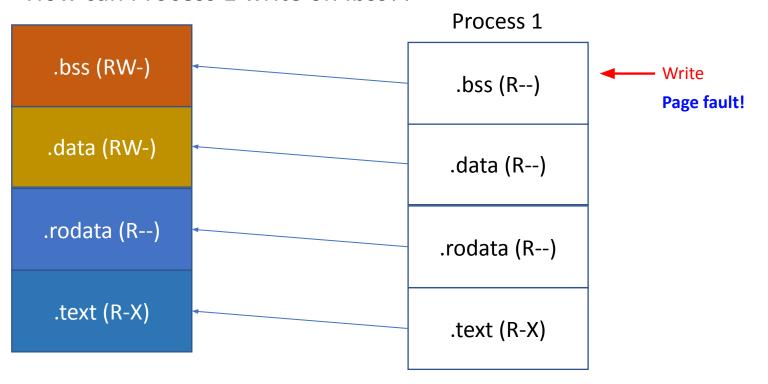




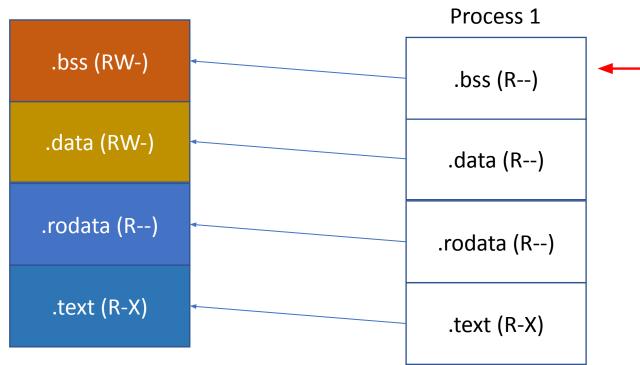


- Read CR2
 - A fault from one of the shared location!
- Read Error code
 - Write on read-only memory
 - Hmm... the process requires a private copy! (we actually mark if COW is required in PTE)
- ToDo: create a writable, private copy for that process!
 - Map a new physical page (page_alloc, page_insert)
 - Copy the contents
 - Mark it read/write
 - Resume...





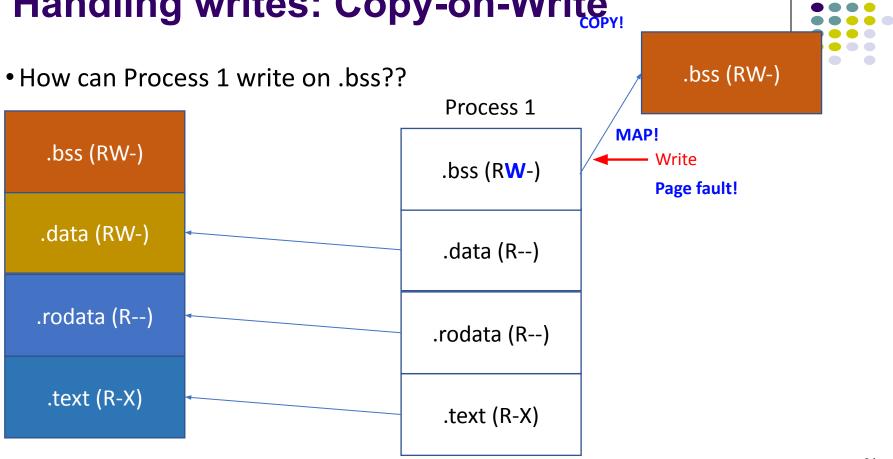
How can Process 1 write on .bss??

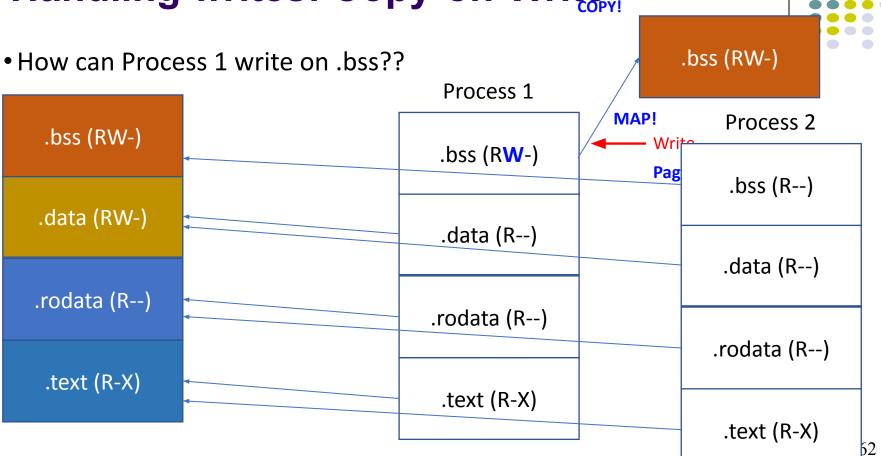


.bss (RW-)

Write

Page fault!





Benefits



- Can reduce time for copying contents that is already in some physical memory (page cache)
- Can reduce actual use of physical memory by sharing code/read-only data among multiple processes
 - 1,000,000 processes, requiring only 1 copy of .text/.rodata
- At the same time
 - Can support sharing of writable pages (if not has written at all)
 - Can create private pages seamlessly on write

Benefits

By exploiting page fault and its handler, we can implement copy-on-write, a mechanism that can reduce physical memory usage by sharing pages of same contents among multiple processes.

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- Can reduce actual use of physical memory by sharing code/read-only data among multiple processes
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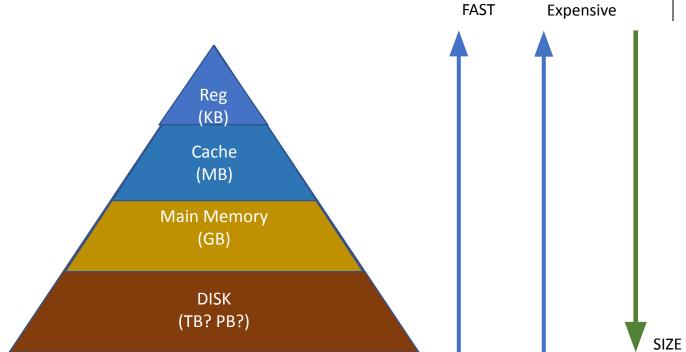
Handling low memory

Suppose you have 8GB of main memory

- Can you run a program that its program size is 16GB?
 - Yes, you can load them part by part
 - This is because we do not use all of data at the same time
- Can your OS do this execution seamlessly to your application?

Memory Hierarchy

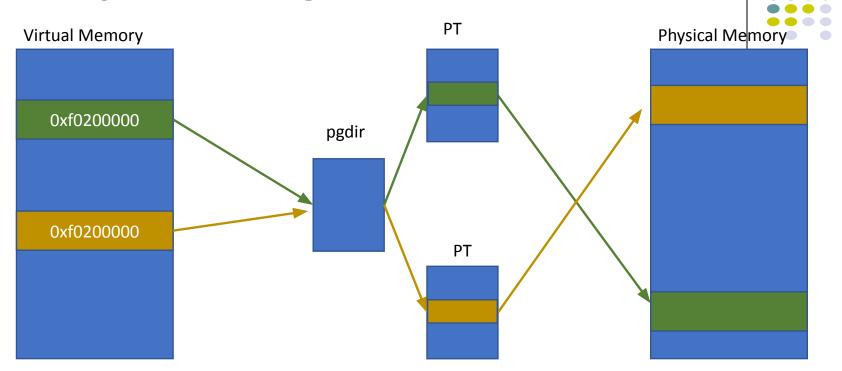




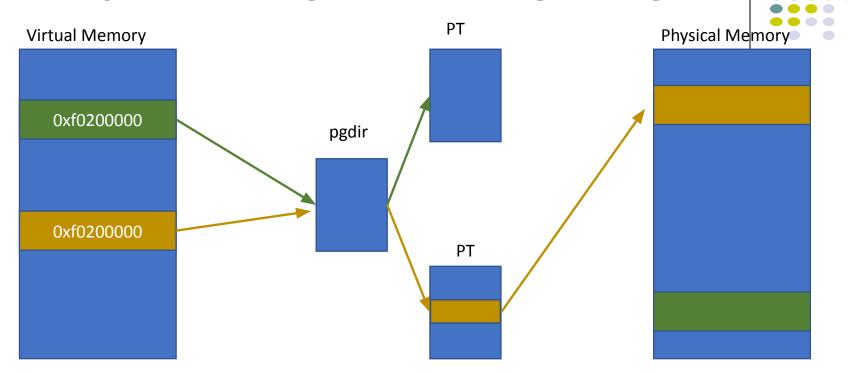
Memory Swapping

• Use disk as backing store under memory pressure

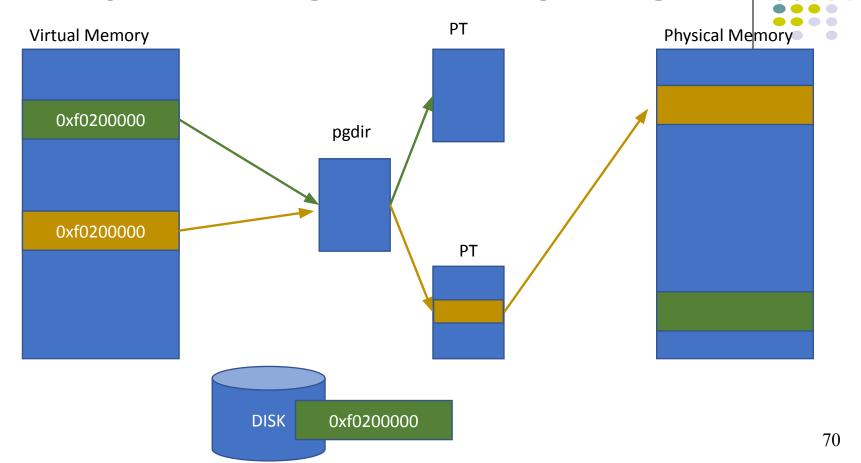
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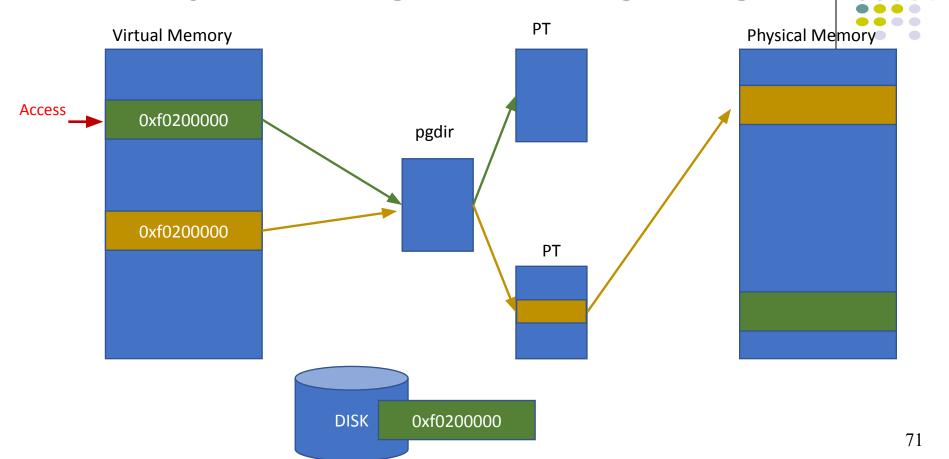
Memory Swapping - Removing a page



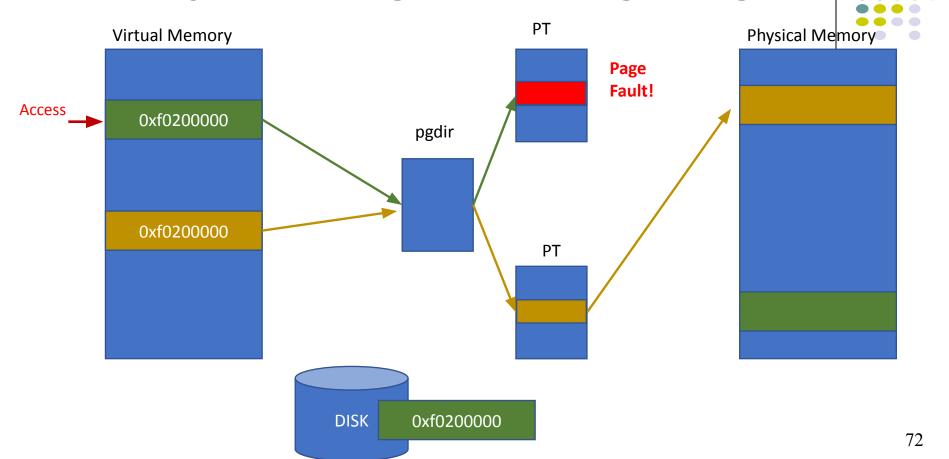
Memory Swapping - Removing a page



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Memory Swapping - Removing a page





Page fault handler



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 - Read CR2 (get address, 0xf0200000)



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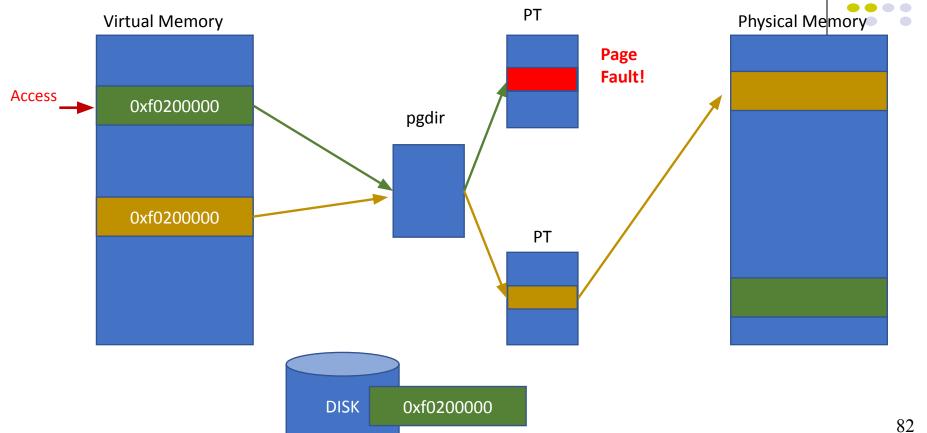
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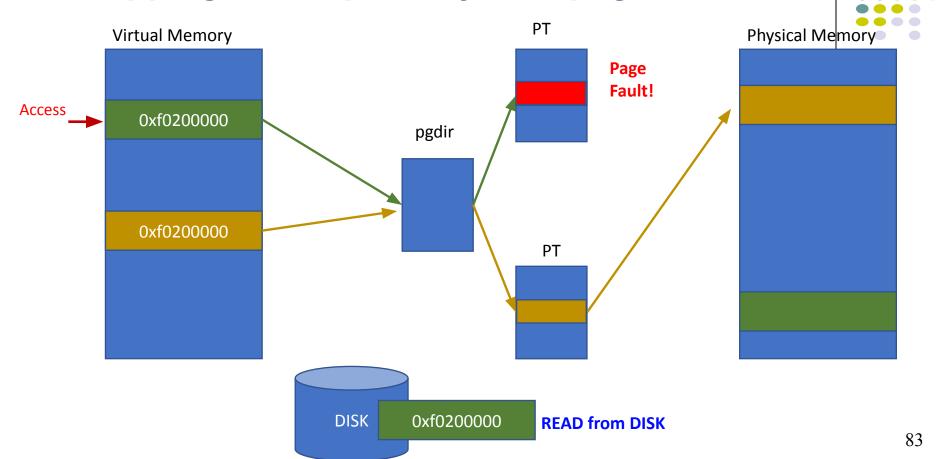


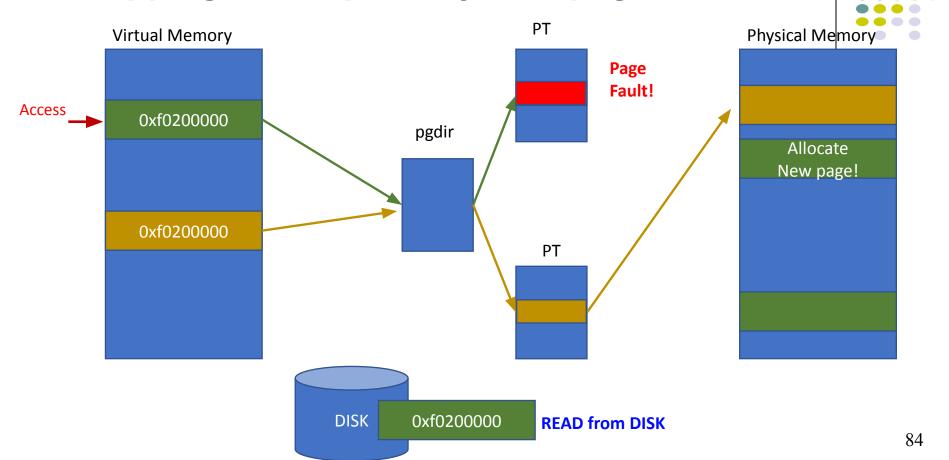
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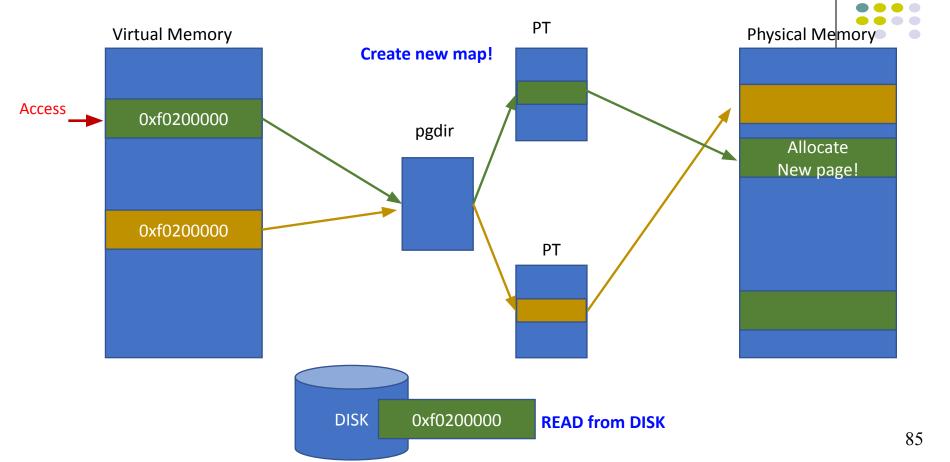


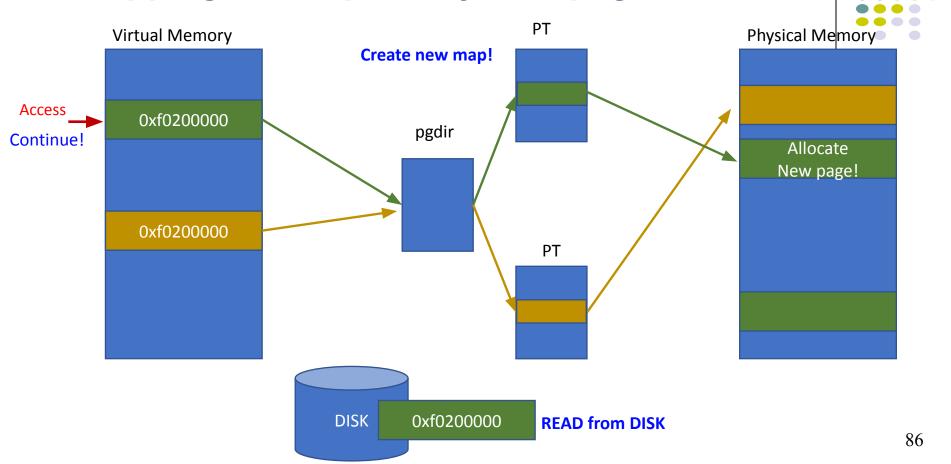
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- Load that page into physical memory
- Map it and then continue!











Page Fault



- Is generated when there is a memory error (regarding paging)
- Is an exception that can be recovered
 - And user program may resume the execution

- Is useful for implementing
 - Automatic stack allocation
 - Copy-on-write (will do in Lab4)
 - Swapping