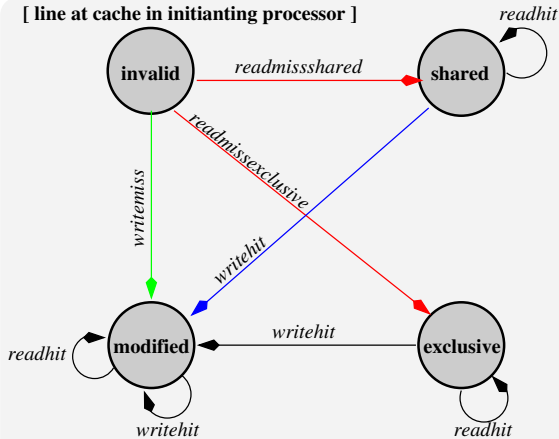


[ line at cache in initiating processor ]



—▶ read with intend to modify

—▶ invalidate transaction

—▶ cache line fill