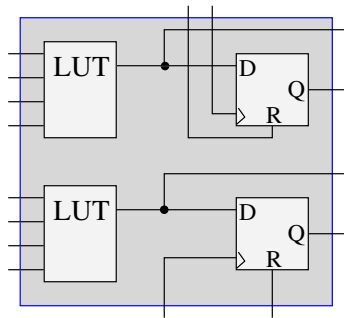
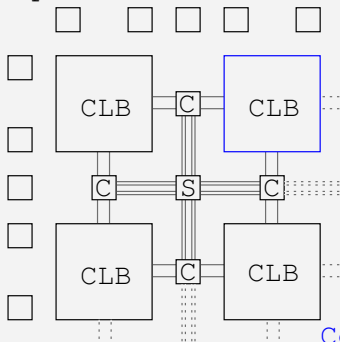


# Input/Output-Blocks



Configurable Logic Block