

# Yulong Hui

qinchuanhuiyulong@sjtu.edu.cn | 18717351390 | qinchuanhui.github.io

---

## RESEARCH INTERESTS

**Expected Future:** Cloud System, Database, AI System

**Past:** Networks, Wireless Communication, Deep Learning

## EDUCATION

**School of Electronic Information, Shanghai Jiaotong University**

Major in Computer Science and Engineering      GPA: 90.3/100      Rank:17/157(10.8%)      Sep 2018 - present

**Zhiyuan College, Shanghai Jiaotong University**

Member of Zhiyuan Honors Program of Engineering (Top 5%)      Feb 2019 - present

**Antai College of Economics, Shanghai Jiaotong University**

Minor in Finance      Feb 2020 - present

## PROFESSIONAL EXPERIENCE

**Shanghai Jiaotong University**

Shanghai, China

Supervised by *Quan Chen*, in Emerging Parallel Computing Center

Mar 2021 – Present

- Research Intern
- Focus on the optimization of Cloud resource and AI system.

Supervised by *Jiadi Yu*, in a school program called PRP

Feb 2021 – Present

- Research Intern
- Project about Gait Recognition based on WiFi, aimed at InfoCom-2021 (Participant)

Supervised by *Linghe Kong*, in Advanced Network Lab

Sep 2019 – Mar 2021

- Research Intern
- Project about LoRa orthogonality, aimed at MobiCom-2021 (Core member)
- Paper about multi-modal learning, though marginally rejected by IJCAI-2021 (Core member)
- Paper about AI used in 5G policy, though rejected by TII-2020(IF>9) (Participant)
- Project: Reinforce the signal strength of LoRa using backscatter (Core member)
- Project: Defraud navigation apps using crowdsourcing method (Participant)

**Tencent**

Shenzhen, China

Department of Cloud Architecture Platform

July 2021 – Sep 2021

- Expected Engineer Intern
- Focus on Operating System.

## SELECTED PROJECT

**Unprivileged Container on HPC** *EI313: Sci and Tech Innovation*

Nov 2020 – Dec 2020

- Score:30/30, Rank:1/157
- I deployed a container using remote virtual machine to help the users control the resource without giving them root authority, which improves the security of the HPC.

**Advanced Linux Kernel** *CS356: Experiments on OS*

Mar 2020 – June 2020

- Score:100/100, Rank:1/157
- I designed and implemented an advanced OOM killer, which improved the performance of the Android linux kernel.

**MIPS CPU on FPGA** *CS145: Experiments on Arch*

Mar 2020 – May 2020

- Score:100/100, Rank:1/157
- I implemented a MIPS CPU with 5-stage pipeline using Verilog HDL.

**B Plus Tree** *CS158: Data Structure (Honor)*

June 2019

- Score:30/30, Rank:1/80
- I implemented a toy b-plus-tree using C++.

## SCHOLARSHIPS

- Fan Hsu-chi Principal Scholarship (1%)

2019, 2020

- Zhiyuan Honors Scholarship (5%) 2019, 2020
- Excellent Undergraduate Scholarship (15%) 2019, 2020

### **SKILLS**

**English:** CET4: 601, CET6: 561 (Passing Score: 425)

**Competent:** C and C++, Python, Java, Html & CSS

**Experiences:** Verilog, JavaScript, Matlab

### **EXTRACURRICULAR ACTIVITIES**

|  |                                  |                                  |
|--|----------------------------------|----------------------------------|
| President of Fan-scholar student union       | Member of the school soccer team | Academic Counselor of the school |
| Leader of Zhiyuan Engineering Honors Program | Volunteer of Xingran Class       |                                  |