Can compiler optimization be seen as a search problem?

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A bit about me

- MS in Computer Science
- Software Engineer with Hewlett-Packard (Distributed Systems)
- Software Engineer with Synopsys Inc (VLSI)
- **Interest:** Compilers ∩ Distributed Systems

Outline

This Presentation

- Intro to Optimization (20 min).
- Optimization and the search space (5 min).
- What is Superoptimization and Why do we care? (10 min).
- Stoke and Souper (10 min)

Optimization problem. Given a set X of candidate solutions, and a criterion map $F: X \to R$, find an $x \in X$ such that $x \in argmax_{v \in X}F(y)$

Eg,

- Domain: Set of stack locations assigned to variables
- **Codomain:** Non-interfering sets of registers that can be mapped to the stack locations.

Intro to Optimization Contd..

- F: Function that maps most number of registers to stack locations.
 - Register Allocation
 - ► Efficient Coloring, Dominance, LVN Based Liveness, DSE, etc

Stack vs Register

Stack

mov \$2, -10(%ebp)
pushl -10(%ebp)
call print

x = 2 print 2

Register

mov \$2, %eax
pushl %eax
call print

Coloring

- Every register gets a color
- Every stack location gets a color

Algorithm

- Construct a graph
- Insert all the variables as nodes in the graph
- Assign a color to each variable
- If there are more variables than registers, spill them over to stack.

Efficient Coloring

Handle spilling by reassigning the stack location to a register.

```
mov $2, -4(%ebp)
mov -4(%ebp), %eax
pushl %eax
call print
```

Coloring with interference

• Analyze liveness (on IR)

$$\textit{live}_{\textit{before}}(\textit{inst}) = (\textit{live}_{\textit{after}}(\textit{inst}) - \textit{write}(\textit{inst})) \cup \textit{read}(\textit{inst})$$

```
mov $2, x
mov $4, y
addl $7, x
print x
```

Coloring with interference

Analyze liveness (on IR)

$$\textit{live}_{\textit{before}}(\textit{inst}) = (\textit{live}_{\textit{after}}(\textit{inst}) - \textit{write}(\textit{inst})) \cup \textit{read}(\textit{inst})$$

```
{}
mov $2, x
\{x\}
mov $4, y
\{x, y\}
addl y, x
\{x\}
print x
{}
We know that x and y interfere!
```

VN Based Liveness

x = 2 y = 3 z = x + yw = x + y mov 2, x
mov 3, y
mov x, z
add y, z
mov x, w
add y, w

VN Based Liveness

```
x = 2

y = 3

z = x + y

w = x + y
```

```
{}
mov 2, x
\{x\}
mov 3, y
\{x, y\}
mov x, z
\{x, y, z\}
add y, z
\{x, y\}
mov x, w
\{y, w\}
add y, w
{}
```

VN Based Liveness

```
x (1) = 2 (1)
y (2) = 3 (2)
z (3) = x (1) + y (2)
w (?) = x (3) + y (2)
---> Look in the Hash Table
```

```
{}
mov 2 (1), x (1)
\{x\}
mov 3 (2), y (2)
\{x, y\}
mov x (1), z (1)
\{x, y, z\}
add y (2), z (3)
\{x, y\}
mov x (1), w (1)
\{y, w\}
add y (2), w (?)
----> Look in the Hash Table
```

Benefit: w does not have considered for register allocation.

```
x = 2

y = 3

z = x + y

w = x + y
```

```
{}
mov 2, x
\{x\}
mov 3, y
\{x, y\}
mov x, z
\{x, y, z\}
add y, z
\{x, y\}
mov x, w
\{y, w\}
add y, w
{}
```

```
x = 2

y = 3

z = x + y

w = x + y
```

```
mov 2, x
{x}
mov 3, y
\{x, y\}
mov x, z
\{x, y, z\}
add y, z
\{x\}
mov x, w
{}
```

$$x = 2$$

 $y = 3$
 $z = x + y$
 $w = x + y$

```
{}
mov 2, x
{x}
mov 3, y
{x, y}
mov x, z
{y, z}
add y, z
{}
```

$$x = 2$$

 $y = 3$
 $z = x + y$
 $w = x + y$

```
x = 2

y = 3

z = x + y

w = x + y
```

```
x = 2
y = 3
z = x + y
w = x + y
```



VN Based Liveness and DSE

```
y = 3
z = x + y
w = x + y
print w
print z
```

x = 2

This would completely change our DSE

If Statements, Loops, etc

- Basic Blocks
- CFG
- LVN
- Dominance

CFG and Basic Blocks

```
if input():
   foo()
else:
   baz()
```

- call input
- cmp %eax, \$1
- jne else
- then:
- call foo
- jmp endif
- else:
- call baz
- jmp endif

CFG and Basic Blocks

```
if input():
   foo()
else:
   baz()
```

Block 1:

- call input
- cmp %eax, \$1
- jne else

Block 2:

- then:
- call foo
- jmp endif

Block 3:

- else:
- call baz
- jmp endif

CFG and Basic Blocks

- mov 2, x
- mov 0, y
- while:
- cmp x, 12
- je endwhile
- mov x, y
- add 1, x
- mov x, y
- add 1, y
- jmp while
- endwhile:
- print x

What do we do?

- Advanced optimizations cannot be done in the above code because ?
- Solution: SSA + Dominance + Phi Explain Store, Definition, and SSA (Dominance is out of scope)

```
x = 2

y = 0

while x != 12:

x = y + 1

y = x + 1

print x

x = 2

y = 0

while x(?) != 12:

x = y (?) + 1

y = x + 1

print x(?)
```

What do we do?

```
x = 2
y = 0
while x != 12:
    x = y + 1
    y = x + 1
print x
```

```
x1 = 2
y1 = 0
while x(phi(x1, x2)) != 12:
    x2 = y(phi(y1, y2)) + 1
    y2 = x2 + 1
print x(phi(x1, x2))
```

Optimization and the search space

Thinking of optimization as a search problem?

- Pure search problems only need verification of the solution.
- Optimization problems need verification of the entire domain.

• Optimization problem as search problem

- What is the search space here?
 - ▶ The computational universe space of all possible programs

- Perturbation
 - ightharpoonup mul ightharpoonup shifts + movs + adds (depends upon context)
 - lacktriangle control flow ightarrow add + carry

- Correctness
 - ► Equivalence?

- Performance
 - ► Speed up?
 - ► Less Memory?

- Applicability
 - $\blacksquare \ \, \mathsf{X} (\mathsf{X} \mathsf{Y}) \to \mathsf{Y}$
 - $\quad \blacksquare \ \, (X @ 1) X \to X$

- Limitations
 - Size Verification, Compile Time
 - Complexity Control Flow

- Only works on Loop Free Code.
- Treats program optimization task as a cost minimization problem.

- Verification?
 - $\qquad \qquad \mathsf{C}(\mathsf{R};\,\mathsf{T})\colon\mathsf{Eq}(\mathsf{R};\,\mathsf{T})\,+\,\mathsf{Perf}(\mathsf{R};\,\mathsf{T})$

- Eq(.)
 - ► Z3: SAT Solver

- Perf(.)
 - MCMC: Metropolis Hasting

Stochastic Superoptimizer

```
# STOKE
   # gcc -03
   .L0:
                                  .L0:
     movq rsi, r9
                                    shlq 32, rcx
     mov1 ecx, ecx
                                    mov1 edx, edx
     shrq 32, rsi
                                    xorq rdx, rcx
     andl Oxffffffff, r9d
                                    movq rcx, rax
     movq rcx, rax
                                    mulq rsi
 9
     mov1 edx, edx
                                    addq r8, rdi
10
     imulq r9, rax
                               10
                                    adcq 0, rdx
11
     imulq rdx, r9
                               11
                                    addq rdi, rax
12
     imulq rsi, rdx
                               12
                                    adcq 0, rdx
13
     imulq rsi, rcx
                               13
                                    movq rdx, r8
14
     addq rdx, rax
                               14
                                    movq rax, rdi
15
     iae .L2
16
     movabsq 0x100000000, rdx
17
     addq rdx, rcx
18 .L2:
19
     movq rax, rsi
20
     movq rax, rdx
     shrq 32, rsi
21
22
     salq 32, rdx
23
     addq rsi, rcx
24
     addq r9, rdx
25
     adcq 0, rcx
26
     addq r8, rdx
27
     adcq 0, rcx
28
     addq rdi, rdx
```

29

adcq 0, rcx

Souper

First attempt at using superoptimizers in code generation.

- LLVM IR
- Phi Node exploitation
- $\bullet \ \mathsf{Redis} + \mathsf{Temp} \ \mathsf{KV} \ \mathsf{Store}$

Souper

- Straight line optimization (Deterministic) vs Novel Logic(Superoptimization)
- Path Dependence vs Path Linearity

Souper

Example Result

Future Interest

Transformers and Superoptimization Eg. Codex by OpenAI (Github Copilot)

QA