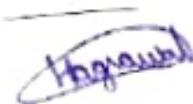


Declaration and statement of authorship

I, bearing Registration Number 106118036, agree and acknowledge that:

1. The assessment was answered by me as per the instructions applicable to each assessment, and that I have not resorted to any unfair means to deliberately improve my performance.
2. I have neither impersonated anyone, nor have I been impersonated by any person for the purpose of assessments.

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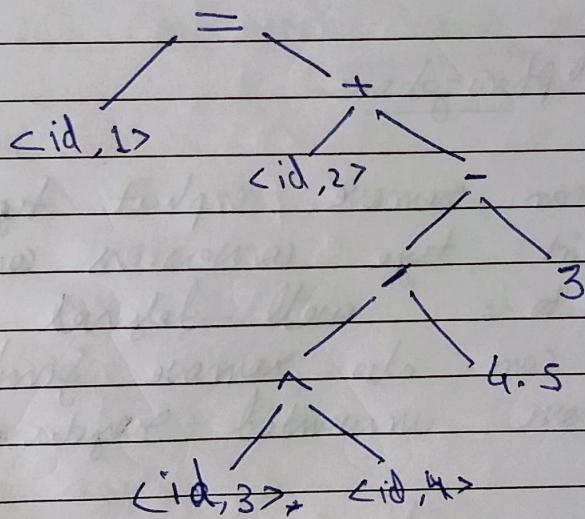
1) Statement :- $x = b + a^c / 4.5 - 3$
 $(\wedge \rightarrow \text{Power})$

A) Stage 1 :- Lexical Analysis

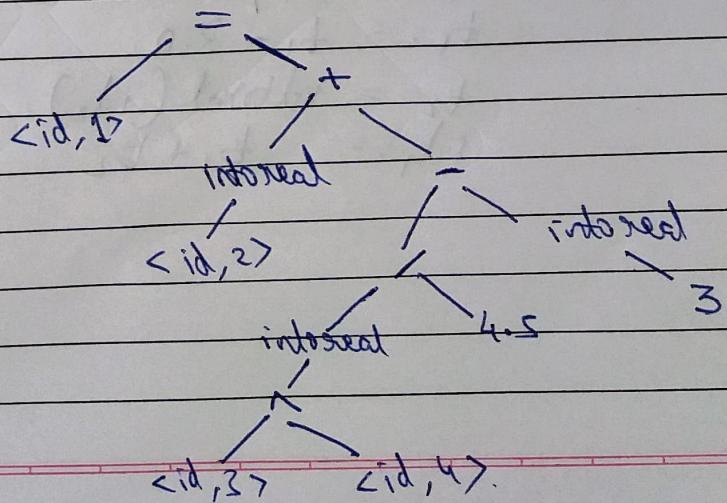
$\langle id, 1 \rangle \Leftrightarrow \langle id, 2 \rangle \langle + \rangle \langle id, 3 \rangle \langle \wedge \rangle \langle id, 4 \rangle$
 $\langle / \rangle \langle 4.5 \rangle \Leftrightarrow \langle - \rangle \langle 3 \rangle$

B) Syntax Analyzer:-

Generating Parse Tree



C) Semantic Analysis:-



D) I.C.6: We write the 3 address code.

$$\begin{aligned}
 t_1 &:= id_4 \\
 t_2 &:= id_3 \wedge t_1; \\
 t_3 &:= interval(t_2); \\
 t_4 &:= t_3 / 4.5; \\
 t_5 &:= interval(3); \\
 t_6 &:= t_4 - t_5; \\
 t_7 &:= interval(id_2); \\
 t_8 &:= t_7 + t_6; \\
 id_1 &:= t_8;
 \end{aligned}$$

E) Code Optimization:

- We can remove explicit type conversions as implicit type conversions can take place due to a well-defined 'n'.
- We can also remove final copy propagation.
- Remove unwanted registers or extra variables.

$$\begin{aligned}
 t_1 &:= id_4 \\
 t_2 &:= id_3 \wedge t_1 \\
 t_3 &:= t_1 / 4.5 && \text{[Implicit conversion happens]} \\
 t_4 &:= t_1 - 3.0 \\
 t_5 &:= interval(id_2) \\
 id_1 &:= t_3 + t_5
 \end{aligned}$$

(E) Code Generation:

LDF	R ₁ , id ₄ ;
PDW	id ₃ , R ₁ ;
DIV	#4.5, R ₁ ;
SUB	#3.0, R ₁ ;
LDF	R ₂ , id ₂ ;
ADD	R ₂ , R ₁ ;
STF	id ₁ , R ₁ ;

∴ id₁ get the value from R₁ at the
final result.

2) Lex program: Count Vowels in string.

Program:

-1. {

int vCount = 0;

1. }

1. 1.

[aeiouAEIOU] { vCount++; }

1. 1.

int yywrap() { }

int main()

{

printf("Input String: ");

getchar();

printf("Num Vowels: %d\n", vCount);

return 0;

}

3.)

Grammar:

$$\begin{aligned} S &\rightarrow BCD \mid a \\ A &\rightarrow CEF \mid aA \\ B &\rightarrow b \mid e \\ C &\rightarrow dB \mid G \\ D &\rightarrow cA \mid e \\ E &\rightarrow e \mid fE \end{aligned}$$

We can see that the grammar here is free from any left Recursion & left Factoring.

∴ We can write :

$$\begin{aligned} \text{first}(S) &= \{a\} \cup \text{first}(B) \cup \text{first}(C) \cup \text{first}(D) \\ &= \{a, b, c, d, e\} \end{aligned}$$

$$\begin{aligned} \text{first}(A) &= \{a\} \cup \text{first}(C) \cup \text{first}(E) \\ &= \{a, d, e, f, g\} \end{aligned}$$

$$\text{first}(B) = \{b, e\}$$

$$\text{first}(C) = \{d, e\}$$

$$\text{first}(D) = \{c, e\}$$

$$\text{first}(E) = \{e, f\}$$



Now, for follow, we get

$$\text{follow}(S) = \{\$\}$$

$$\begin{aligned}\text{follow}(A) &= \text{follow}(D) \\ &= \{\$\}\end{aligned}$$

$$\begin{aligned}\text{follow}(B) &= \text{first}(C) \cup \text{follow}(C) \cup \text{first}(D) \\ &\quad \cup \text{follow}(D) \\ &= \{d, c, \$\}\end{aligned}$$

$$\begin{aligned}\text{follow}(C) &= \{c\} \cup \text{follow}(S) \cup \text{first}(E) \\ &= \{c, f, e, \$\}\end{aligned}$$

$$\text{follow}(D) = \text{follow}(S) = \{\$\}$$

$$\begin{aligned}\text{follow}(E) &= \text{first}(B) \cup \text{follow}(S) \\ &= \{b, \$\}\end{aligned}$$

4)

Grammar

$$S' \rightarrow S$$

$$S \rightarrow L = E$$

$$E \rightarrow E + E$$

$$E \rightarrow (E)$$

$$E \rightarrow L$$

$$L \rightarrow Elist]$$

$$L \rightarrow id$$

$$Elist \rightarrow Elist, E$$

$$Elist \rightarrow id [E$$

So,

goto	State	Actions
	0	$\{ [S' \rightarrow S, \$], [S \rightarrow L = E, \$];$ $[L \rightarrow Elist], [=];$ $[L \rightarrow .id, =]; - - -$
goto(0, \$)	1	$\{ [S' \rightarrow S, \$] \}$ $\{ [S \rightarrow L = E, \$] \}$
goto(0, =)	2	$\{ [L \rightarrow Elist], [=] / \$ / + \} /$ $, / \} ; [Elist \rightarrow Elist,$ $E, V,] \}$
goto(0, Elist)	3	$\{ [L \rightarrow Elist], [=] / \$ / + \} /$ $, / \} ; [Elist \rightarrow Elist,$ $E, V,] \}$
goto(0, id)	4	$\{ [L \rightarrow id., =] / + / \} / , / \} ;$ $[Elist \rightarrow id. [E,], / ,] \}$
goto(2, =)	5	$\{ [S \rightarrow L = E, \$]; [E \rightarrow .E + E,$ $\$ / +];$
goto(3, =)	6	$\{ [L \rightarrow Elist], [=] / \$ / + / \} / \}$
goto(3, =)	7	$\{ [Elist \rightarrow Elist, .E,], / \}; - - -$
goto(4, C)	8	$- - - - -$
goto(5, =)	9	$- - - - -$

State	Output
get (S, c)	9
get (S, L)	10
get (S, Elit)	11
get (S, d)	3
get (7, E)	4
get (7, c)	12
get (7, L)	10
.	11
.	3
.	4
.	13

Following the NFA table, we get the LR table.

State	=	Action	()] id, [, \$	Get O.
0				S ₁ , S E L Elit
1				S ₄
2	S ₅			
3			S ₆ S ₇	
4	π ₆	π ₆	π ₆	π ₆
5				
6				
7	S ₁₀			
8				
9				
10	S ₁₀			
11				
12				
13				
14				
15				
16				
17				

5>

TAC for the given code can be written as:

$\text{low} = -2$ - - - Leader (B.)

$\text{high} = 100$

L1: 1) $\text{low} \geq \text{high}$ goto L7 - - - ~~B1~~ (B.)
 if $\text{flag} = 0$ - - - B2
 if $\text{low} > 1$ goto L2
 $\text{low} = \text{low} + 1$
 goto L1

L2: $i = 2$ - - - B4
 $t_1 = \text{low}/2$

L3: if $i > t_1$ goto LS - - - B5
 $t_2 = \text{low} \gamma i$ - - - B6
 if $t_2 \neq 0$ goto L4
 $\text{flag} = 1$ - - - B7
 goto LS

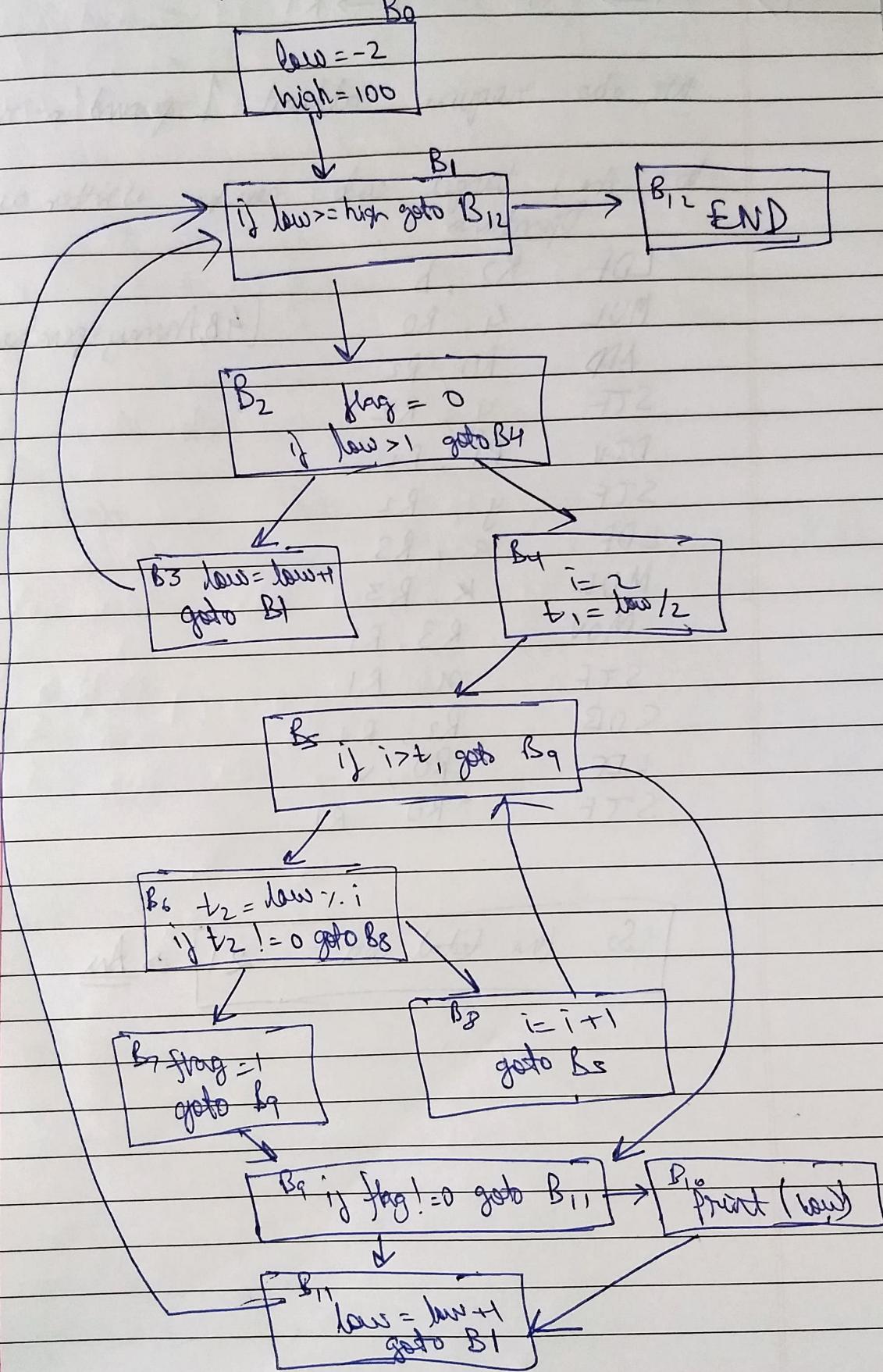
L4: $i = i + 1$ - - - B8
 goto L3

LS: if $\text{flag} \neq 0$ goto L6 - - - B9
 print (Low) - - - B10

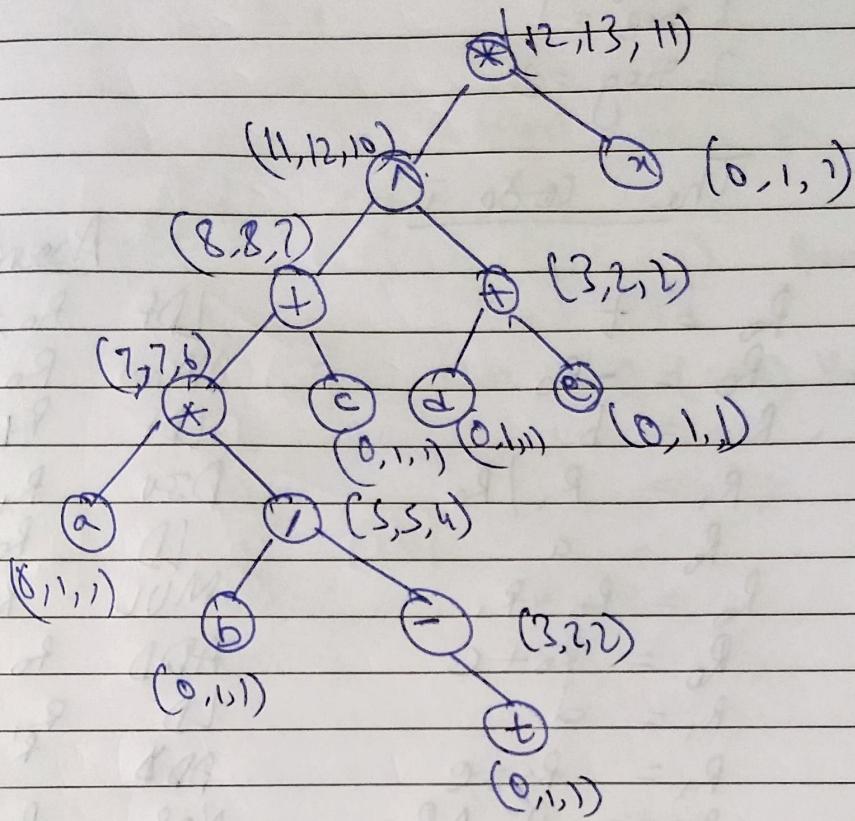
L6: $\text{low} = \text{low} + 1$ - - - B11
 goto L1

L7: END - - - B12

So, we get the flow control graph as follows:



6. > $(a+b)(-t) + c^r (2+t)^s * x$



→ For the '+' operator
 2 reg = min (8, 9, 7)
 = 7

$$\begin{aligned} 1 \text{ reg} &= 8 \\ 0 \text{ reg} &= 8 \end{aligned}$$

For the '^' operator
 2 reg = 0
 1 reg = 12
 0 reg = 11

For * operator :-
 2 reg = 11
 1 reg = 13
 0 reg = 12

for all leaf nodes,

$$0 \text{ reg} = 0$$

$$1 \text{ reg} = 1$$

$$2 \text{ reg} = 2$$

The code is:

$$\begin{aligned} R_0 &= t \\ R_0 &= -R_0 \\ R_1 &= b \\ R_1 &= R_1 / R_0 \\ R_0 &= a \\ R_0 &= R_0 + R_1 \\ R_0 &= R_0 + C \\ R_1 &= d \\ R_1 &= R_1 + C \\ R_0 &= R_0 \setminus R_1 \\ R_0 &= R_0 * x \end{aligned}$$

Assembly-

LDF	R ₀ , t
UMINW	R ₀ , R ₀
LDF	R ₁ , b
DIV	R ₁ , R ₀
LD	R ₀ , a
MUL	R ₀ , R ₀ , R ₁
ADD	R ₀ , R ₀ , C
LD	R ₁ , D
ADD	R ₁ , R ₁ , C
XOR	R ₀ , R ₀ , R ₁
MUL	R ₀ , R ₀ , X

7>

Basic Block:

$$t_1 := b + c$$

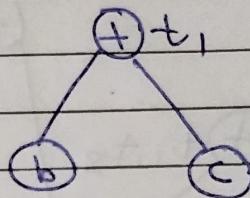
$$t_2 := c/d$$

$$t_3 := b + c$$

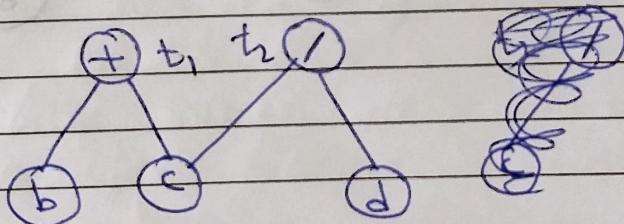
$$b := c/d$$

$$d := b + c$$

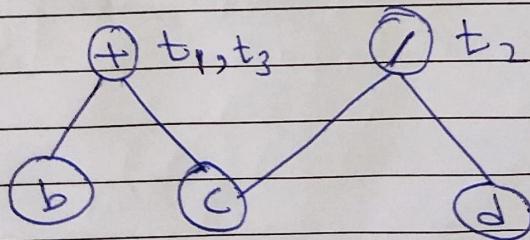
So, Step 1:- $t_1 := b + c$



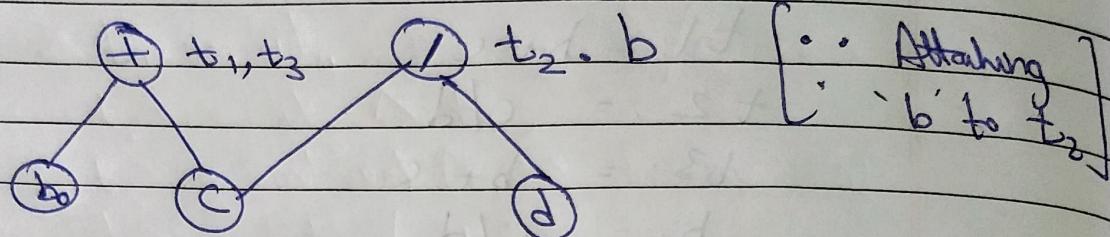
Step 2:- $t_2 := c/d$



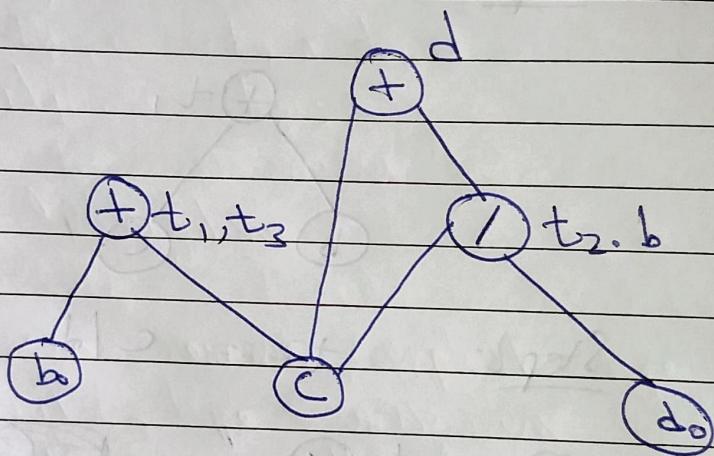
Step 3:- $t_3 := b + c$



Step 4:- $b := c / d$, c/d stored in t_2



Step 5:- $d := b + c$



8.7

3-AC in Quaduples Representation

$$z : a[m] \wedge (b^* - m + i) / c * (d \% e) - e$$

Here $\wedge \rightarrow$ Bitwise XOR.

So, TAC:-

$$t_1 = m;$$

$$t_2 = 4 * t_1;$$

$$t_3 = a[t_1];$$

$$t_4 = \text{unimm}(m);$$

$$t_5 = b^* + t_3;$$

$$t_6 = t_5 / c;$$

$$t_7 = e;$$

$$t_8 = d \% t_7;$$

$$t_9 = t_6 * t_8;$$

$$t_{10} = t_9 - \cancel{t_7} - t_7;$$

$$t_{11} = t_2 \text{ bitwise } t_{10};$$

$$z = t_{11};$$



9.) $i \rightarrow R_0$ $x \rightarrow R_1$

We also require atleast 1 operand in registers.

So, the target code can be written as:

<u>Operations</u>	<u>Cost</u>
LDF R_2, b	2
MUL $4, R_0$	[4B, Array operation] 2
ADD R_0, R_2	1
STF y, R_2	2
DIV R_1, R_2	1
STF y, R_2	2
LDF a, R_3	2
MUL k, R_3	2
MOV R_3, R_1	1
STF n, R_1	2
SUB R_2, R_1	1
LDF R_0, v	2
STF $*R_0, R_1$	1

So, the total cost = 21 \Rightarrow Ans

(10)

 $d_1 : j=1$ $d_2 : a=2$ $d_3 = i=m$ $d_4 = k=2$ while $i < j$ and $j < n$ or $k < n$ do $d_5 : c=1$ if ($c < j$) then $d_6 : n = n + b$ else if ($c > a$) then $d_7 : n = 2 + e;$ $d_8 : a = a + l;$

else

 $d_9 : n = c * a;$ $d_{10} : a = a - 1;$ $d_{11} : c = c + 1;$ $d_{12} : j = j + 1;$ $d_{13} : K = k * 2;$

end

 $d_{14} : a = n + n$

So, tree can be drawn as:

