



Selected Topics of Embedded Software Development 2

WS-2021/22

Prof. Dr. Martin Schramm

**Testing and generating Prime Numbers and Safe Primes
using CryptoCore**

Group 2 – Team 4

Rashed Al-Lahaseh – 00821573

Vikas Gunti - 12100861

Supriya Kajla – 12100592

Srijith Krishnan – 22107597

Wannakuwa Nadeesh – 22109097

Core RAM Structure

The RAM of the core must be capable of holding all the necessary operands and intermediate values required during the execution of cryptographic algorithms.

The basic structure of the described RAM is figured bellow.

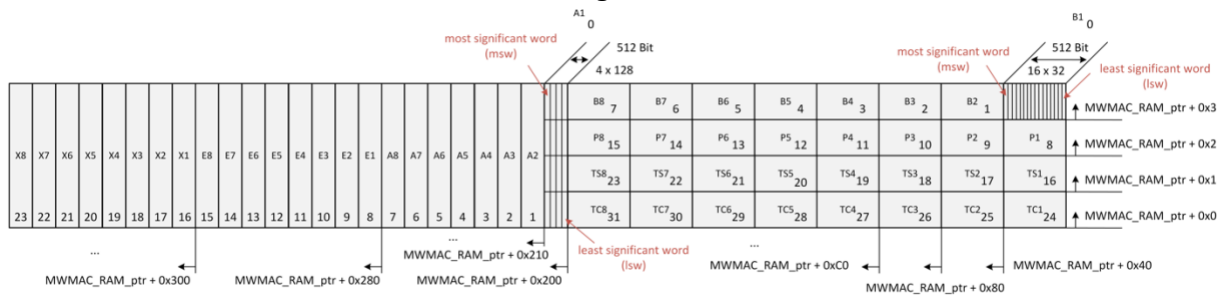


Figure 1: Montgomery Multiplication Core RAM Organization

It features four symbolic horizontal RAM operand locations with MAX_PRECISION_WIDTH bit each which are organized as eight pieces of MAX_PRECISION_WIDTH|8 bit each.

The location named **B** is intended to hold operand B in Montgomery Multiplication and Montgomery Exponentiation operations.

The location named **P** is intended to hold the modulus.

The location **TS** usually holds the temporary sum value during Montgomery Multiplications and Montgomery Exponentiation or the first operand in modular addition or subtraction operations.

The location **TC** usually holds the temporary carry stream during Montgomery Multiplications and Montgomery Exponentiation or the second operand in modular addition or subtraction operations.

Besides the horizontal RAM operand locations three symbolic vertical RAM operand locations with MAX_PRECISION_WIDTH bit each have been defined which are organized as eight pieces of MAX_PRECISION_WIDTH|8 bit each.

The locations named **A**, **E** and **X** for convenience usually are used to hold operand A in Montgomery Multiplication and Montgomery Exponentiation operations as well as the exponent operand **E** and the auxiliary operand **X** in Montgomery Exponentiation operations. In addition, all RAM slots are intended to hold intermediate values during the execution of cryptographic algorithms.

Implementation on CryptoCore

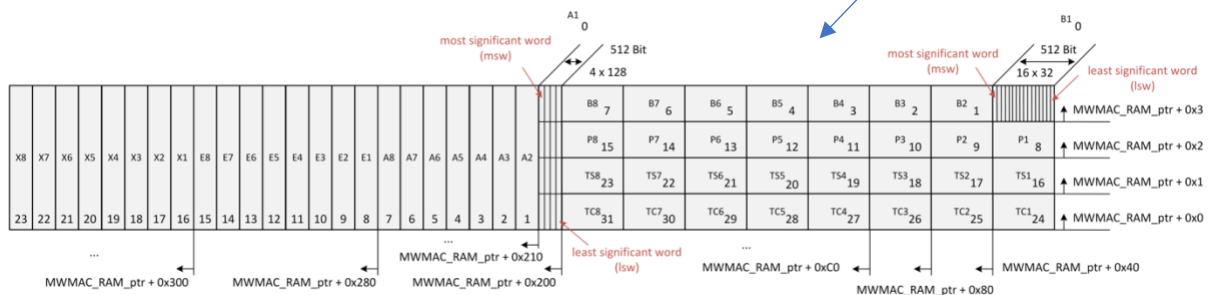
```

6  def miller_rabin(n, k = 40, isMontgomery = False):
7
8      s = 0
9      d = n-1
10     prec = n.nbits()
11     r = (2^prec) % n
12     rin = inverse_mod(r,n)
13
14     while d & 1 == 0:
15         d = d >> 1
16         s += 1
17
18     for _ in range(k):
19         a = random.randrange(2, n-2)
20         if isMontgomery:
21             temporary = Prime_MontExp(a,d,n,prec,r,rin)
22             if temporary == r or temporary == n-r:
23                 continue
24             for _ in range(s - 1):
25                 temporary = (temporary * temporary) % n
26                 if temporary == n - 1:
27                     break
28         else:
29             return False
30     else:
31         temporary = Prime_ModExp(a,d,n,prec)
32         if temporary == 1 or temporary == n-1:
33             continue
34         for _ in range(s - 1):
35             temporary = (temporary * temporary) % n
36             if temporary == n - 1:
37                 break
38     else:
39         return False
40     return True

```

So, after understanding the RAM management in the current application we are going to talk about, how we are going to implement the current code (MRT in Montgomery domain) in CryptoCore.

Following figure given an overview of required offsets in order to access specific memory addresses, and exemplified highlights the definition of least significant word (lsw) and most significant word (msw).



Dive into the /driver file

Part 1

```

6 def miller_rabin(n, k = 40, isMontgomery = False):
7
8     s = 0
9     d = n-1
10    prec = n.nbits()
11    r = (2^prec) % n
12    rinvs = inverse_mod(r,n)
13
14    while d & 1 == 0:
15        d = d >> 1
16        s += 1
17
18    for _ in range(k):
19        a = random.randrange(2, n-2)
20        if isMontgomery:
21            temporary = Prime_MontExp(a,d,n,prec,r,rinvs)
22            if temporary == r or temporary == n-r:
23                continue
24            for _ in range(s - 1):
25                temporary = (temporary * temporary) % n
26                if temporary == n - 1:
27                    break
28            else:
29                return False
30        else:
31            temporary = Prime_ModExp(a,d,n,prec)
32            if temporary == 1 or temporary == n-1:
33                continue
34            for _ in range(s - 1):
35                temporary = (temporary * temporary) % n
36                if temporary == n - 1:
37                    break
38            else:
39                return False
40    return True

```

In this algorithm, we have one main for loop and before the for loop there are some mathematical calculations like shifting the input (n) and calculate (d) and (s).

In addition we calculate the (r) and ($n-r$) that we use them a lot in the algorithm.

In addition we calculate the (r) and $(n-r)$ that we use them a lot in the algorithm.

Coming from an empty or rather unknown memory content.

X8	X7	X6	X5	X4	X3	X2	X1	E8	E7	E6	E5	E4	E3	E2	E1	A8	A7	A6	A5	A4	A3	A2	A1	B8 7	B7 6	B6 5	B5 4	B4 3	B3 2	B2 1	B1 0
																								P8 15	P7 14	P6 13	P5 12	P4 11	P3 10	P2 9	P1 8
																								T8 23	T7 22	T6 21	T5 20	T4 19	T3 18	T2 17	T1 16
																								C8 31	C7 30	C6 29	C5 28	C4 27	C3 26	C2 25	C1 24

