

# **Quick Setup Example on AIK-RA4E1 Solution Kit**

Renesas Advanced (RA) Family - RA4 Series

### **Description**

Welcome to Quick Setup Example for Renesas RA using AIK-RA4E1 Solution Kit! The objective of this workshop is to build a basic Renesas RA application utilizing Renesas tools.

You will start by setting up the display with the basic operations project. The application used in this lab is built to run on AIK-RA4E1 Solution Kit. A foundation Display project will be created from scratch and populated with several HAL drivers provided by the Flexible Software Package (FSP). Accelerometer and Ethernet demo projects are also added.

Objectives	Prerequisites  Renesas AIK-RA4E1 VUI Solution Kit  Renesas Flexible Software Package 5.0.0 platform installation, which includes:  e² studio 2023-10 or newer  FSP 5.0.0 or newer  GCC Arm Embedded 10.3.1  PC running Windows 10 64-bit with at least one USB port.
Skill Level  Basic familiarity with embedded electronics  Basic understanding of C language  Understanding of how to import projects into e <sup>2</sup> studio (optional – for use with ready checkpoint projects).	Time • 2 hours to complete

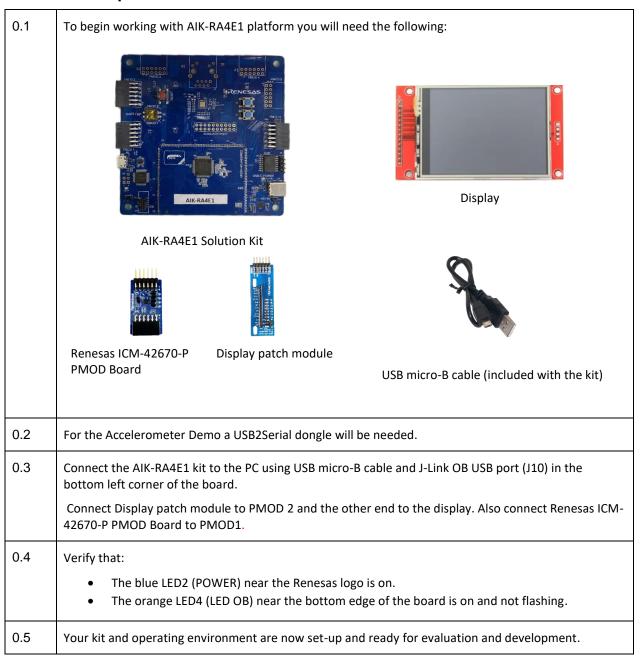
## **Workshop Sections**

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## 0 Setting up the hardware

## **Procedural Steps**



#### **END OF SECTION**

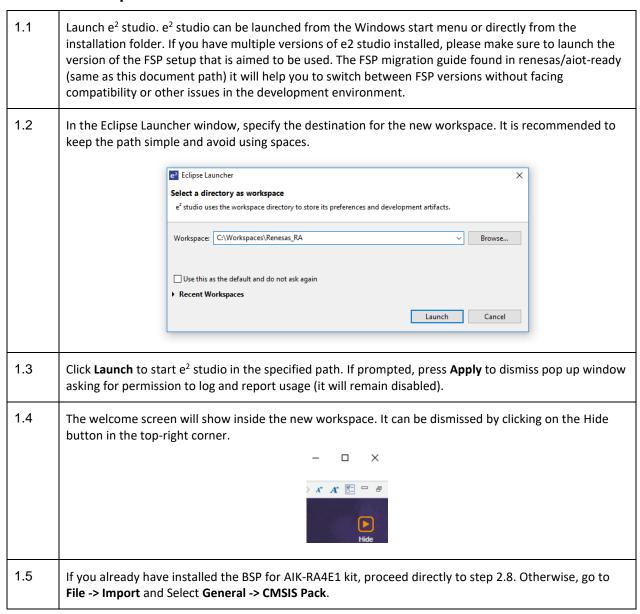


## 1 Implementing Display with the basic operations demo

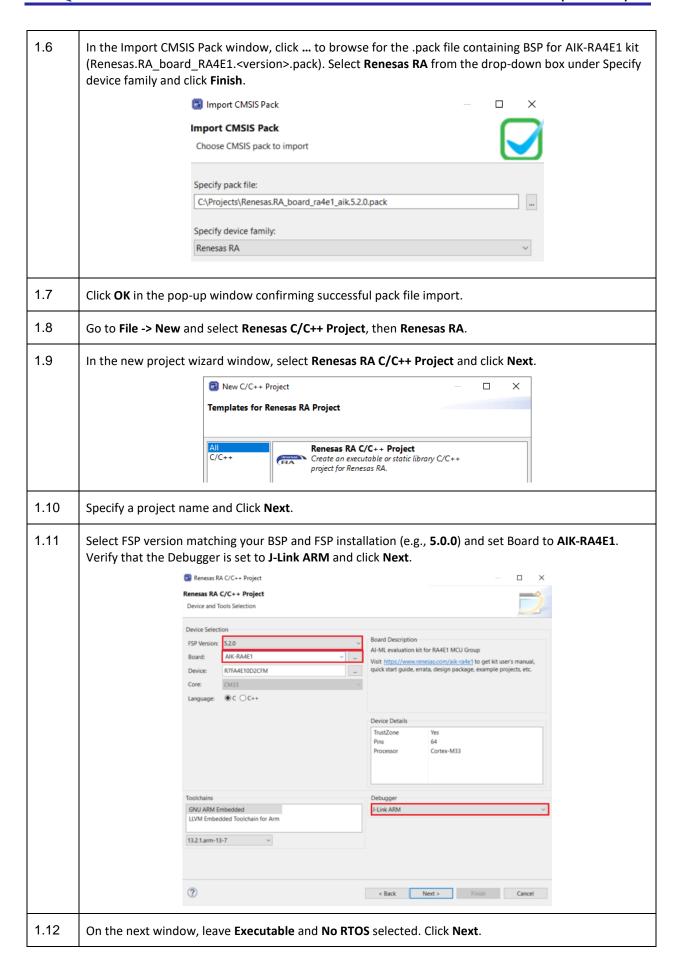
#### Overview

Following section describes in details steps required to create an e<sup>2</sup> studio workspace and set up a Display with basic operations-based project for AIK RA4E1 kit.

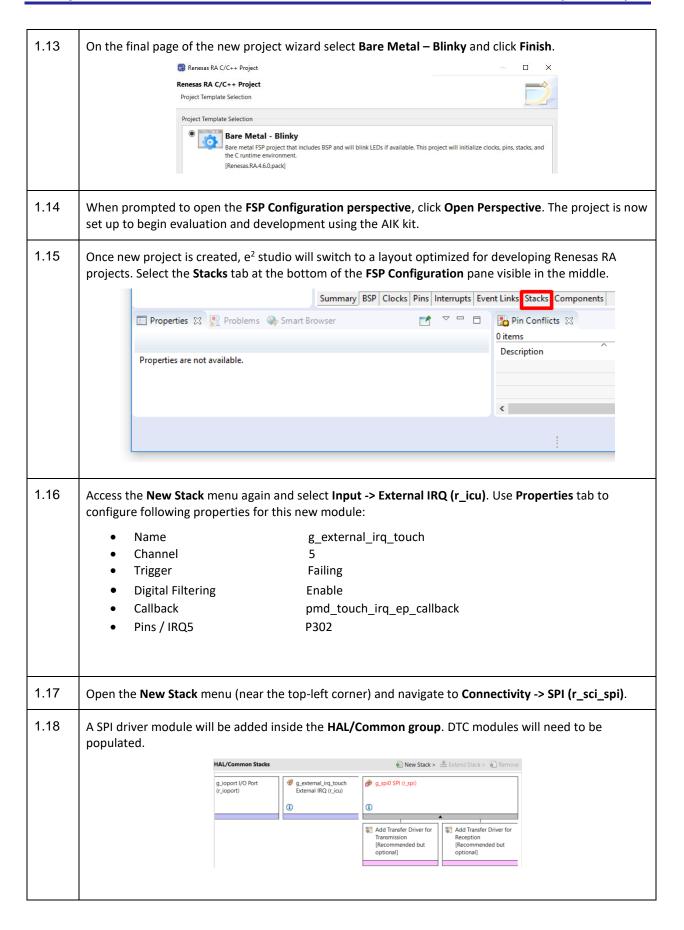
### **Procedural Steps**













1.19 Click on g\_spi0 SPI (r\_sci\_spi), go to the Properties tab and apply the following settings. You may need to expand the chevrons to access all of the properties: Name g\_spi\_pmod1 Channel Receive interrupt Priority Priority 2 Transmit buffer Empty Interupt Priority Priority 2 **Transmit Complete Interupt Priority** Priority 2 **Error Interupt Priority** Priority 2 **Operating Mode** Master Callback spi\_pmod1\_callback **Bitrate** 15000000 1.20 Open the New Stack menu (near the top-left corner) and navigate to Connectivity -> SPI (r spi). A second SPI driver module will be added inside the HAL/Common group. 1.21 HAL/Common Stacks New Stack > ♣ Extend Stack > ♠ Remove # g\_spi\_pmod1 SPI (r\_sci\_spi) g\_spi\_pmod2 SPI (r\_sci\_spi) g\_transfer0 Transfer (r\_dtc) SCI0 TXI g\_transfer1 Transfer
 (r\_dtc) SCI0 RXI (Received) # g\_transfer2 Transfer (r\_dtc) SCI3 TXI 1.22 Click on g\_spi0 SPI (r\_spi), go to the Properties tab and apply the following settings. You may need to expand the chevrons to access all of the properties: Name g\_spi\_pmod2 Channel 3 **Receive interrupt Priority** Priority 2 Transmit buffer Empty Interupt Priority Priority 2 **Transmit Complete Interupt Priority** Priority 2 **Error Interupt Priority** Priority 2 sci\_spi\_pmod2\_callback Callback Bitrate 15000000 1.23 RA Configuration for this section is complete. Apply changes to the project source by clicking the Generate Project Content button in the top-right corner of the Configurator window. When prompted to Proceed with save and generate, tick the box next to Always save and generate without asking and click Proceed. 0 Generate Project Conten New Stack > Extend Stack > Remove 1.24 The FSP Configurator will extract all the necessary drivers and generate the code based on the configuration provided in the Properties tab.



1.25	In the <b>Project Explorer</b> pane, expand the <b>src</b> folder in the project and add the following folders and files than can be found in the demo folder:	
	PMD_FONT	
	PMD_TFT	
	SEGGER_RTT	
	• common_utils.h	
	• pictures.h	
1.26	In the <b>Project Explorer</b> pane, expand the <b>src</b> folder in the project and open <b>hal_entry.c</b> .	
	✓ ﷺ src  > → PMD_FONT	
	> 🇁 PMD_TFT	
	>	
	> a hal_entry.c	
	> h pictures.h	
1.27	hal_entry.c contains user application entry point (hal_entry function) for RTOS-less projects. The R_BSP_WarmStart callback is provided for the user to specify additional functions to be called during the FSP initialization sequence (e.g., pin configuration).	
1.28	Add #include statement to include common_utils.h, PMD_TFT/pmd_tft.h, PMD_TFT/pmd_text.h, pictures.h near the top of the file.	
1.29	Add static uint16_t buffer_rgb565[RENESAS_110_17_WIDTH*RENESAS_110_17_HEIGHT + (RENESAS_110_17_WIDTH*RENESAS_110_17_HEIGHT +1)/2 ]; after the includes.  After extern bsp_leds_t g_bsp_leds; add uint16_t act_state=0; uint16_t tft_ori_cnt=0;	
1.30	hal_entry.c can be used to exercise API of the various modules configured inside FSP Configurator using Developer Assist or by writing code manually.	
	Following code can be used to completely replace contents of hal_entry.c to perform basic operations using the display for the AIK-RA4E1 board:	
	#include "hal_data.h"	
	// START USER include	
	//some definitions for RTT #include "common utils.h"	
	//some definitions for TFT #include "PMD TFT/pmd tft.h"	
	#include PMD_TFT/pmd_ttt.n  #include "PMD_TFT/pmd_text.h"  // END USER include	
	//picture input by array #include "pictures.h" //	
	static uint16_t buffer_rgb565[RENESAS_110_17_WIDTH*RENESAS_110_17_HEIGHT +	
	<pre>extern bsp_leds_t g_bsp_leds; uint16_t act_state=0; uint16_t tft_ori_cnt=0;</pre>	
	// End user	



```
FSP CPP HEADER
void R_BSP_WarmStart(bsp_warm_start_event_t event);
FSP_CPP_FOOTER
****************//**
* @brief Blinky example application
* Blinks all leds at a rate of 1 second using the software delay function provided by the BSP.
********************************
void hal_entry (void)
#if BSP_TZ_SECURE_BUILD
   /* Enter non-secure code */
   R_BSP_NonSecureEnter();
#endif
   /* Define the units to be used with the software delay function */
   const bsp_delay_units_t bsp_delay_units = BSP_DELAY_UNITS_MILLISECONDS;
   /* Set the blink frequency (must be <= bsp_delay_units */</pre>
   const uint32_t freq_in_hz = 2;
   /* Calculate the delay in terms of bsp_delay_units */
   const uint32_t delay = bsp_delay_units / freq_in_hz;
   /* LED type structure */
   bsp_leds_t leds = g_bsp_leds;
   /* If this board has no LEDs then trap here */
   if (0 == leds.led_count)
       while (1)
       {
                                   // There are no LEDs on this board
   }
   /* Holds level to set for pins */
   bsp_io_level_t pin_level = BSP_IO_LEVEL_LOW;
   // START USER include
   /* set the TFT orientation */
   tft_set_ori_set (TFT_R90);
   /* init the TFT */
   tft_configure ();
   /* draw some shapes */
   tft_set_draw_color(0xff0000);
   tft_draw_rect((int16_t)20,(int16_t)20,
                (int16_t)(tft_get_act_width()/2-1),(int16_t)(tft_get_act_height()/2-1));
   tft_set_draw_color(0x00ff00);
   tft_draw_rect((int16_t)(tft_get_act_width()/2), (int16_t)(tft_get_act_height()/2),
                (int16_t)(tft_get_act_width()-1-20),(int16_t)(tft_get_act_height()-1-20));
   // START USER include
   pmd_text_init();
restart_demo:
   act_state = 0 ;
   tft_ori_cnt = 0;
   while (1)
       /* Enable access to the PFS registers. If using r_ioport module then register
protection is automatically
        * handled. This code uses BSP IO functions to show how it is used.
```



```
R BSP PinAccessEnable();
                   /* Update all board LEDs */
                  for (uint32_t i = 0; i < leds.led_count; i++)</pre>
                             /* Get pin to toggle */
                            uint32_t pin = leds.p_leds[i];
                             /* Write to this pin */
                            R_BSP_PinWrite((bsp_io_port_pin_t) pin, pin_level);
                  }
                   /* Protect PFS registers */
                  R_BSP_PinAccessDisable();
                   /* Toggle level for next write */
                  if (BSP_IO_LEVEL_LOW == pin_level)
                            pin_level = BSP_IO_LEVEL_HIGH;
                   }
                   else
                   {
                            pin_level = BSP_IO_LEVEL_LOW;
                   /* check if the touch screen calibration screen should be opened */
                   uint16_t loop_wait = 20;
                   while (((R_BSP_PinRead\ (USR_BTN2_S2) == false) \mid |\ (R_BSP_PinRead\ (USR_BTN1_S3) == false) \mid |\ (R_BSP_PinR
false)) && loop_wait )
                             R_BSP_SoftwareDelay (20, BSP_DELAY_UNITS_MILLISECONDS);
                            if (loop_wait)
                                      loop_wait--;
                   if (loop_wait == 0)
                            tft_touch_calibrate_rot_all ();
                            goto restart_demo;
                   /* end touch screen calibration */
                   /* some output on the Display */
                  //act_state=0;
                   switch (act state++)
                   {
                            case 0:
                                     if (tft_ori_cnt > 3)
                                                tft_ori_cnt = 0;
                                     if (tft_ori_cnt == 0)
                                                tft_set_ori (TFT_R0);
                                      if (tft_ori_cnt == 1)
                                               tft_set_ori (TFT_R90);
                                      if (tft_ori_cnt == 2)
                                                tft_set_ori (TFT_R180);
                                      if (tft_ori_cnt == 3)
                                               tft_set_ori (TFT_R270);
                                      //clear the screen
                                     tft_cls(CLS_COLOR);
                                      /* draw some shapes */
                                     tft_set_draw_color(0x0000ff);
                                     tft_draw_rect(5,5, 15,15); //This top left corner
                                     tft_set_draw_color(0xff0000);
                                     tft_draw_rect(20,20,
                                                                        (int16_t)(tft_get_act_width()/2-1),
                                                                        (int16_t)(tft_get_act_height()/2-1));
                                     tft_set_draw_color(0x00ff00);
                                     tft_draw_rect((int16_t)(tft_get_act_width()/2),
                                                                        (int16_t)(tft_get_act_height()/2),
                                                                        (int16_t)(tft_get_act_width()-1-20)
                                                                        (int16_t)(tft_get_act_height()-1-20));
```



```
pmd_text_set_b_color(0x0000ff00); // A - is 0 keep background
                pmd text set f color(0xff0000ff); // A - is 255 use foreground color blue
                pmd_text_set_font(1) ;
                pmd_text_set_rotation(TFT_TXT_R0);
                if (tft_ori_cnt == 0)
                    pmd_draw_string("TFT_R0",20,3);
                if (tft_ori_cnt == 1)
                    pmd_draw_string("TFT_R90",20,3);
                if (tft_ori_cnt == 2)
                    pmd_draw_string("TFT_R180",20,3);
                if (tft ori cnt == 3)
                    pmd_draw_string("TFT_R270",20,3);
                tft_ori_cnt++;
                break;
                // output a RGB565 picture at x(centered horizontal) y(bottom in vertical)
                // background of picture has color 0x00000 we want to keep the LCD background
so alpha -is 0x00
                // --> color = 0x00000000u
                // foreground of picture we want to blend E7
                // foreground RGB is set to 0x00000 (no use)
                // --> color = 0xE7000000u
                tft_blit_copy_blend ((uint16_t*) buffer_rgb565, (uint16_t*)
picture_renesas_110_17_rgb565,
                                      (int16_t) ((tft_get_act_width () - RENESAS_110_17_WIDTH) /
2),
                                      (int16_t) ((tft_get_act_height () - RENESAS_110_17_HEIGHT
- 1)),
                                      RENESAS_110_17_WIDTH,
                                      RENESAS 110_17_HEIGHT,
                                      0x00000000u, // BG color is RGB 0x000000 and alpha will
be 0x00 so keep background on LCD
                                      0xE7000000u); // FG color is unused only alpha channel
will be used for blend
            break:
            case 4:
                pmd_text_set_rotation(TFT_TXT_R0);
                pmd\_text\_set\_b\_color(0x00000ff00); \ // \ A \ - \ is \ 0 \ keep \ background
                pmd_text_set_f_color(0xff000000); //black
pmd_text_set_font(1);
                pmd_draw_string("Hey\f
                                          Renesas\r\n",
                                 (int16 t)(tft get act width()/2),
                                 (int16_t)(tft_get_act_height()/2));
                pmd_text_set_font(0);
                pmd_draw_string(" TFT_TXT_R0 \r\n",
                                 pmd_text_get_cursor_x(),
                                                                 // please take care for correct
offsets to screen and text start point
                                 pmd_text_get_cursor_y() + 10); // please take care for correct
offsets to screen and text start point
                break;
            case 6:
                pmd_text_set_rotation(TFT_TXT_R90);
                pmd_text_set_b_color(0x38eff0ef); // A lets'use some alpha blending
                pmd_text_set_f_color(0xff0000bf); //blue ARGB
                pmd_text_set_font(1) ;
                pmd_draw_string("Hey\f
                                          Renesas\r\n",
                                 (int16_t)(tft_get_act_width()/2),
                                 (int16_t)(tft_get_act_height()/2));
                pmd_text_set_font(0);
pmd_draw_string(" TFT_TXT_R90 \r\n",
                                 pmd_text_get_cursor_x() + 10 , // please take care for correct
offsets to screen and text start point
                                                               // please take care for correct
                                 pmd_text_get_cursor_y());
offsets to screen and text start point
                break:
                pmd_text_set_rotation(TFT_TXT_R180);
                pmd_text_set_b_color(0x00000ff00); // A - is 0 keep background
                pmd_text_set_f_color(0xfffffffff); //white ARGB
```



```
pmd_text_set_font(1) ;
                pmd_draw_string("Hey\f
                                          Renesas\r\n".
                                 (int16_t)(tft_get_act_width()/2),
                                 (int16_t)(tft_get_act_height()/2));
                pmd_text_set_font(0) ;
                pmd_draw_string(" TFT_TXT_R180 \r\n",
                                 pmd_text_get_cursor_x() ,
                                                                  // please take care for correct
offsets to screen and text start point
                                 pmd_text_get_cursor_y() -10 ); // please take care for correct
offsets to screen and text start point
                break;
            case 10:
                pmd_text_set_rotation(TFT_TXT_R270);
                pmd_text_set_b_color(0x0000ff00); // A - is 0 keep background
                pmd_text_set_f_color(0xff0000ff); //blue ARGB
                pmd_text_set_font(1) ;
                pmd_draw_string("Hey\f
                                          Renesas\r\n",
                                 (int16_t)(tft_get_act_width()/2),
                                 (int16_t)(tft_get_act_height()/2));
                pmd_text_set_font(0) ;
                pmd_draw_string(" TFT_TXT_R270 \r\n",
                                 pmd_text_get_cursor_x() -10, // please take care for correct
offsets to screen and text start point
                                 pmd_text_get_cursor_y() ); // please take care for correct
offsets to screen and text start point
                break;
            case 12:
                tft_set_draw_color (0x8f8f8f);
                tft_draw_rect (20, 20, (int16_t) (tft_get_act_width () - 1 - 20),
                                (int16_t) (tft_get_act_height () - 1 - 20));
            break;
            case 13:
            {
                int16_t xt = 30;
                int16_t x = (int16_t) ((tft_get_act_width () - 0 ) / 2 + 25 );
                int16_t y2 = 40;
                pmd_text_set_rotation(TFT_TXT_R0);
                pmd\_text\_set\_b\_color(0x00000ff00); \ // \ A \ - \ is \ 0 \ keep \ background
                pmd_text_set_f_color(0xffb0ffb0); // ARGB
pmd_text_set_font(1);
                pmd_draw_string("circle",
                                 xt,
                                 (int16_t)(y2-pmd_font_get_height()/2));
                tft_set_draw_color(0x2020ff);
                tft_draw_circle(x + 6, y2, 11);
                tft_draw_circle(x + 6, y2, 9);
            }
                break ;
            case 14:
                int16_t xt = 30;
                int16_t x = (int16_t) ((tft_get_act_width () - 0 ) / 2 + 25 );
                int16_t y3 = 70;
                pmd_text_set_rotation(TFT_TXT_R0);
                pmd\_text\_set\_b\_color(0x00000ff00); \ // \ A \ - \ is \ 0 \ keep \ background
                pmd_text_set_f_color(0xffb0ffb0); // ARGB
                pmd_text_set_font(1) ;
                // circle does not support line width
                pmd_draw_string("filled circle",
                                 xt,
                                 (int16_t)(y3-pmd_font_get_height()/2));
                tft_set_draw_color(0x2020ff);
                tft_draw_filled_circle(x + 6, y3, 11);
            }
                break;
            case 15:
```



```
int16 t xt = 30;
    int16_t x = (int16_t) ((tft_get_act_width () - 0 ) / 2 + 25);
    int16_t y1 = 100 - 26/2;
    pmd_text_set_rotation(TFT_TXT_R0);
    pmd_text_set_b_color(0x0000ff00); // A - is 0 keep background
    pmd_text_set_f_color(0xffb0ffb0); // ARGB
    pmd_text_set_font(1) ;
    pmd_draw_string("frame",
                    (int16_t)(y1-pmd_font_get_height()/2+13));
    tft_set_draw_color(0x2020ff);
    pmd_set_linesize (1);
    pmd_draw_frame(x, y1, 60, 26);
    break;
case 16:
    int16_t xt = 30;
    int16_t x = (int16_t) ((tft_get_act_width () - 0 ) / 2 + 25);
    int16_t y2 = 130 - 26/2;
    pmd_text_set_rotation(TFT_TXT_R0);
    pmd_text_set_b_color(0x0000ff00); // A - is 0 keep background
    pmd_text_set_f_color(0xffb0ffb0); // ARGB
    pmd_text_set_font(1) ;
    pmd_draw_string("text frame",
                     (int16_t)(y2-pmd_font_get_height()/2+13));
    pmd_set_linesize (3);
    pmd_text_set_font(0);
    pmd_text_set_b_color(0xff0000Af); // overwrite background
    pmd_text_set_f_color(0xfffffffff); // ARGB
    pmd_draw_text_frame("IN_BOX\f42", x, y2, 60, 26);
    }
    break;
case 17:
    int16_t xt = 30;
    int16_t x = (int16_t) ((tft_get_act_width () - 0) / 2 + 25);
    int16_t y3 = 160 + 10;
    pmd_text_set_rotation (TFT_TXT_R0);
    \label{eq:pmd_text_set_b_color} \verb"pmd_text_set_b_color" (0x00000ff00); // A - is 0 keep background
    pmd_text_set_f_color (0xffb0ffb0); // ARGB
    pmd_text_set_font (1);
    pmd_draw_string ("line", xt, (int16_t)(y3 - pmd_font_get_height () / 2 + 0) );
    tft_draw_line (0 + x, 20 + y3, 10 + x, 0 + y3);
    tft_draw_line (0 + x, 20 + y3, -10 + x, 0 + y3);
    tft_draw_line (-10 + x, 0 + y3, 0 + x, -20 + y3);
    tft_draw_line (+10 + x, 0 + y3, 0 + x, -20 + y3);
    tft_draw_v_line (x + 20, y3 - 10, y3 + 10, 3);
    tft_draw_h_line (x + 30, y3, x + 30 + 20, 3);
    break;
```



```
case 18:
                int16 t xt = 30;
                int16_t x = (int16_t) ((tft_get_act_width () - 0) / 2 + 25);
                int16_t x2 = x;
                int16_t y4 = 190 + 10;
                if ( tft_get_ori() == TFT_R0 || tft_get_ori() == TFT_R180)
                pmd_text_set_rotation (TFT_TXT_R0);
                pmd_text_set_b_color (0x0000ff00); // A - is 0 keep background
                pmd_text_set_f_color (0xffb0ffb0); // ARGB
                pmd_text_set_font (1);
                pmd_draw_string ("picture", xt, (int16_t)(y4 + (RENESAS_110_17_HEIGHT -
pmd_font_get_height ()) / 2 + 0) );
                // output a RGB565 picture at x(centered horizontal) y(bottom in vertical)
                // background of picture has color 0x000000 we want to keep the LCD background
so alpha -is 0x00
                // --> color = 0x00000000u
                // foreground of picture we want to blend E7
                // foreground RGB is set to 0x00000 (no use)
                // --> color = 0xE7000000u
                tft_blit_copy_blend ((uint16_t*) buffer_rgb565, (uint16_t*)
picture_renesas_110_17_rgb565,
                                      x2,
                                      y4,
                                      RENESAS_110_17_WIDTH,
                                      RENESAS 110 17 HEIGHT,
                                      0x00000000u, // BG color is RGB 0x000000 and alpha will
be 0x00 so keep background on LCD
                                      0xff000000u); // FG color is unused only alpha channel
will be used for blend
                }
                break ;
            case 23:
                int16_t keep_out_y ;
                uint32 t timeout = 0 ;
                int16\_t \ key\_cls\_x = (int16\_t)(tft\_get\_act\_width() - (pmd\_font\_get\_width()*3+4)
- 5);
                int16 t key cls v = 8;
                uint16_t key_cls_w = (uint16_t)(pmd_font_get_width()*(uint16_t)3 +(uint16_t)2)
;
                uint16_t key_cls_h = pmd_font_get_height() +(uint16_t)2;
                bool key_cls_in_window=false ;
                bool key_red_in_window=false ;
                bool key_green_in_window=false ;
                bool key_blue_in_window=false ;
                uint16_t key_cls_in_window_timeout = 0 ;
                uint16_t key_red_in_window_timeout=0
                uint16_t key_green_in_window_timeout=0 ;
                uint16_t key_blue_in_window_timeout=0 ;
                restart_paint: // a cls key will retrigger the drawing routine
                //clear the screen
                tft_cls (0xffffff);
                /* draw some shapes */
                tft_set_draw_color (0x0000ff);
                pmd_text_set_b_color (0x0000ff00); // A - is 0 keep background
pmd_text_set_f_color (0xff0000ff); // A - is 255 use foreground color blue
                pmd_text_set_font (1);
                pmd_text_set_rotation (TFT_TXT_R0);
                pmd_draw_string ("TOUCH PAINT\r\n", 5, 7);
                keep_out_y = pmd_text_get_cursor_y () + 2;
                pmd_set_linesize (1);
                pmd_draw_text_frame ("CLS", key_cls_x, key_cls_y, key_cls_w, key_cls_h);
```



```
tft set draw color (0x0000ff);
                tft_draw_filled_circle ((int16_t)(key_cls_x - 1 * (pmd_font_get_height () +
12)),
                                         (int16_t)(key_cls_y + pmd_font_get_height () / 2),
                                        pmd_font_get_height () / 2 + 1);
                tft_set_draw_color (0x00ff00);
                tft_draw_filled_circle ((int16_t)(key_cls_x - 2 * (pmd_font_get_height () +
12)),
                                         (int16_t)(key_cls_y + pmd_font_get_height () / 2),
                                        pmd_font_get_height () / 2 + 1);
                tft_set_draw_color (0xff0000);
                tft_draw_filled_circle ((int16_t)(key_cls_x - 3 * (pmd_font_get_height () +
12)),
                        (int16_t)(key_cls_y + pmd_font_get_height () / 2),
                                        pmd_font_get_height () / 2 + 1);
                tft_set_draw_color (0x0000ff);
                while (1)
                   static touch_ctrl_s touch_ctrl;
                    tft_touch_scan_start ();
                    while (!tft_touch_data_ready ())
                    touch_ctrl = tft_touch_read_raw ();
                    timeout ++;
                    if ( timeout == 30000 )
                        break;
                    // CLS key
                    if ( tft_touch_in_window(&touch_ctrl, &key_cls_in_window,
&key_cls_in_window_timeout,
                            key_cls_x, key_cls_y,
                            key_cls_w, key_cls_h,
                            5) == true)
                    {
                        goto restart_paint ; // yes let's restart the routine
                    // END CLS key
                    if (tft_touch_in_window(&touch_ctrl, &key_red_in_window,
&key_red_in_window_timeout,
                                              (int16_t)(key_cls_x - 3 * ( pmd_font_get_height()
+ 12 ) - pmd_font_get_height()/2),
                                              key_cls_y,
                                             pmd_font_get_height()+2,
                                              pmd_font_get_height()+2,
                                              5) == true)
                     {
                        tft_set_draw_color(0xff0000);
                        pmd text set f color(0xffff0000); // A - is 255 use foreground color
red
                        pmd_draw_string("TOUCH PAINT\r\n",5,7);
                     }
                    if (tft_touch_in_window(&touch_ctrl, &key_green_in_window,
&key green in window timeout,
                                              (int16_t)(key_cls_x - 2 * ( pmd_font_get_height()
+ 12 )-pmd_font_get_height()/2),
                                              key_cls_y,
                                              pmd_font_get_height()+2,
                                              pmd_font_get_height()+2,
                                             5) == true)
                        tft_set_draw_color(0x00ff00);
                        pmd_text_set_f_color(0xff00ff00); // A - is 255 use foreground color
green
                        pmd_draw_string("TOUCH PAINT\r\n",5,7);
                     }
                    if (tft_touch_in_window (&touch_ctrl, &key_blue_in_window,
&key_blue_in_window_timeout,
                                              (int16_t)(key_cls_x - 1 * (pmd_font_get_height ()
+ 12) - pmd_font_get_height () / 2),
```



```
key_cls_y,
                                         pmd_font_get_height () + 2,
                                         pmd_font_get_height () + 2,
                                         5) == true)
                  {
                      tft_set_draw_color (0x0000ff);
                      pmd_text_set_f_color (0xff0000ff); // A - is 255 use foreground color
blue
                      pmd_draw_string ("TOUCH PAINT\r\n", 5, 7);
                  }
                  // touch_ctrl.touch_press pressure value not normalized
                  if ((touch_ctrl.g_touch_press) && (touch_ctrl.touch_y > keep_out_y ))
                    tft_draw_line ((int16_t)touch_ctrl.touch_x,
                                  (int16_t)touch_ctrl.touch_y,
                                  (int16_t)touch_ctrl.touch_x,
                                  (int16_t)touch_ctrl.touch_y);
                  }
                  if (touch_ctrl.g_touch_press)
                      timeout = 0;
              }
           break:
           case 24:
              act_state = 0;
              break;
           default:
             break;
       }
       /* Delav */
       R_BSP_SoftwareDelay(delay, bsp_delay_units);
   }
}
********************//**
 * This function is called at various points during the startup process. This implementation
uses the event that is
 * called right before main() to set up the pins.
  @param[in] event
                     Where at in the start up process the code is currently at
********************************
*********************
void R_BSP_WarmStart (bsp_warm_start_event_t event)
   if (BSP_WARM_START_RESET == event)
#if BSP_FEATURE_FLASH_LP_VERSION != 0
       /* Enable reading from data flash. */
       R_FACI_LP->DFLCTL = 1U;
       /* Would normally have to wait tDSTOP(6us) for data flash recovery. Placing the enable
here, before clock and
        * C runtime initialization, should negate the need for a delay since the
initialization will typically take more than 6us. */
#endif
   }
   if (BSP_WARM_START_POST_C == event)
       /* C runtime environment and system clocks are setup. */
       /* Configure pins. */
       IOPORT_CFG_OPEN (&IOPORT_CFG_CTRL, &IOPORT_CFG_NAME);
```



```
#if BSP_TZ_SECURE_BUILD
                          FSP_CPP_HEADER
                          BSP_CMSE_NONSECURE_ENTRY void template_nonsecure_callable ();
                          /* Trustzone Secure Projects require at least one nonsecure callable function in order to build
                          (Remove this if it is not required to build). */
                         BSP_CMSE_NONSECURE_ENTRY void template_nonsecure_callable ()
                         FSP_CPP_FOOTER
                         #endif
1.31
                         The project is now ready to compile. Press the "hammer" icon to start building the project.
1.32
                          Once the build has finished, the Console pane in the lower-right corner of e<sup>2</sup> studio will report zero
                          errors:
                                                                        Problems ☐ Console X ☐ Properties → Smart Browser ☐ Smart Manual ☐ Memory → Debug ☐ Memory → Search
                                                                       CDT Build Console (AIK RA4E1_ttt basic_demo)

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_group_irq.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_group_irq.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_irq.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_irq.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_register_protection.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_register_protection.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_sork.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_security.c

Building file: .../ra/fsp/src/bsp/mcu/all/bsp_security.c

Building file: .../ra/fsp/src/bsp/msis/Device/RENESAS/Source/startup.c

Building file: .../ra/board/aik_ra4e1/board_init.c

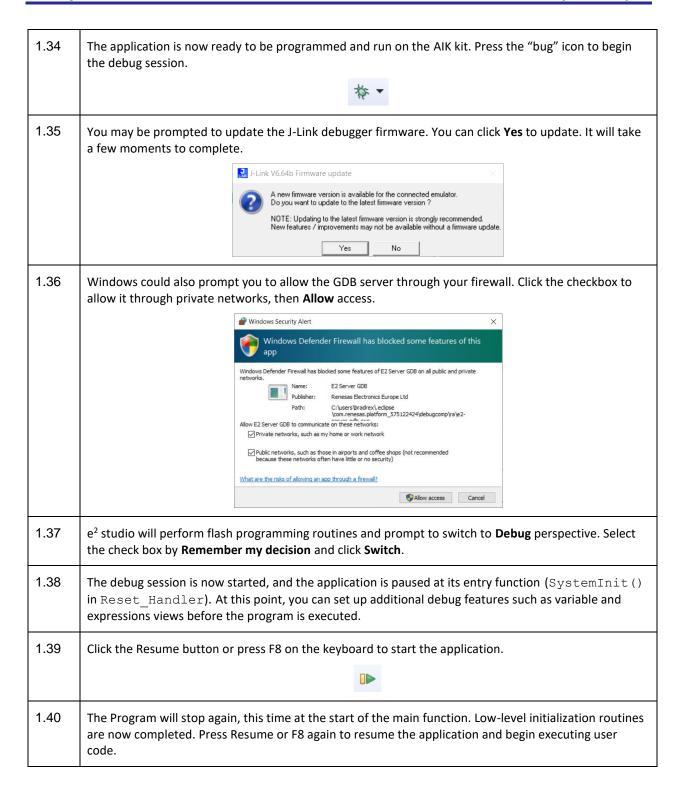
Building file: .../ra/board/aik_ra4e1/board_leds.c

Building file: .../ra/fsp/src/bsp/cedemo.elf "AIK_RA4E1_tft_basic_demo.elf" "AIK_RA4E1_tft_basic_demo.srcc" arm-none-eabi-size--format-berkeley "AIK_RA4E1_tft_basic_demo.elf"

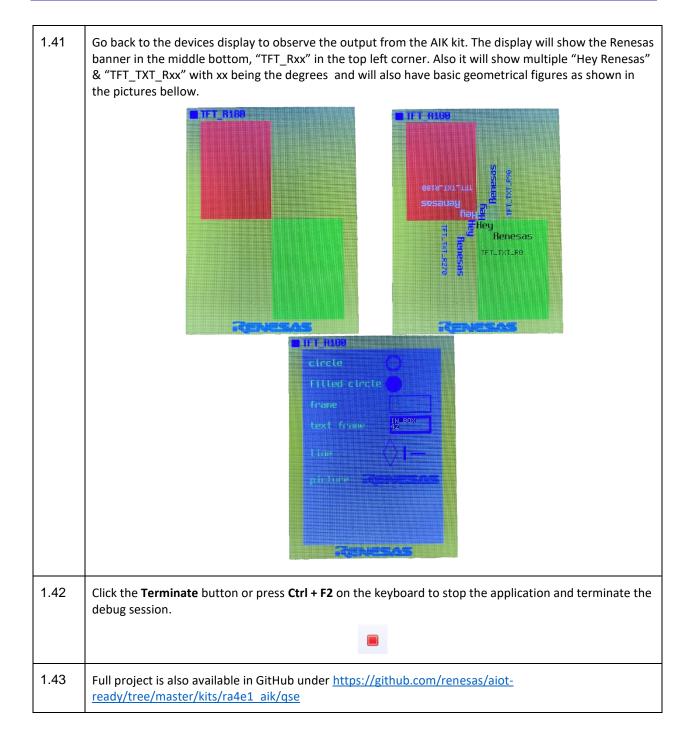
text data bss dec hex filename

31168 76 41756 73000 11d28 AIK_RA4E1_tft_basic_demo.elf
                                                                        CDT Build Console [AIK_RA4E1_tft_basic_demo]
                                                                        11:39:01 Build Finished. 0 errors, 9 warnings. (took 3s.60ms)
1.33
                          Check that the Display is connected to PMOD2 as seen below.
                                                                                                                                                                                       AIK-RA4E1
```









**END OF SECTION** 

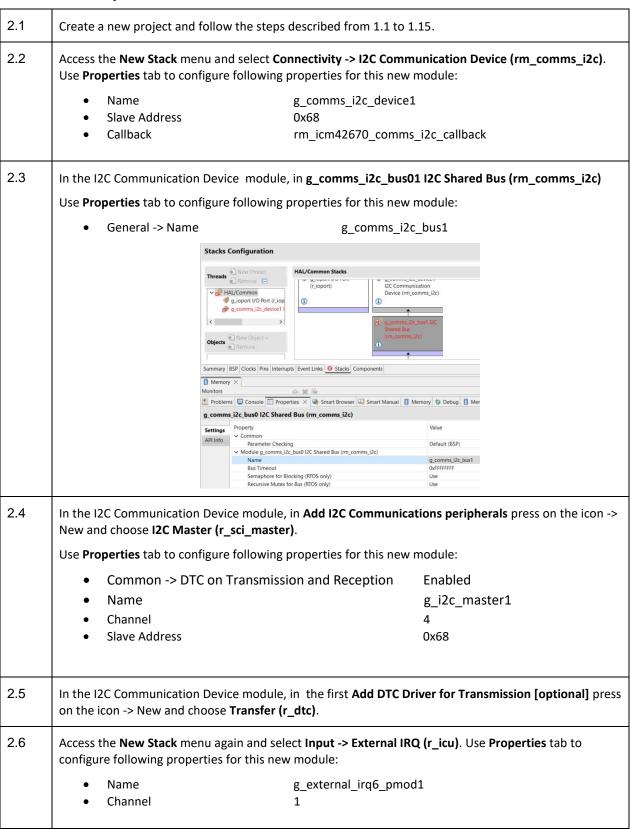


## 2 Implementing Accelerometer demo

#### Overview

Following section describes in details steps required to set up an accelerometer demo project for AIK RA4E1 kit.

### **Procedural Steps**





0.7	A	alas Carres di ita a HART (noni anna) lla Romantia dala da	
2.7	Access the <b>New Stack</b> menu again and select <b>Connectivity -&gt; UART (r_sci_uart)</b> . Use <b>Properties</b> tab to configure following properties for this new module:		
	<ul><li>Common -&gt; DTC Support</li><li>General -&gt; Name</li></ul>	Enable	
	General -> Channel	g_uart3_pmod2 3	
		5	
	<ul><li>Baud -&gt; Max Error (%)</li><li>Interrupts -&gt; Callback</li></ul>	rm_uart_callback	
	interrupts -> Camback	IIII_uait_caiiback	
2.8	In the UART module, in <b>Add DTC Driver for Transmission [optional]</b> press on the icon -> New and choose <b>Transfer (r_dtc)</b> .		
2.9	Generate Project Content button in the	plete. Apply changes to the project source by clicking the top-right corner of the Configurator window. When prompted the box next to <b>Always save and generate without asking</b> and	
		Generate Project Content	
	New Stace	k > # Extend Stack > Remove	
2.10	The FSP Configurator will extract all the configuration provided in the <b>Propertie</b>	necessary drivers and generate the code based on the <b>s</b> tab.	
2.11	In the <b>Project Explorer</b> pane, expand th that can be found in the demo folder:	e <b>src</b> folder in the project and add the following folders and files	
	• rm_icm42670		
	SEGGER_RTT		
	_		
	• common_utils.h		
2.12	In the <b>Project Explorer</b> pane, expand th	e <b>src</b> folder in the project and open <b>hal_entry.c</b> .	
	⊯ RA6	M3_AIK_accelerometer_demo [Debug]	
	> 📦 Ir		
	> <u>@</u> ra		
	>		
		rm_icm42670	
		SEGGER_RTT	
	_	common_utils.h	
	> [c	hal_entry.c	
2.13		ed for the user to specify additional functions to be called	
2.14	Athe the beginning of hal_entry.c before add the #include statement for the following the following statement for	e "void R_BSP_WarmStart(bsp_warm_start_event_t event);" owing:	
	<pre>#include <stdio.h></stdio.h></pre>		
	<ul><li>#include (stude.ii)</li><li>#include (string.h)</li></ul>		
	<ul><li>#include "common_utils.h"</li></ul>		
	• #include "rm_icm42670/rm_icm4		
	<ul><li>#include "rm_icm42670/rm_comm</li><li>#define RM_ICM42670_EXAMPLE_I</li></ul>		
	#UETITIE NIT_TCH420/0_EXAMPLE_L	PELAT_JUNG JU	



- #define RM\_ICM42670\_EXAMPLE\_DELAY\_1US 10
- #define RM\_ICM42670\_EXAMPLE\_IRQ\_ENABLE 1
- hal\_entry.c can be used to exercise API of the various modules configured inside FSP Configurator using 2.15 Developer Assist or by writing code manually.

Following code can be used to completely replace contents of hal entry.c to perform basic operations

```
using the display for the AIK-RA4E1 board:
include "hal_data.h"
#include <stdio.h>
#include <string.h>
#include "common_utils.h"
#include "rm_icm42670/rm_icm42670_hal_data.h"
#include "rm_icm42670/rm_common_uart.h"
#define RM_ICM42670_EXAMPLE_DELAY_50MS 50
#define RM_ICM42670_EXAMPLE_DELAY_1US 10
#define RM_ICM42670_EXAMPLE_IRQ_ENABLE 1
FSP_CPP_HEADER
void R_BSP_WarmStart(bsp_warm_start_event_t event);
//void __attribute__((optimize("00"))) init_i2c_comm(void) ;
void init_i2c_comm(void) ;
fsp_err_t rm_icm42670_irq_open (rm_icm42670_ctrl_t * const p_api_ctrl);
FSP_CPP_FOOTER
#ifdef RTT_DEBUG_ON
char segBuf1[16] ;
char segBuf2[16];
#endif
volatile rm_comms_i2c_bus_extended_cfg_t * p_extend ;
volatile i2c_master_instance_t * p_driver_instance ;
* @brief initialize the comms bus interface
void init_i2c_comm(void)
{
   fsp_err_t err = FSP_SUCCESS;
    /* Open the I2C bus if it is not already open. */
   p_extend = (rm_comms_i2c_bus_extended_cfg_t*) g_icm42670_sensor0_cfg.p_comms_instance-
>p_cfg->p_extend;
   p_driver_instance = (i2c_master_instance_t*) p_extend->p_driver_instance;
   p_driver_instance->p_ctrl = &g_icm42670_sensor0_ctrl;
    err = p_driver_instance->p_api->open (p_driver_instance->p_ctrl, p_driver_instance->p_cfg);
    if(err != FSP_SUCCESS){__BKPT(0);}
#if BSP_CFG_RTOS
    /* Create a semaphore for blocking if a semaphore is not NULL */
    if (NULL != p_extend->p_blocking_semaphore)
 #if BSP_CFG_RTOS == 1
                                       // AzureOS
        tx_semaphore_create(p_extend->p_blocking_semaphore->p_semaphore_handle,
                            p_extend->p_blocking_semaphore->p_semaphore_name,
                            (ULONG) 0);
                                       // FreeRTOS
 #elif BSP CFG RTOS == 2
        *(p_extend->p_blocking_semaphore->p_semaphore_handle) =
            xSemaphoreCreateCountingStatic((UBaseType_t) 1,
                                            (UBaseType_t) 0,
                                           p_extend->p_blocking_semaphore->p_semaphore_memory);
 #endif
   }
    /* Create a recursive mutex for bus lock if a recursive mutex is not NULL */
    if (NULL != p_extend->p_bus_recursive_mutex)
 #if BSP_CFG_RTOS == 1
                                       // AzureOS
        tx_mutex_create(p_extend->p_bus_recursive_mutex->p_mutex_handle,
                        p_extend->p_bus_recursive_mutex->p_mutex_name,
                        TX_INHERIT);
 #elif BSP CFG RTOS == 2
                                       // FreeRTOS
```



```
*(p_extend->p_bus_recursive_mutex->p_mutex_handle) =
          xSemaphoreCreateRecursiveMutexStatic(p extend->p bus recursive mutex-
>p_mutex_memory);
#endif
#endif
}
**********************
* main() is generated by the RA Configuration editor and is used to generate threads if an
RTOS is used. This function
* is called by main() when no RTOS is used.
*************************
*******************
void hal_entry(void)
   /* TODO: add your own code here */
#if BSP_TZ_SECURE_BUILD
   /* Enter non-secure code */
   R_BSP_NonSecureEnter();
#endif
   fsp_err_t err = FSP_SUCCESS;
   rm_icm42670_raw_data_t raw_data;
   rm_icm42670_accel_data_t icm42670_accel_data;
   rm_icm42670_gyro_data_t icm42670_gyro_data;
   rm_icm42670_temp_data_t icm42670_temp_data;
#if 0 == RM_ICM42670_EXAMPLE_IRQ_ENABLE
   rm_icm42670_device_status_t device_status;
#endif
   /* Enable access to the PFS registers. If using r_ioport module then register protection is
automatically
    * handled. This code uses BSP IO functions to show how it is used.
   R_BSP_PinAccessEnable ();
   R_BSP_PinWrite(LED1_BLUE, BSP_IO_LEVEL_HIGH);
   /* Open the uart bus if it is not already open. */
   err = rm_uart_initialize ();
   if ( err != FSP_SUCCESS)
   {
       R_BSP_PinWrite(LED1_RED, BSP_IO_LEVEL_HIGH);
   else
       R_BSP_PinWrite(LED1_GREEN, BSP_IO_LEVEL_HIGH);
   R_BSP_PinWrite(LED1_BLUE, BSP_IO_LEVEL_LOW);
   /* cursor home */
   printf ("%c[H", 27);
#ifdef RTT_DEBUG_ON
   // RTT seems not to support cursor home
   //APP_PRINT("\x1B[H");
#endif
   /* cls terminal clear screen */
   printf ("%c[2J", 27);
#ifdef RTT_DEBUG_ON
   APP_PRINT ("RTT_CTRL_CLEAR");
   APP_PRINT (BANNER_INFO);
#endif
   printf ("UART
                               : initialized\r\n");
#ifdef RTT_DEBUG_ON
```



```
APP_PRINT ("UART
                                     : initialized\r\n");
#endif
    /* init the i2c comm interface */
   init_i2c_comm ();
    printf ("I2c common interface : initialized\r\n");
#ifdef RTT_DEBUG_ON
   APP_PRINT ("I2c common interface : initialized\r\n");
#endif
    /* After reset unlock the Open ICM42670 state */
    g_icm42670_sensor0_ctrl.open = 0;
    /* Open ICM42670 */
   err = RM_ICM42670_Open (&g_icm42670_sensor0_ctrl, &g_icm42670_sensor0_cfg);
   if (err != FSP_SUCCESS)
    {
       R_BSP_PinWrite(LED1_RED, BSP_IO_LEVEL_HIGH);
        __BKPT(0);
   printf ("ICM42670 module
                                  : initialized\r\n");
#ifdef RTT_DEBUG_ON
   APP_PRINT ("ICM42670 module
                                   : initialized\r\n");
#endif
    g_icm42670_interrupt_cfg.int_config |= 0x01 ; // use active high (or change
g_external_irq6_pmod1 raising to falling)
    err = RM_ICM42670_DeviceInterruptCfgSet (&g_icm42670_sensor0_ctrl,
g_icm42670_interrupt_cfg);
   if (err != FSP_SUCCESS)
       R_BSP_PinWrite(LED1_RED, BSP_IO_LEVEL_HIGH);
         _BKPT(0);
    printf ("ICM42670 interrupts : initialized\r\n");
#ifdef RTT DEBUG ON
   APP_PRINT ("ICM42670 interrupts : initialized\r\n");
   /* Start measurement in data ready mode */
   err = RM_ICM42670_MeasurementStart (&g_icm42670_sensor0_ctrl);
   if (err != FSP_SUCCESS)
       R_BSP_PinWrite(LED1_RED, BSP_IO_LEVEL_HIGH);
       __BKPT(0);
    printf ("ICM42670 measurement : started\r\n");
#ifdef RTT_DEBUG_ON
   APP_PRINT ("ICM42670 measurement : started\r\n");
    /* Open external IRQ */
   err = rm_icm42670_irq_open (&g_icm42670_sensor0_ctrl);
   if (err != FSP_SUCCESS)
       R_BSP_PinWrite(LED1_RED, BSP_IO_LEVEL_HIGH);
       __BKPT(0);
   printf ("ICM42670 interrupt : opened\r\n");
#ifdef RTT DEBUG ON
   APP_PRINT ("ICM42670 interrupt : opened\r\n");
#endif
    err = R_ICU_ExternalIrqEnable (&g_external_irq6_pmod1_ctrl);
   if (err != FSP_SUCCESS)
       R_BSP_PinWrite(LED1_RED, BSP_IO_LEVEL_HIGH);
        __BKPT(0);
    }
   printf ("ICM42670 interrupt : enabled\r\n");
#ifdef RTT DEBUG ON
   APP_PRINT ("ICM42670 interrupt : enabled\r\n");
#endif
     * Example :
```



```
* Device interrupt : data ready mode
    R_BSP_SoftwareDelay(1500, BSP_DELAY_UNITS_MILLISECONDS);
    //cls terminal clear screen
printf ("%c[2J", 27);
#ifdef RTT DEBUG ON
    APP_PRINT (RTT_CTRL_CLEAR);
#endif
    while (true)
#if RM_ICM42670_EXAMPLE_IRQ_ENABLE
        /* Wait IRQ callback */
        while (0 == g_irq_flag)
            /* Wait callback */
        g_irq_flag = 0;
#else
        do
            RM_ICM42670_DeviceStatusGet (&g_icm42670_sensor0_ctrl, &device_status);
            rm_icm42670_device_status_check (&g_icm42670_sensor0_ctrl);
        while (false == device_status.data_ready);
#endif
#if 1
        /* cursor home */
        printf ("%c[H", 27);
#ifdef RTT_DEBUG_ON
        // RTT seems not to support cursor home
        //APP_PRINT("\x1B[H");
#endif
        /* Read Temperature data */
        RM_ICM42670_TempRead (&g_icm42670_sensor0_ctrl, &raw_data);
        /* Calculate Temperature data */
        RM_ICM42670_TempDataCalculate (&g_icm42670_sensor0_ctrl, &raw_data,
&icm42670_temp_data);
        /* Output Temperature data to console */
        printf ("
                                    \r\n");
        printf ("Temperature: %3.1f [%+3d] degrees Celsius\r\n",
icm42670_temp_data.temp_data_float,
                icm42670_temp_data.temp_data);
#ifdef RTT DEBUG ON
        snprintf(segBuf1,sizeof(segBuf1)-1,"%3.1f",icm42670_temp_data.temp_data_float);
        snprintf(segBuf2,sizeof(segBuf2)-1,"%+3d",icm42670_temp_data.temp_data);
        APP_PRINT ("\r\n");
        APP_PRINT ("Temperature: %s [%s] degrees Celsius\r\n", segBuf1, segBuf2);
#endif
        /* Read Accel data */
        RM_ICM42670_AccelRead (&g_icm42670_sensor0_ctrl, &raw_data);
        /* Calculate Accel data */
        RM_ICM42670_AccelDataCalculate (&g_icm42670_sensor0_ctrl, &raw_data,
&icm42670_accel_data);
        /* Output Accel data to console */
        printf ("
                                   \r\n");
        printf ("Acc_x: %10.3f\r\n", icm42670_accel_data.accel_x);
        printf ("Acc_y: %10.3f\r\n", icm42670_accel_data.accel_y);
        printf ("Acc_z: %10.3f\r\n", icm42670_accel_data.accel_z);
#ifdef RTT_DEBUG_ON
        APP_PRINT ("\r\n");
        snprintf(segBuf1,sizeof(segBuf1)-1,"%10.3f",icm42670_accel_data.accel_x);
        APP_PRINT ("Acc_x: %s\r\n", segBuf1);
```



```
snprintf(segBuf1,sizeof(segBuf1)-1,"%10.3f",icm42670_accel_data.accel_y);
        APP_PRINT ("Acc_y: %s\r\n", segBuf1);
        snprintf(segBuf1, sizeof(segBuf1)-1, "%10.3f", icm42670_accel_data.accel_z);
        APP_PRINT ("Acc_z: %s\r\n", segBuf1);
#endif
        /* Read Gyro data */
        RM_ICM42670_GyroRead (&g_icm42670_sensor0_ctrl, &raw_data);
        /* Calculate Gyro data */
        RM_ICM42670_GyroDataCalculate (&g_icm42670_sensor0_ctrl, &raw_data,
&icm42670_gyro_data);
        /* Output Gyro data to console */
        printf ("
                                    \r\n");
       printf ("Gyro_x: %10.3f\r\n", icm42670_gyro_data.gyro_x);
printf ("Gyro_y: %10.3f\r\n", icm42670_gyro_data.gyro_y);
printf ("Gyro_z: %10.3f\r\n", icm42670_gyro_data.gyro_z);
#ifdef RTT_DEBUG_ON
        APP PRINT ("\r\n");
        snprintf(segBuf1,sizeof(segBuf1)-1,"%10.3f",icm42670_gyro_data.gyro_x);
        APP_PRINT ("Gyro_x: %s\r\n", segBuf1);
        snprintf(segBuf1,sizeof(segBuf1)-1,"%10.3f",icm42670_gyro_data.gyro_y);
        APP_PRINT ("Gyro_y: %s\r\n", segBuf1);
        snprintf(segBuf1,sizeof(segBuf1)-1,"%10.3f",icm42670_gyro_data.gyro_z);
        APP_PRINT ("Gyro_z: %s\r\n", segBuf1);
#endif
        {
            static uint16_t mode = BSP_IO_LEVEL_HIGH ;
            mode = mode == BSP_IO_LEVEL_HIGH ? BSP_IO_LEVEL_LOW : BSP_IO_LEVEL_HIGH ;
            R_BSP_PinWrite(LED1_GREEN, mode);
#endif
    }
}
/**********************************
******************//**
 * This function is called at various points during the startup process. This implementation
uses the event that is
 * called right before main() to set up the pins.
 * @param[in] event
                        Where at in the start up process the code is currently at
**************************
void R_BSP_WarmStart(bsp_warm_start_event_t event)
    if (BSP_WARM_START_RESET == event)
#if BSP_FEATURE_FLASH_LP_VERSION != 0
        /* Enable reading from data flash. */
        R_FACI_LP->DFLCTL = 1U;
        /* Would normally have to wait tDSTOP(6us) for data flash recovery. Placing the enable
here, before clock and
         * C runtime initialization, should negate the need for a delay since the
initialization will typically take more than 6us. */
#endif
    if (BSP_WARM_START_POST_C == event)
        /* C runtime environment and system clocks are setup. */
        /* Configure pins. */
        IOPORT_CFG_OPEN (&IOPORT_CFG_CTRL, &IOPORT_CFG_NAME);
    }
#if BSP_TZ_SECURE_BUILD
```



FSP\_CPP\_HEADER BSP CMSE NONSECURE ENTRY void template nonsecure callable (); /\* Trustzone Secure Projects require at least one nonsecure callable function in order to build (Remove this if it is not required to build). \*/ BSP\_CMSE\_NONSECURE\_ENTRY void template\_nonsecure\_callable () FSP\_CPP\_FOOTER #endif 2.16 Right-click the project in the Project Explorer and select Properties form the context menu, then navigate to C/C++ Build -> Settings. Make sure you're on the tool Setting -> GNU Arm Cross Linker -> Miscellaneous tab and click on the Use float with nano printf (-u\_printf\_float). Properties for RA4E1\_AIK\_accelerometer\_demo type filter text × Settings 0 - 0 - 8 > Resource Builders ~ C/C++ Build Configuration: Debug [Active] Manage Configurations... **Build Variables** Environment Logging Settings 👸 Tool Settings 👸 Toolchain 🎤 Build Steps 🤗 Build Artifact 📸 Binary Parsers 🔞 Error Parsers Target Processor Tool Chain Editor Optimization

Warnings

Debugging

Signu Arm Cross Assembler > C/C++ General > MCU Project Natures Project References Preprocessor
Includes
Warnings
Miscellaneous Renesas QE Run/Debug Settings → 

■ GNU Arm Crass C Compiler Other objects ● 和 留 なり り Preprocessor Miscellaneous → 

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Arm Crass C Linker

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C Linke General
Dibraries
Miscellaneous → ISS GNU Arm Cross Create Flash Image Generate map "\$(BuildArtifactFileBaseName),map" ☼ General
ॐ GNU Arm Cross Print Size
ॐ General Crass reference (-Xlinker --cref) Print link map (-Xlinker --print-map) ☐ Use newlib-nano (--specs=nano.specs)
☐ Use float with nano printf (-u\_printf\_float) Use float with nano scanf (-u\_scanf\_float Do not use systalls (--specs=nosys.specs) ☐ Verbase (-v) Other linker flags --specs=rdimon.specs 3 Apply and Close Cancel 2.17 Navigate to Setting -> GNU Arm Cross C Compiler -> includes tab and click on the Add button to Include paths and add: "\${workspace\_loc:/\${ProjName}/src/SEGGER\_RTT}" "\${workspace\_loc:/\${ProjName}/src/rm\_icm42670}" 🛞 Tool Settings 🚷 Toolchain 🎤 Build Steps 學 Build Artifact 🔒 Binary Parsers 🔞 Error Parsers Target Processor Optimization "\${workspace\_loc:/\${ProjName}/src}" **Warnings** "\${workspace\_loc:/\${ProjName}/src/SEGGER\_RTT}" Debugging "\${workspace\_loc:/\${ProjName}/ra/fsp/inc}" "\${workspace\_loc:/\${ProjName}/ra/fsp/inc/api}" → Signature Preprocessor Includes "\$(workspace loc:/\$(ProjName)/ra/fsp/inc/instances "\${workspace\_loc;/\${ProjName}/ra/arm/CMSIS\_5/CMSIS/Core/Include}" "\${workspace\_loc;/\${ProjName}/ra\_gen}" Warnings Warnings Miscellaneous "\${workspace loc;/\${ProiName}/ra cfg/fsp cfg/bsp} S GNU Arm Cross C Compiler "\${workspace\_loc:/\${ProjName}/ra\_cfg/fsp\_cfg}" Preprocessor Mark Includes Optimization Include system paths (-isystem) 2.18 The project is now ready to compile. Press the "hammer" icon to start building the project.



2.19 Once the build has finished, the **Console** pane in the lower-right corner of e<sup>2</sup> studio will report zero error and warnings:

Problems Console X Properties Smart Browser Smart Manual Memory Debug Memory Search

CDT Build Console [RA4E1\_AIK\_accelerometer\_demo]

Extracting support files...

16:57:55 \*\*\*\* Incremental Build of configuration Debug for project RA4E1\_AIK\_accelerometer\_demo \*\*\*\*

make -r -j8 all

Building target: RA4E1\_AIK\_accelerometer\_demo.elf

Building target: RA4E1\_AIK\_accelerometer\_demo.elf "RA4E1\_AIK\_accelerometer\_demo.elf" "RA4E1\_AIK\_accelerometer\_demo.elf"

arm-none-eabi-size --format-berkeley "RA4E1\_AIK\_accelerometer\_demo.elf"

text data bss dec hex filename

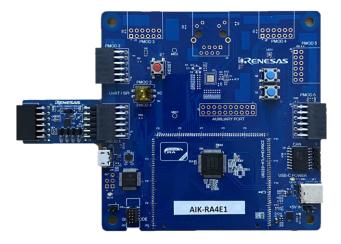
133024 568 29220 62812 f55c RA4E1\_AIK\_accelerometer\_demo.elf

16:57:55 Build Finished. 0 errors, 0 warnings. (took 556ms)

2.20 Connect PMOD2 Pin2 & Pin 3 with the USB2Serial TX & RX pins of the dongle respectively to enable UART output through Teraterm.

Pin	Signal/Bus SPI	Description UART
2	P707	TXD
3	P706	RXD

2.21 Check that the Accelerometer is connected to PMOD1 as seen below.



2.22 The application is now ready to be programmed and run on the AIK kit. Press the "bug" icon to begin the debug session.

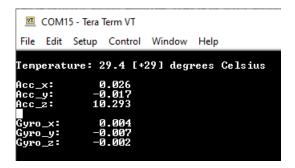


- 2.23 Bring up the serial terminal window to observe the debug output from the AIK kit.
- 2.24 Click the **Resume** button or press **F8** on the keyboard to start the application. Press **Resume** or **F8** again to resume the application and begin executing user code.



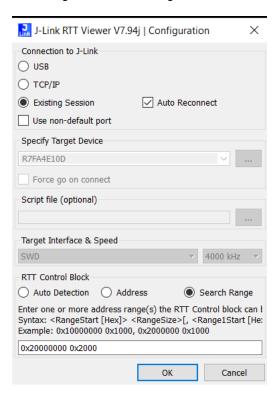


Go back to the serial terminal window which should now be populated with debug output from the AIK kit. First line shows the temperature in degrees Celsius , the rest lines show the data of the accelerometers x,y,z axis and the data from the gyroscope x,y,z axis.



2.26 To view the log from J-Link RTT Viewer, use: Connection to J-Link → Existing Session

RTT Control Block → Search Range and in the Range add: 0x20000000 0x2000



Else go to e2studio RA6M3\_AIK\_accelerometer\_demo.map, find

0xa8 ./src/SEGGER\_RTT/SEGGER\_RTT.o and get the value next to it and in the J\_Link RTT Viewer, use:

Connection to J-Link → Existing Session

RTT Control Block → Address and use the value you found above.

```
.bss._SEGGER_RTT

0x20001044
0xa8 ./src/SEGGER_RTT/SEGGER_RTT.o
0x20001044
__SEGGER_RTT
```



2.27	Click the <b>Terminate</b> button or press <b>Ctrl + F2</b> on the keyboard to stop the application and terminate the debug session.	
2.28	Full project is also available in GitHub under <a href="https://github.com/renesas/aiot-ready/tree/master/kits/ra4e1">https://github.com/renesas/aiot-ready/tree/master/kits/ra4e1</a> aik/qse	

## **END OF SECTION**

