

A

Sheet: Program Counter



File: Program Counter.sch

Sheet: Accumulator a Register



File: Accumulator a Register.sch

Sheet: Display



File: Display.sch

Sheet: Control



File: Control.sch

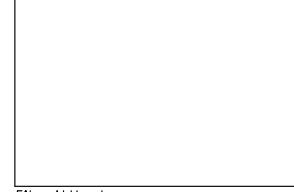
B

Sheet: MAR



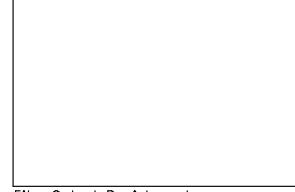
File: MAR.sch

Sheet: ALU



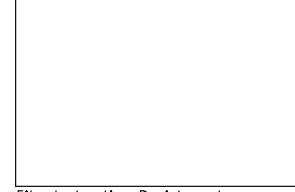
File: ALU.sch

Sheet: Output Register



File: Output Register.sch

Sheet: Instruction Register



File: Instruction Register.sch

C

Sheet: RAM



File: RAM.sch

Sheet: B Register



File: B Register.sch

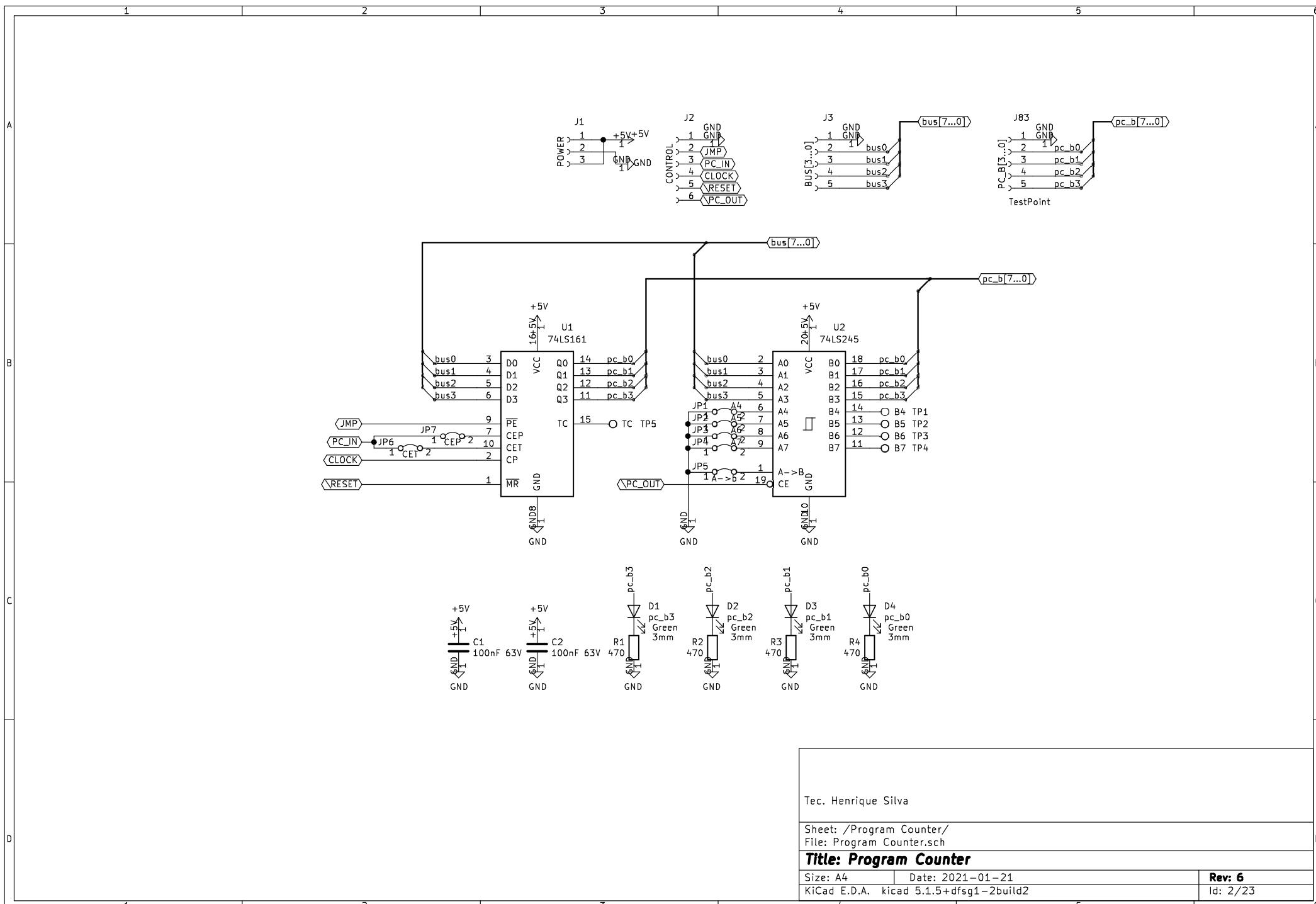
Sheet: BUS

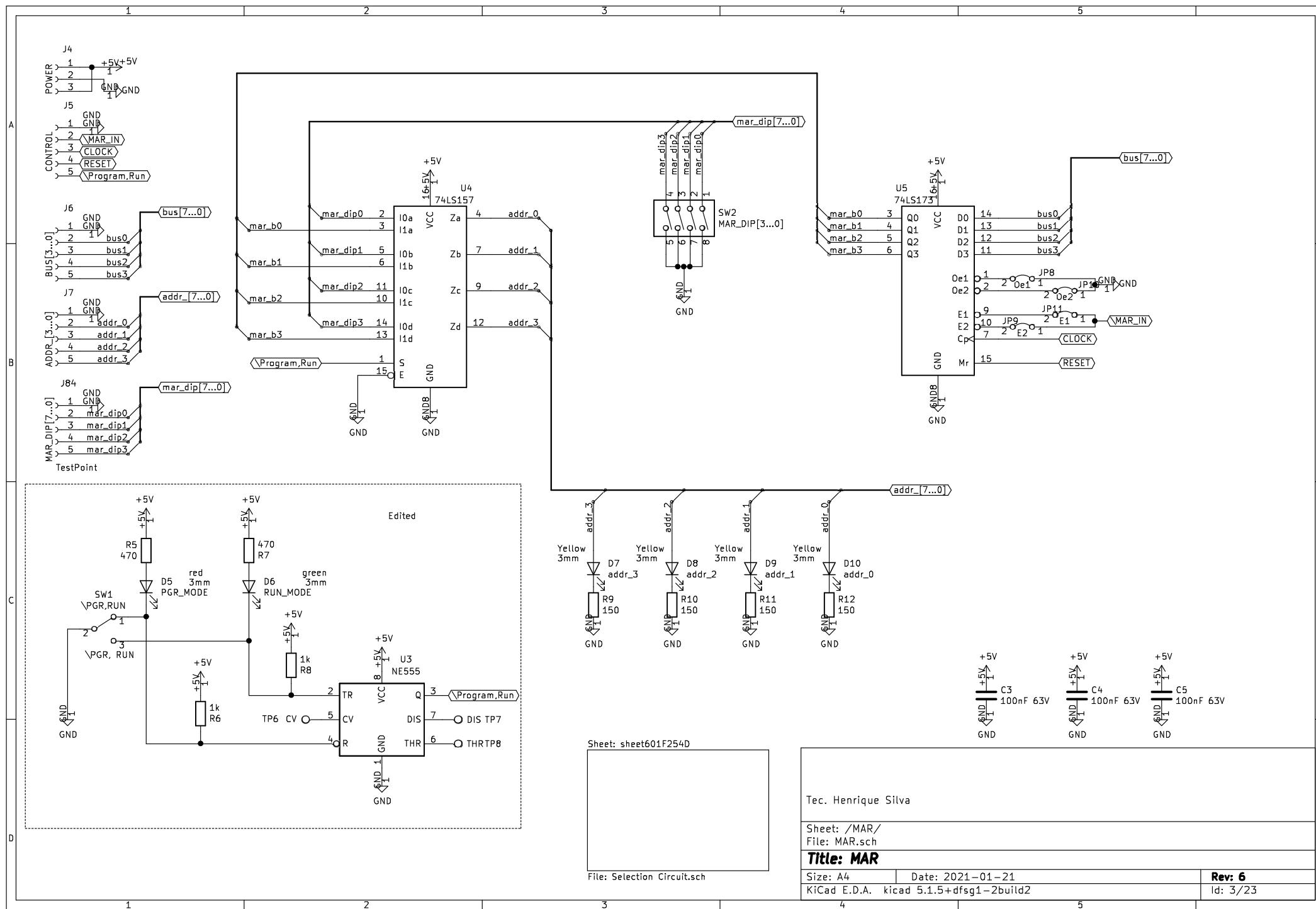


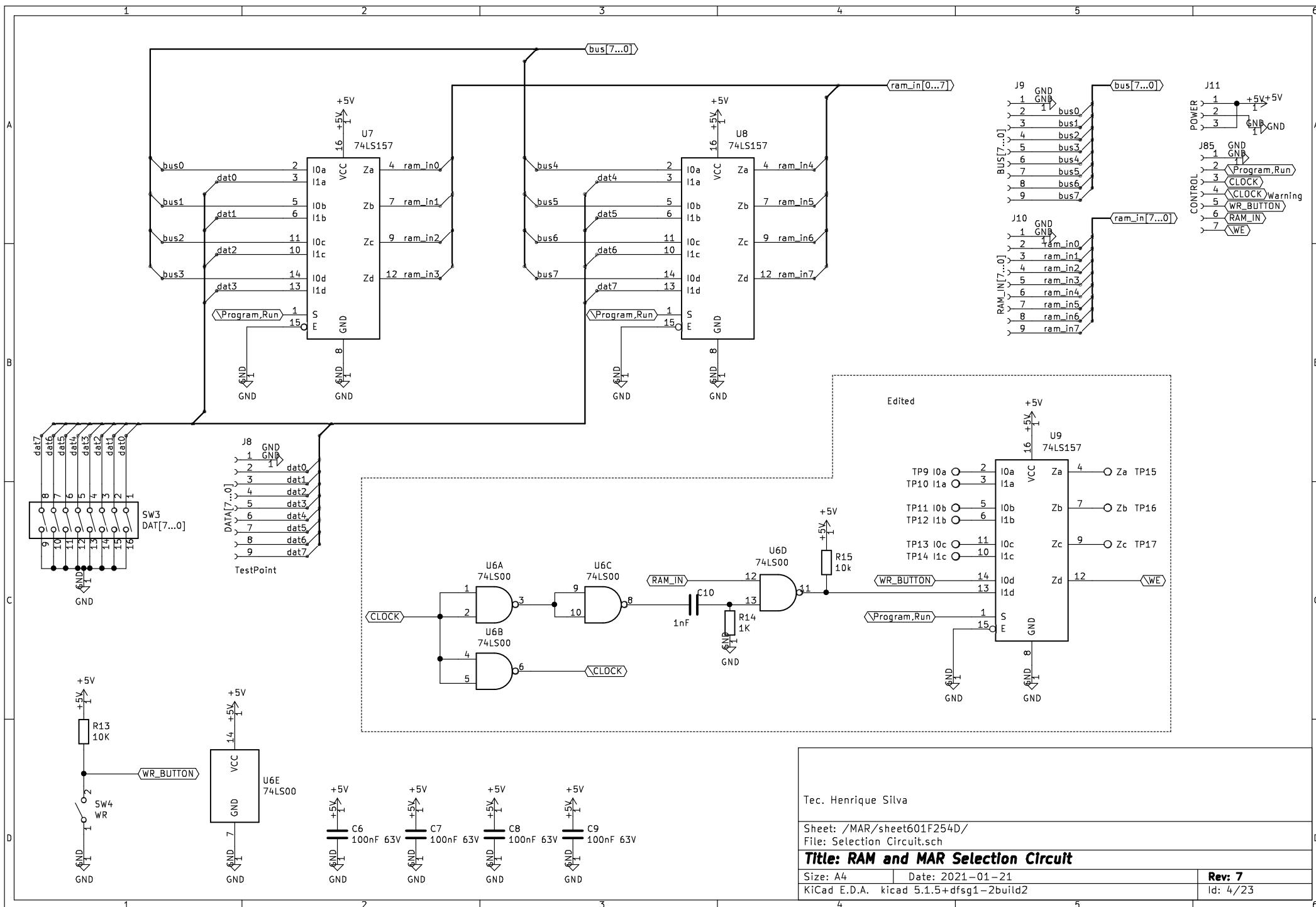
File: BUS.sch

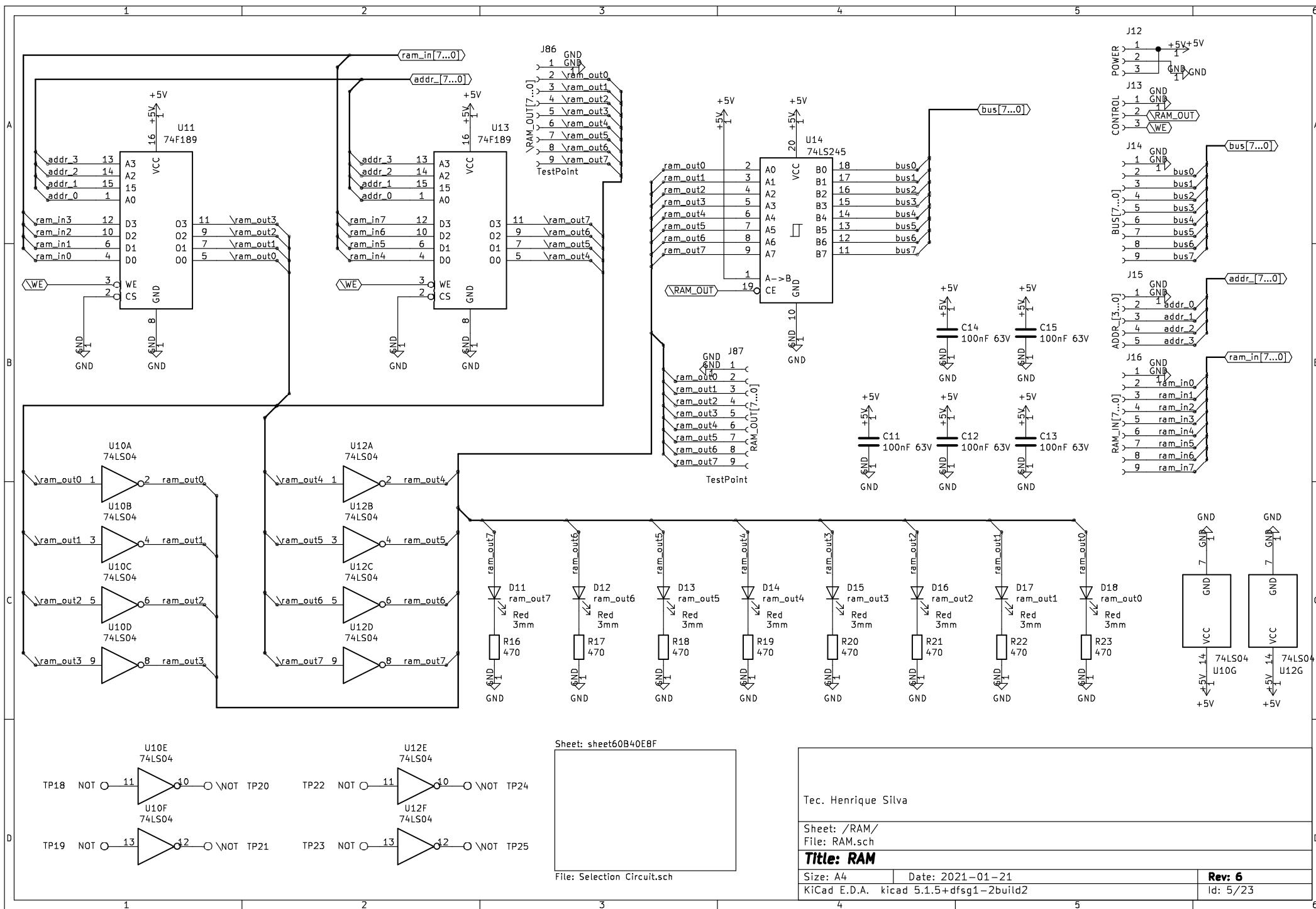
Tec. Henrique Silva

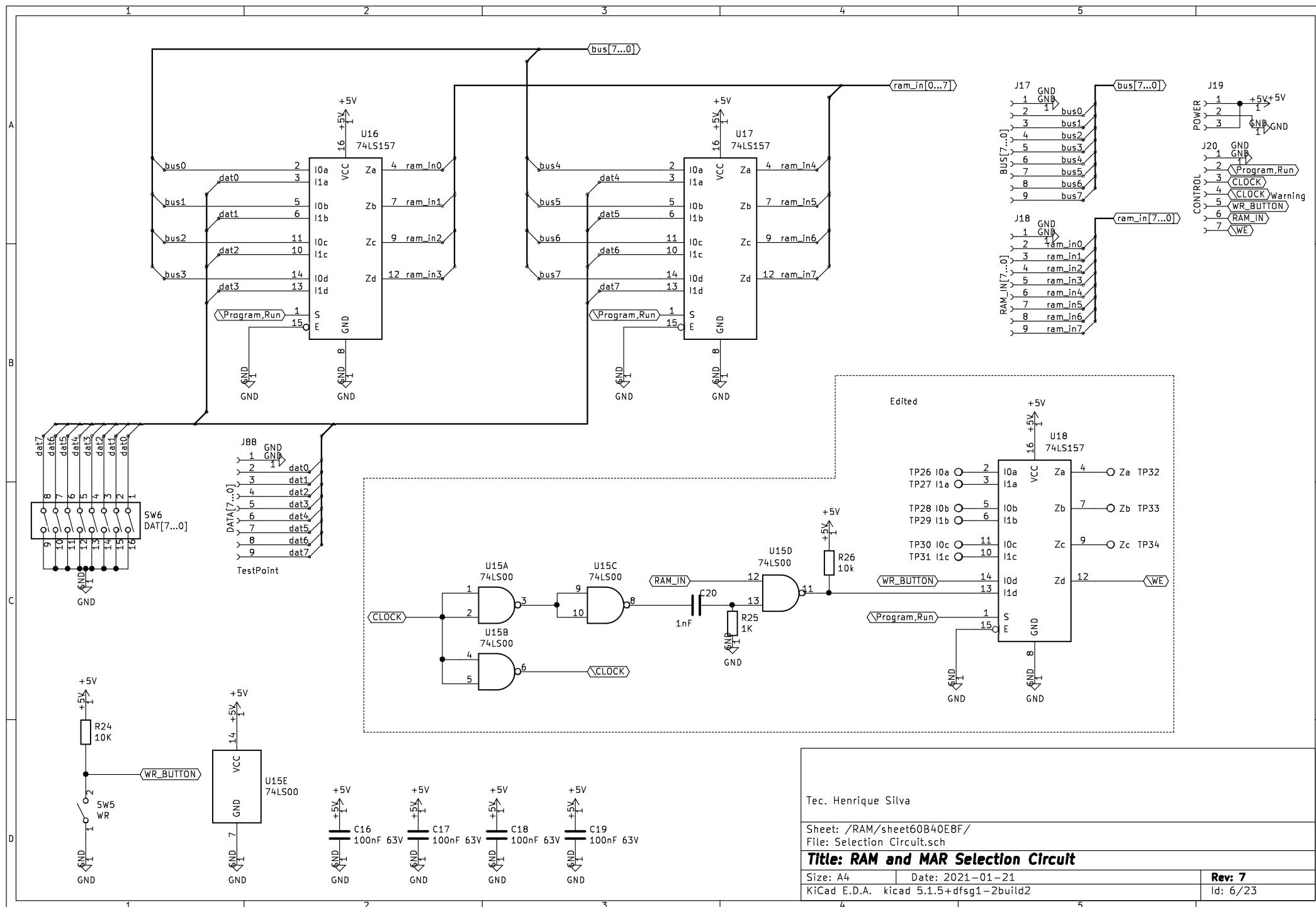
Sheet: /
File: 8-bit computer.sch**Title: 8 bit computer**Size: A4 Date: 2021-01-21
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2Rev: 5
Id: 1/23

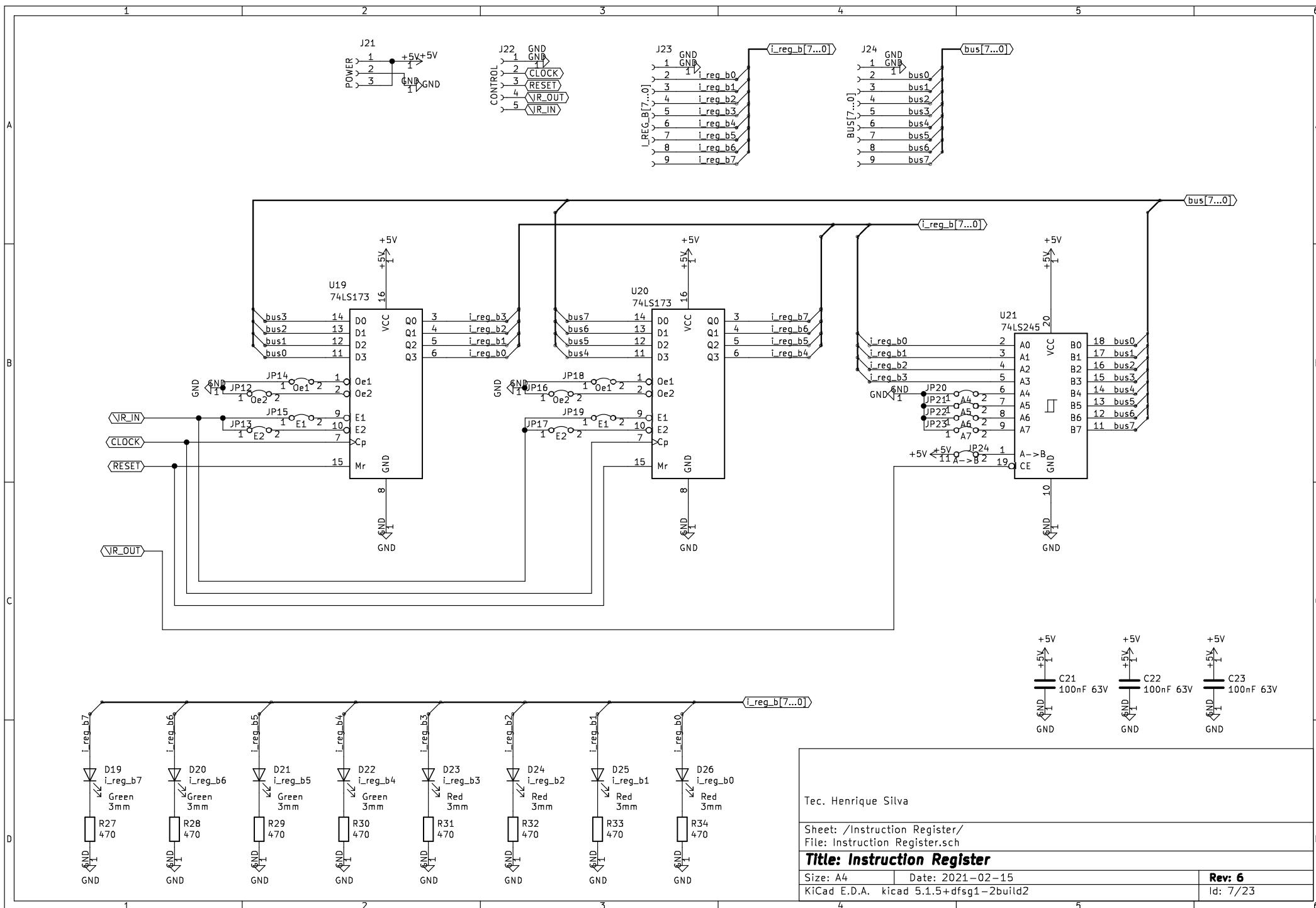


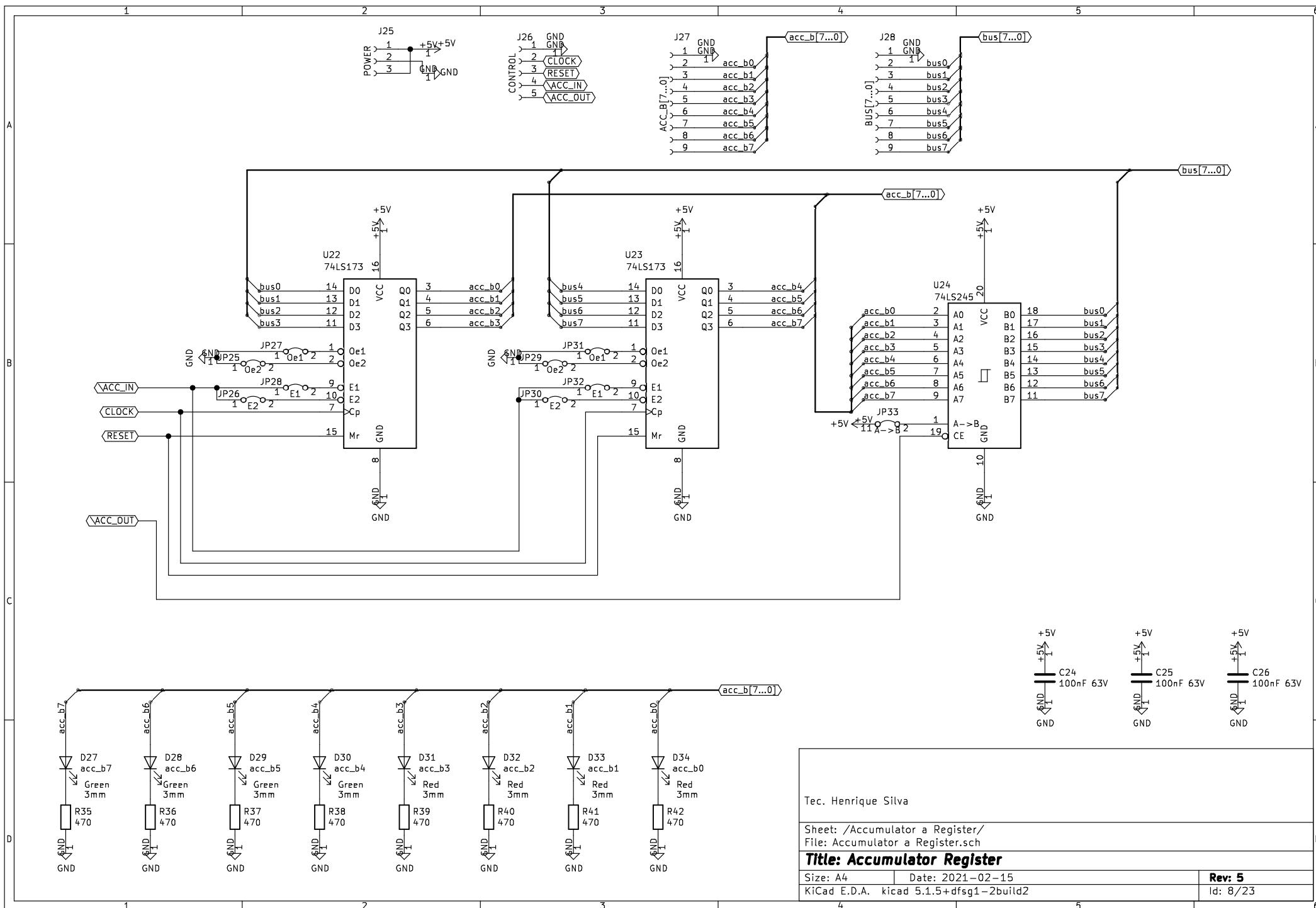


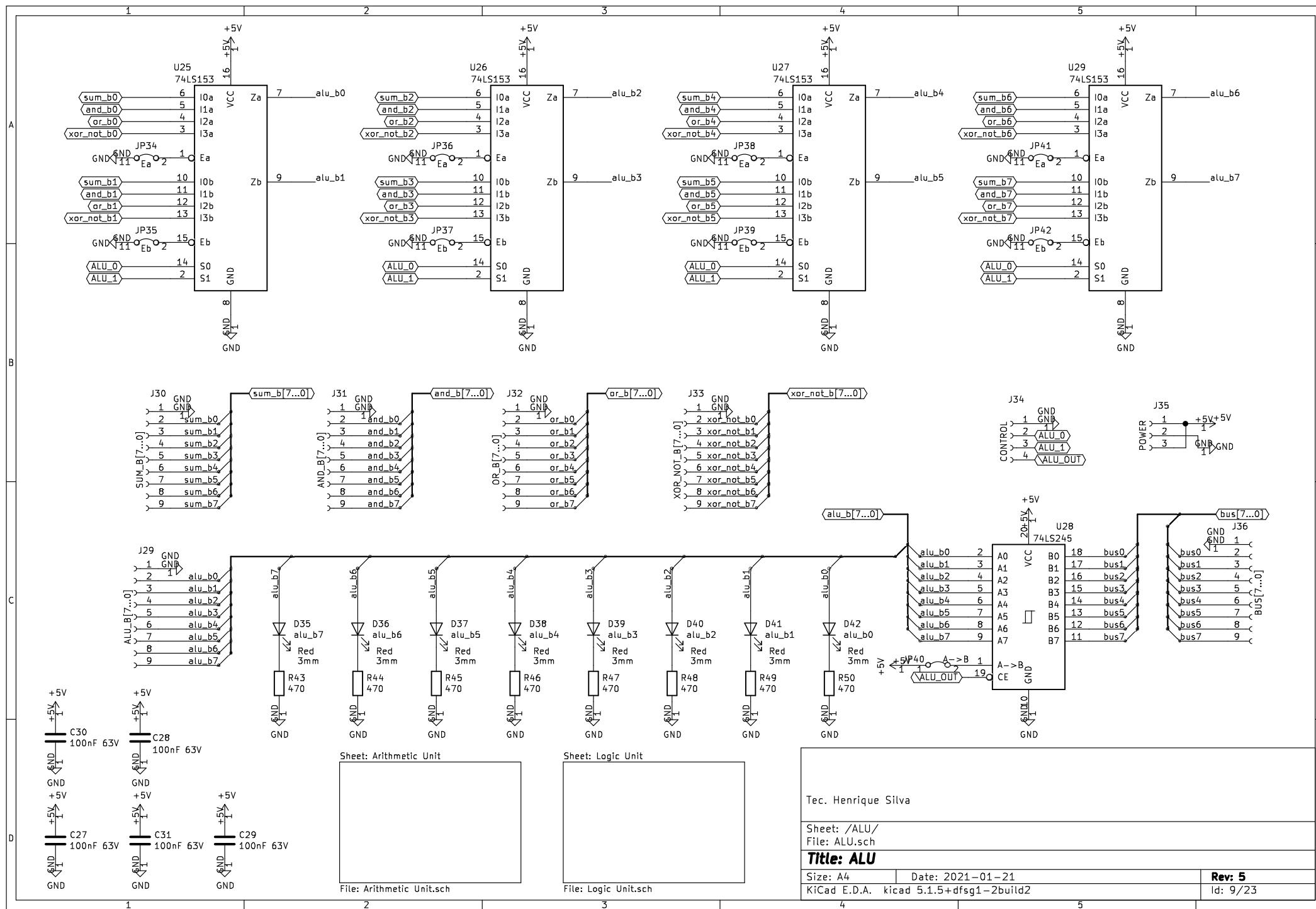


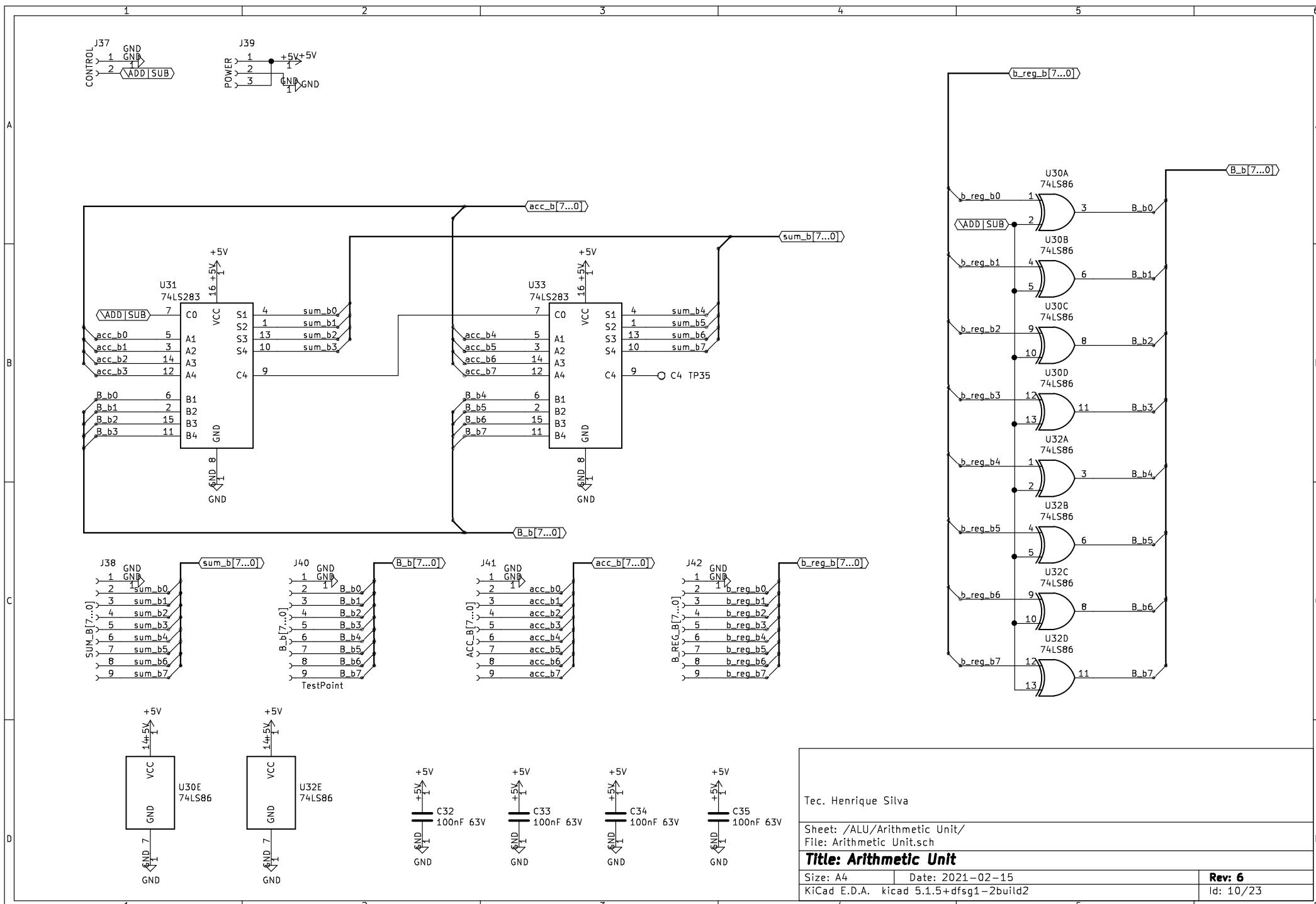


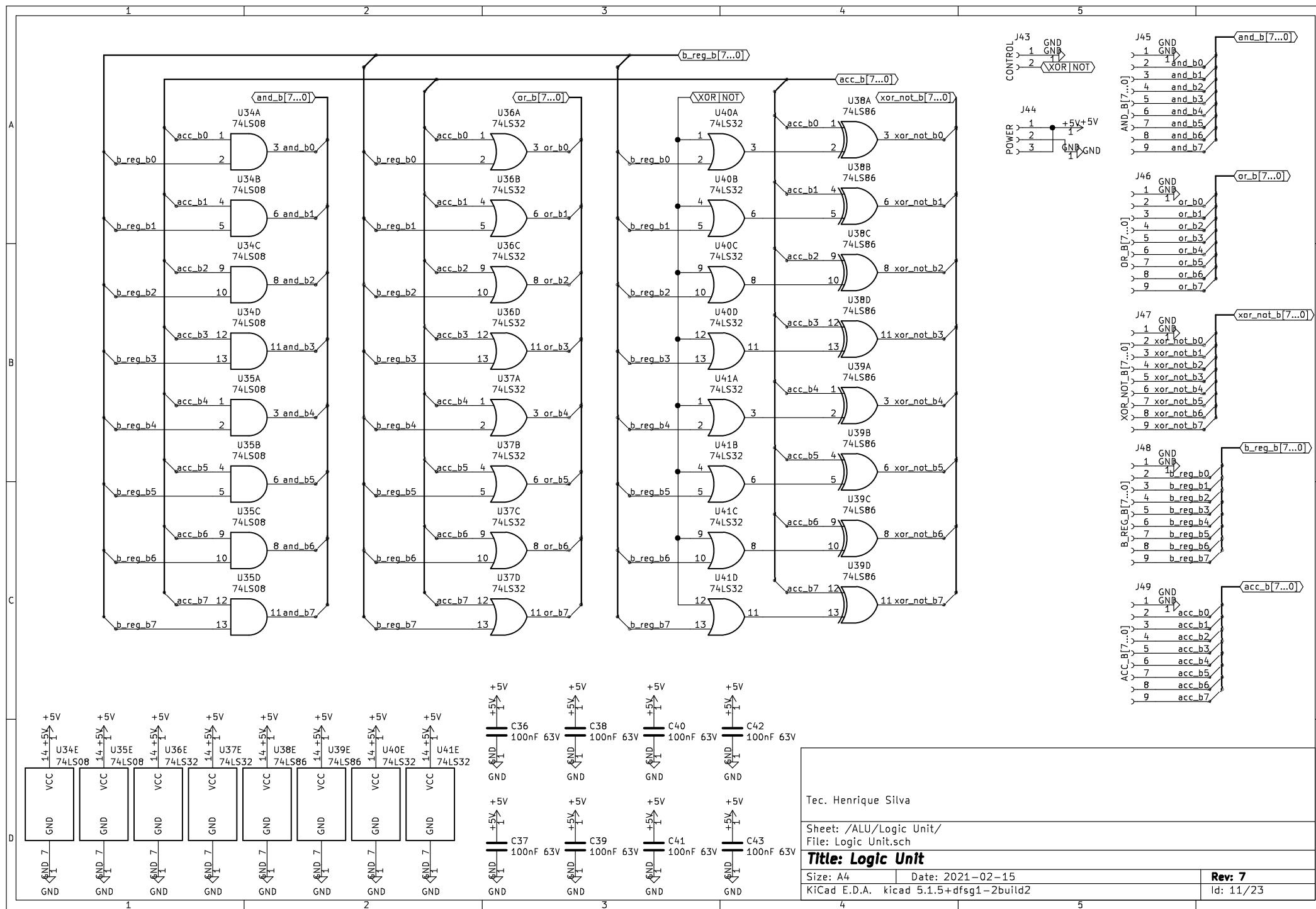


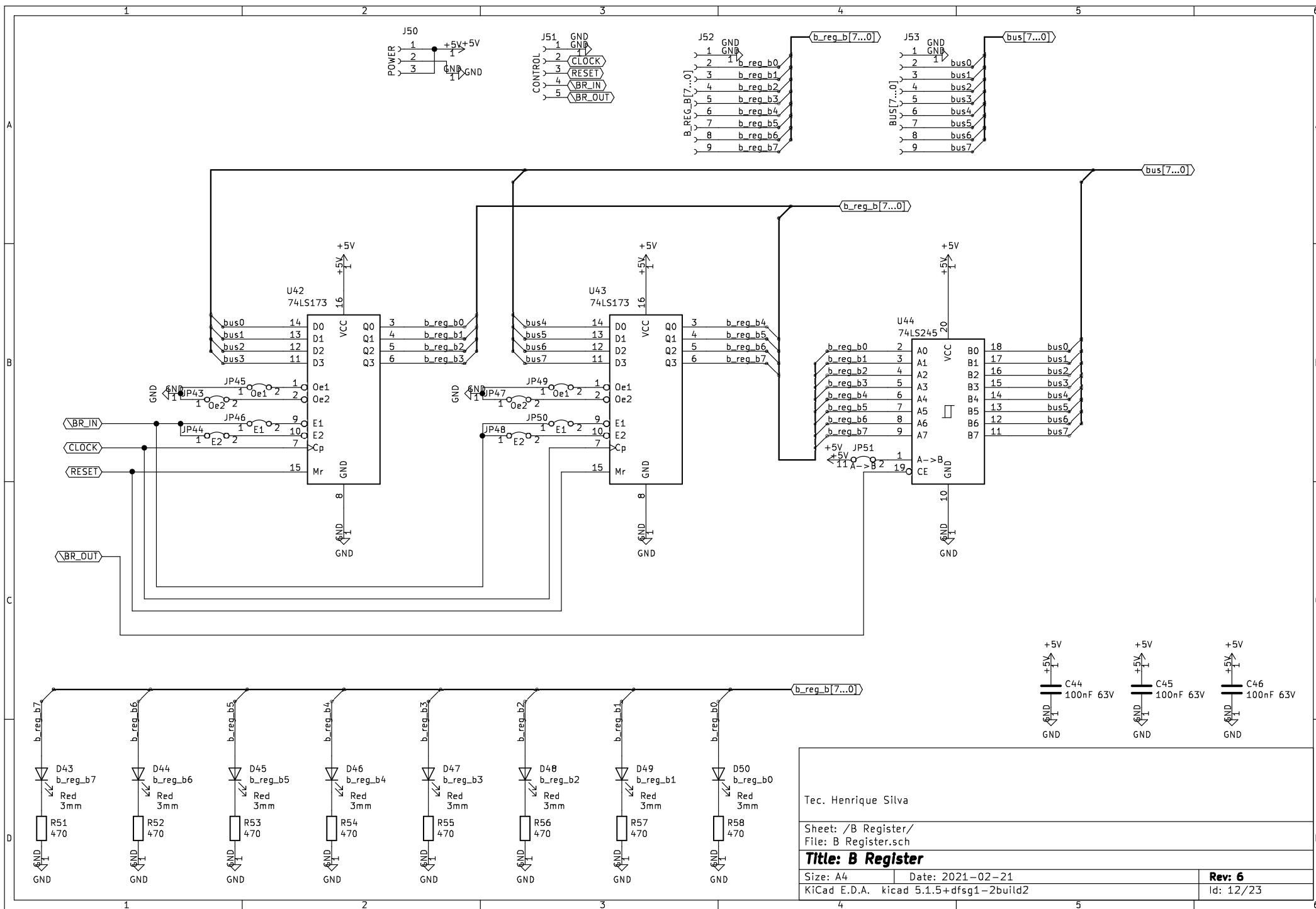


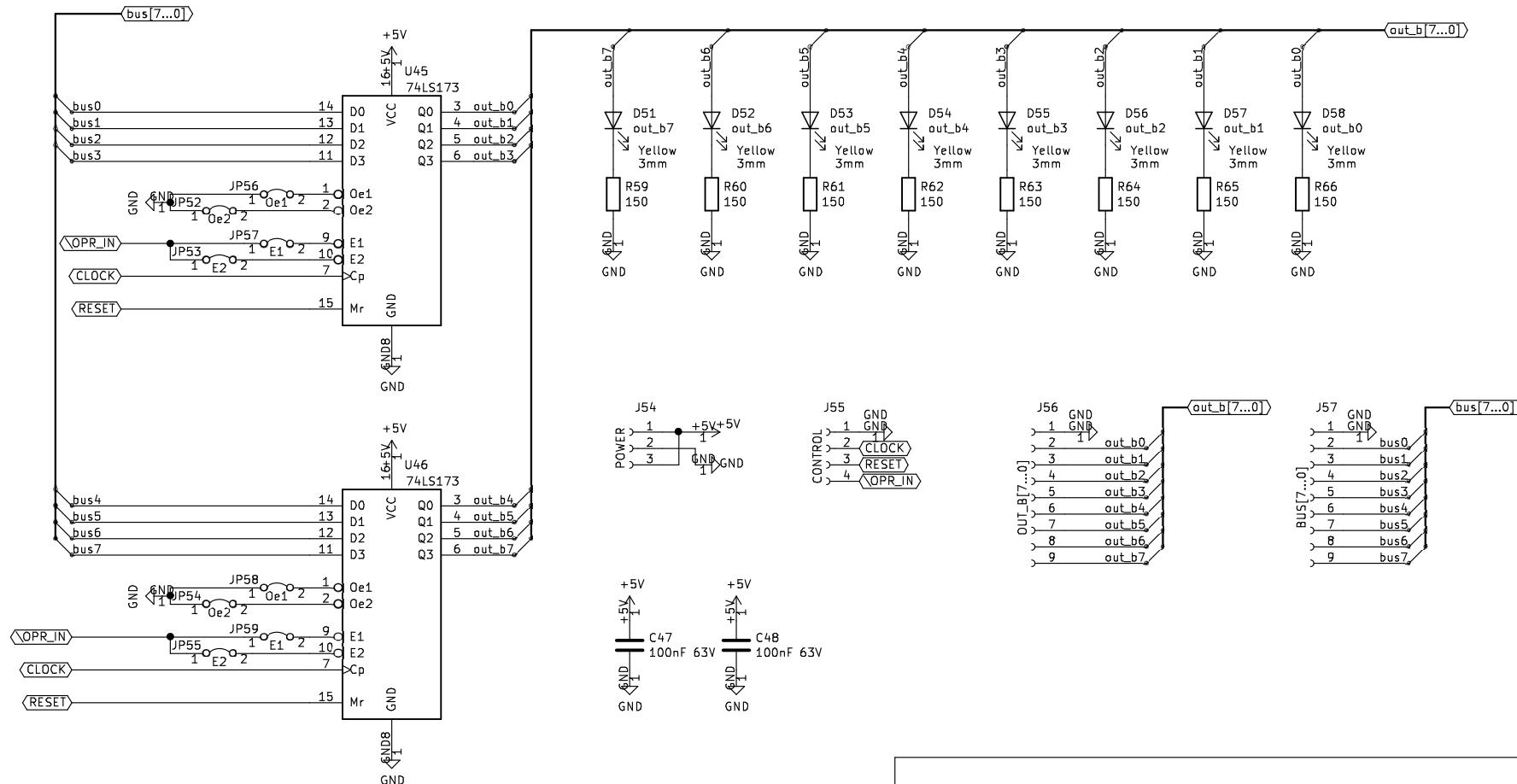












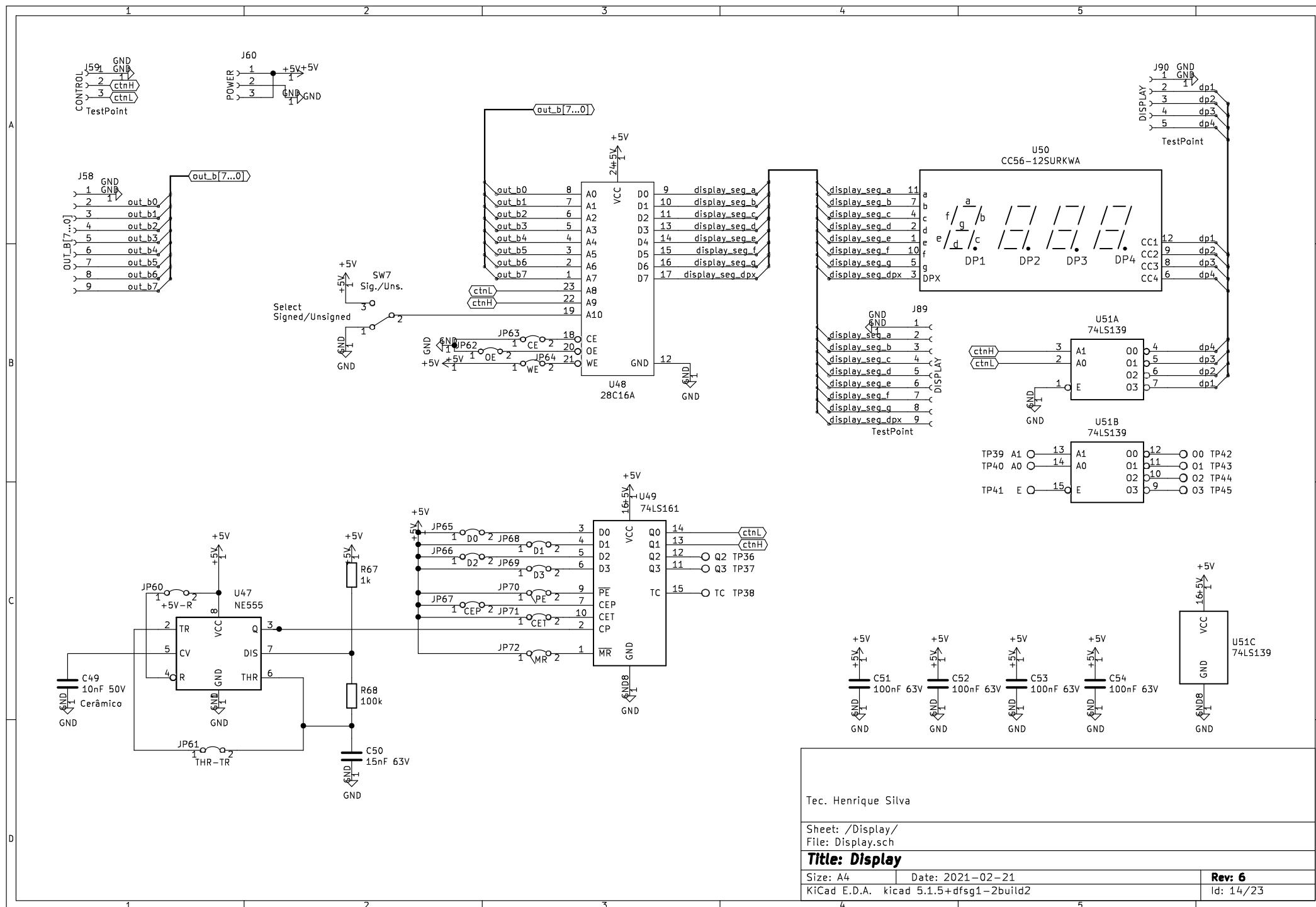
Tec. Henrique Silva

Sheet: /Output Register/
File: Output Register.sch

Title: Output Register

Size: A4 Date: 2021-02-21
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 6
Id: 13/23



A

Sheet: Clock Circuit



File: Clock Circuit.sch

Sheet: Reset Circuit



File: Reset Circuit.sch

Sheet: Control Word



File: Control Word.sch

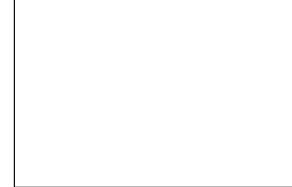
B

Sheet: Control Decoder



File: Control Decoder.sch

Sheet: Search and Execution Counter



File: Search and Execution Counter.sch

Sheet: Bus Simulator



File: Bus Simulator.sch

C

Sheet: Control Decoder Simulator



File: Control Decoder Simulator.sch

D

Tec. Henrique Silva

Sheet: /Control/

File: Control.sch

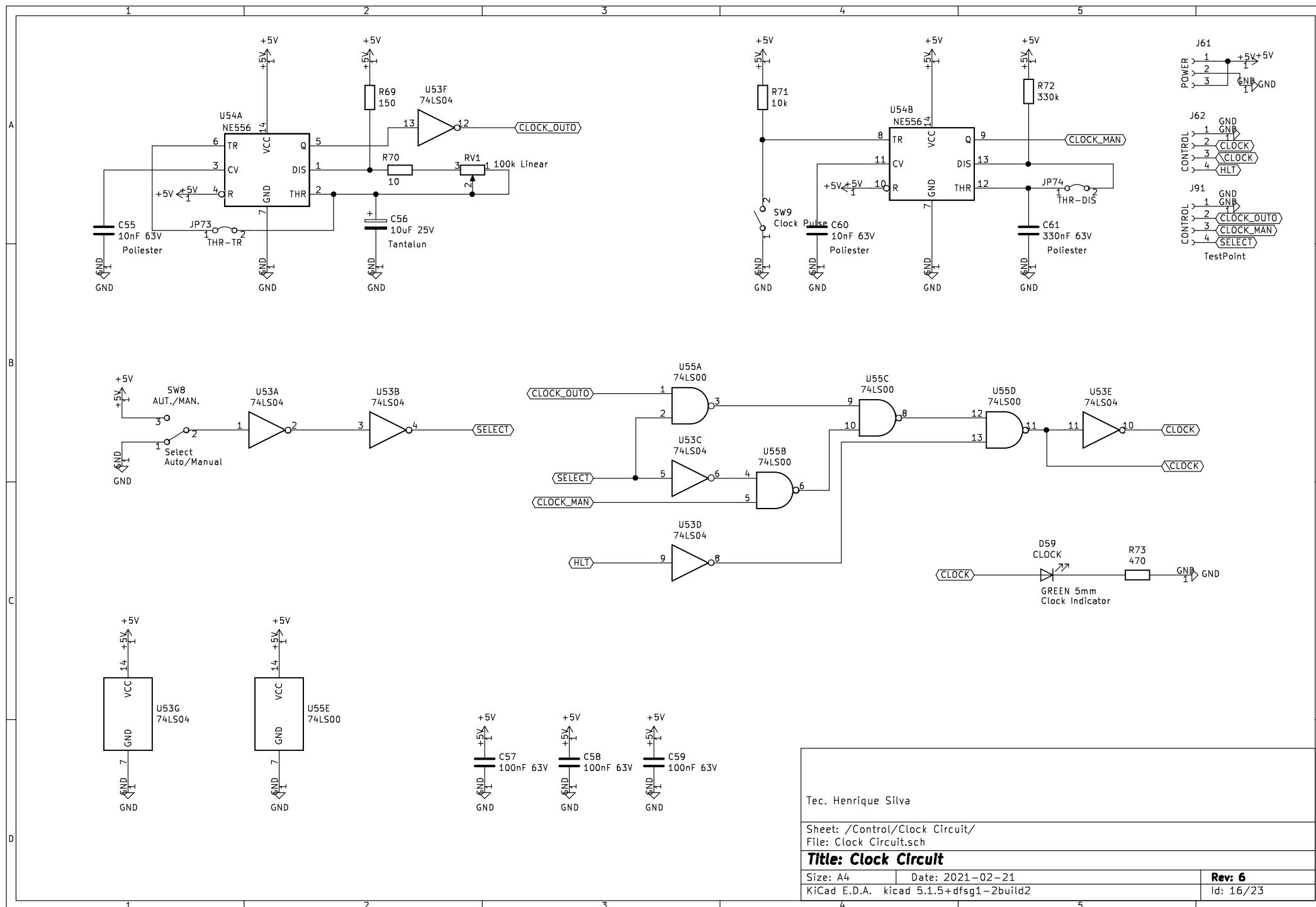
Title: Control

Size: A4 Date: 2021-01-21

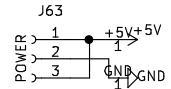
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 5

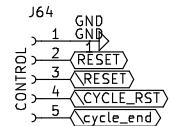
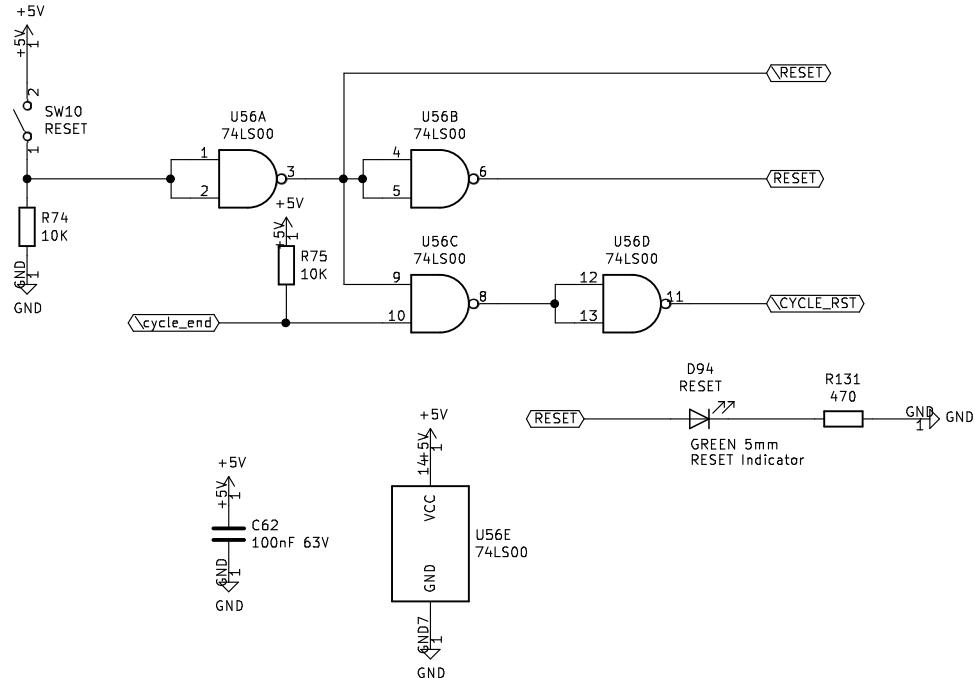
Id: 15/23



A



B



C

D

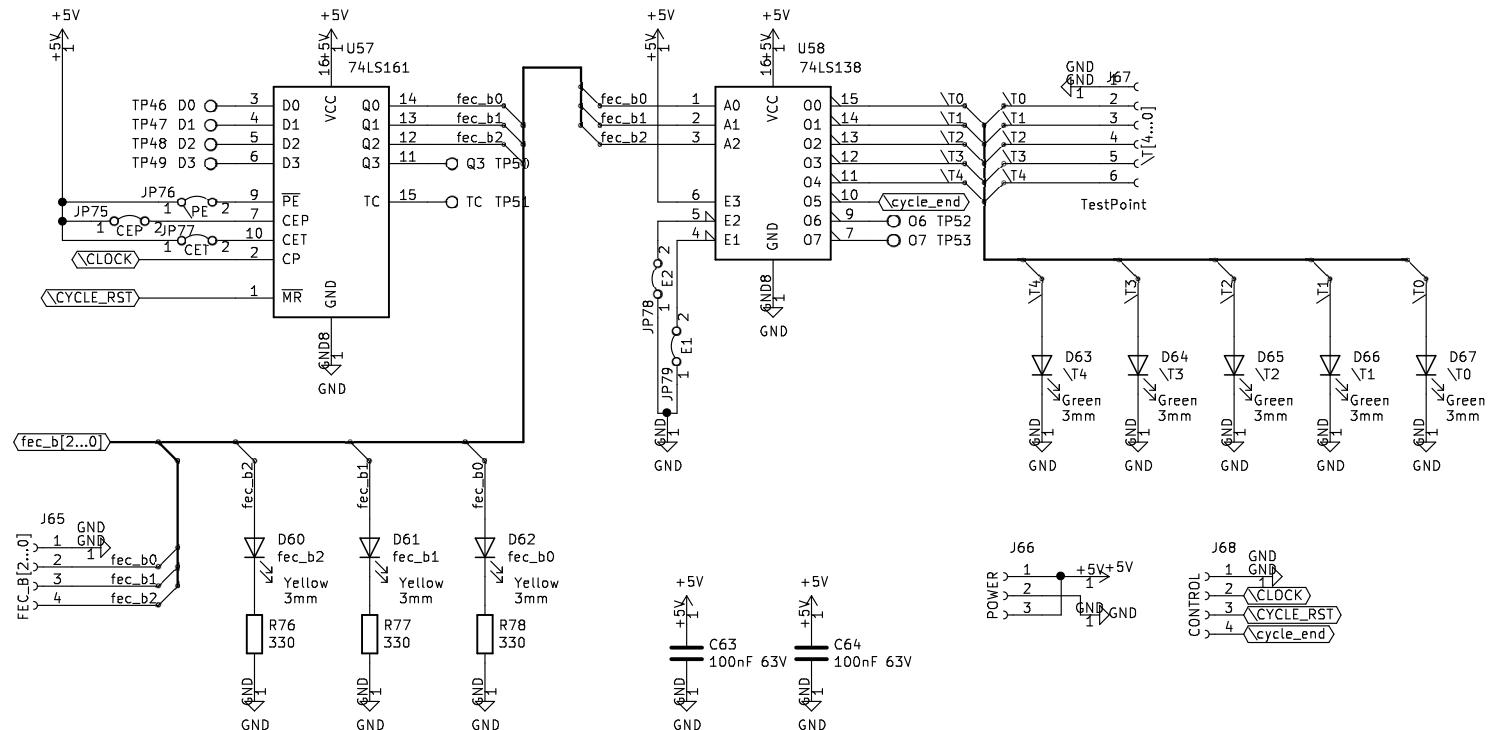
Tec. Henrique Silva

Sheet: /Control/Reset Circuit/
File: Reset Circuit.sch

Title: Reset Circuit

Size: A4 Date: 2021-02-21
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 6
Id: 17/23



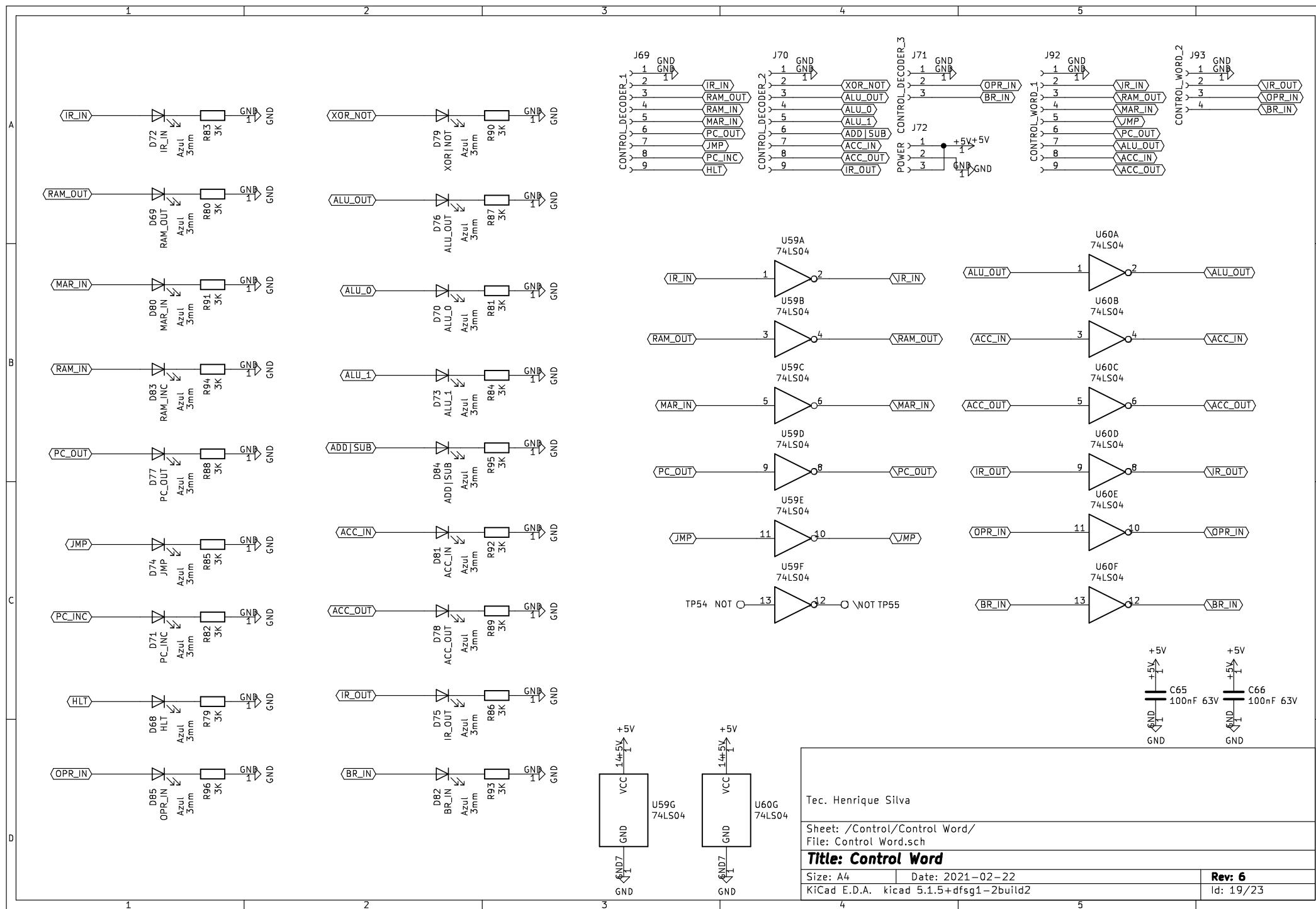
Tec. Henrique Silva

Sheet: /Control/Search and Execution Counter/
File: Search and Execution Counter.sch

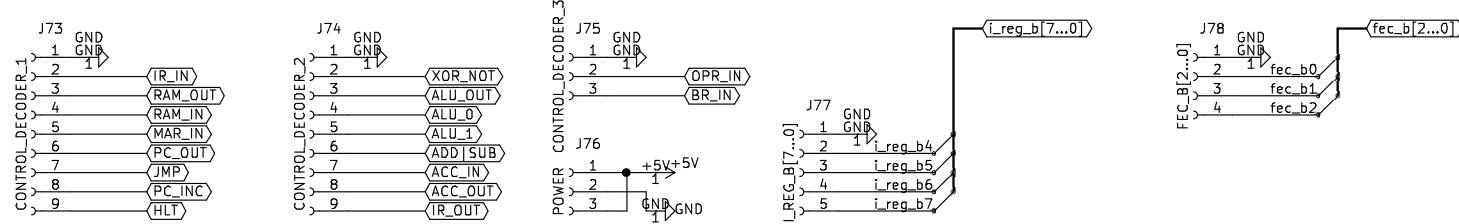
Title: Search and Execution Counter

Size: A4 Date: 2021-02-22

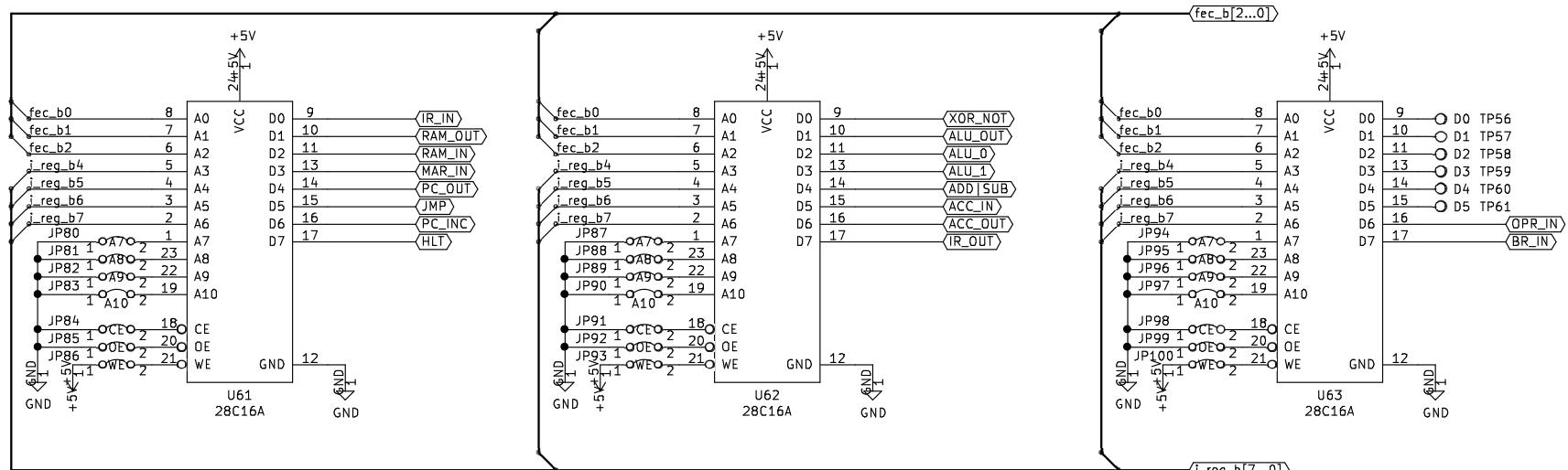
Rev: 6



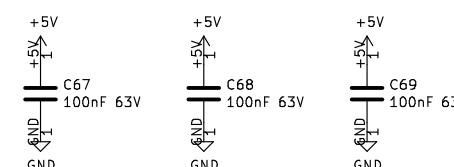
A



B



C



D

Tec. Henrique Silva

Sheet: /Control/Control Decoder/
File: Control Decoder.sch

Title: Control Decoder

Size: A4 Date: 2021-02-22

KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 6

Id: 20/23

A

A

B

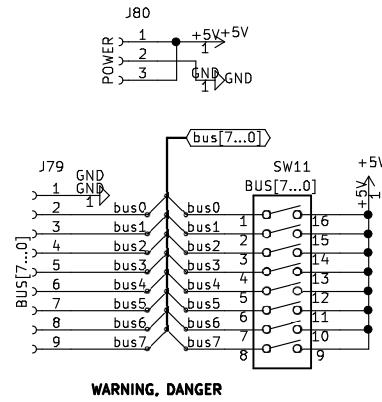
B

C

C

D

D



Tec. Henrique Silva

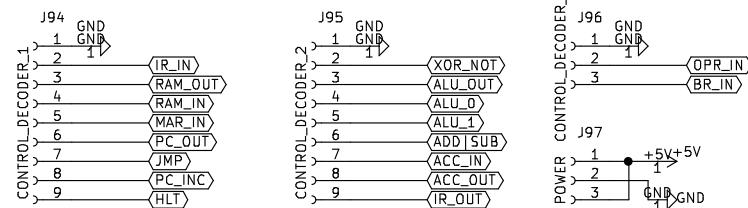
Sheet: /Control/Bus Simulator/
File: Bus Simulator.sch

Title: Bus Simulator

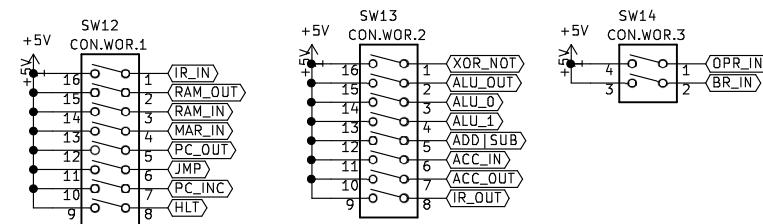
Size: A4	Date: 2021-01-21
KiCad E.D.A.	kicad 5.1.5+dfsg1-2build2

Rev: 6
Id: 21/23

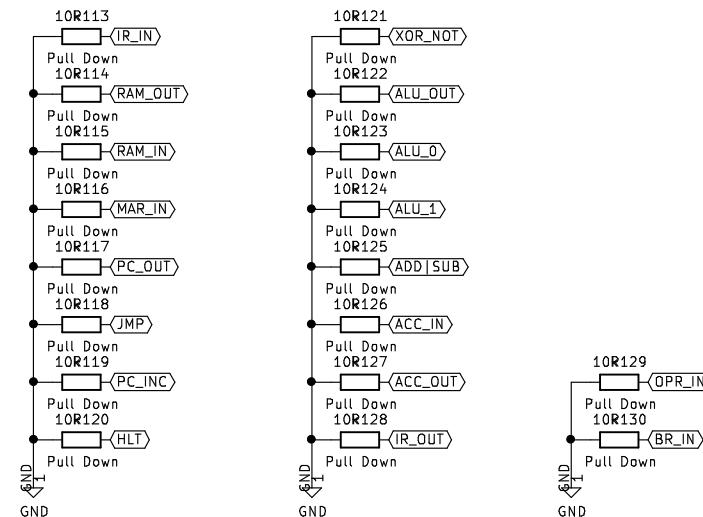
A



B



C



D

Tec. Henrique Silva

Sheet: /Control/Control Decoder Simulator/
 File: Control Decoder Simulator.sch

Title: Control Decoder Simulator

Size: A4 Date: 2021-02-22

KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 6

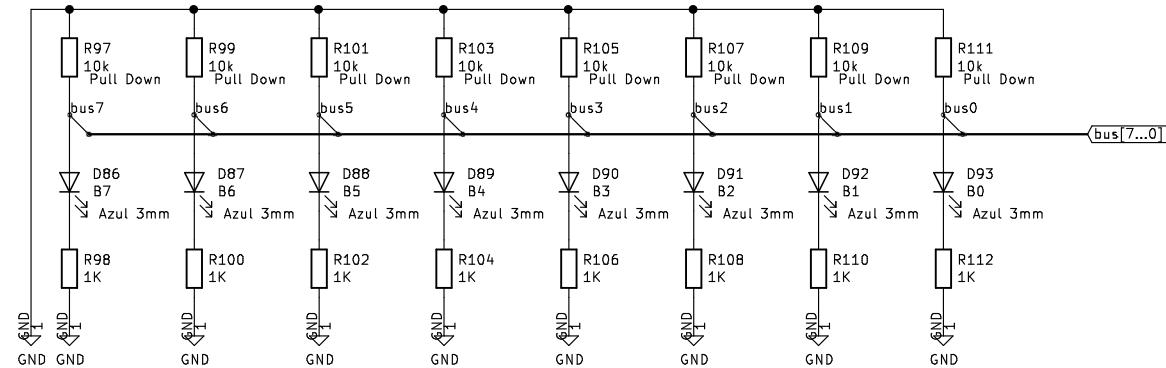
Id: 22/23

A

A

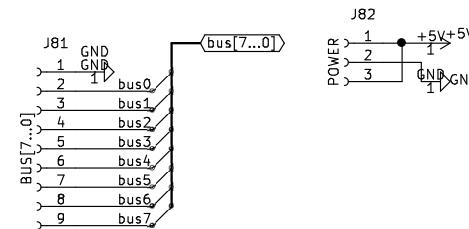
B

B



C

C



D

D

Tec. Henrique Silva

Sheet: /BUS/
File: BUS.sch

Title: BUS

Size: A4 | Date: 2021-01-21
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

Rev: 6
Id: 23/23