

A

**Sheet: Program Counter**

File: Program Counter.sch

**Sheet: Accumulator a Register**

File: Accumulator a Register.sch

**Sheet: Display**

File: Display.sch

**Sheet: Control**

File: Control.sch

B

**Sheet: MAR**

File: MAR.sch

**Sheet: ALU**

File: ALU.sch

**Sheet: Output Register**

File: Output Register.sch

**Sheet: Instruction Register**

File: Instruction Register.sch

C

**Sheet: RAM**

File: RAM.sch

**Sheet: B Register**

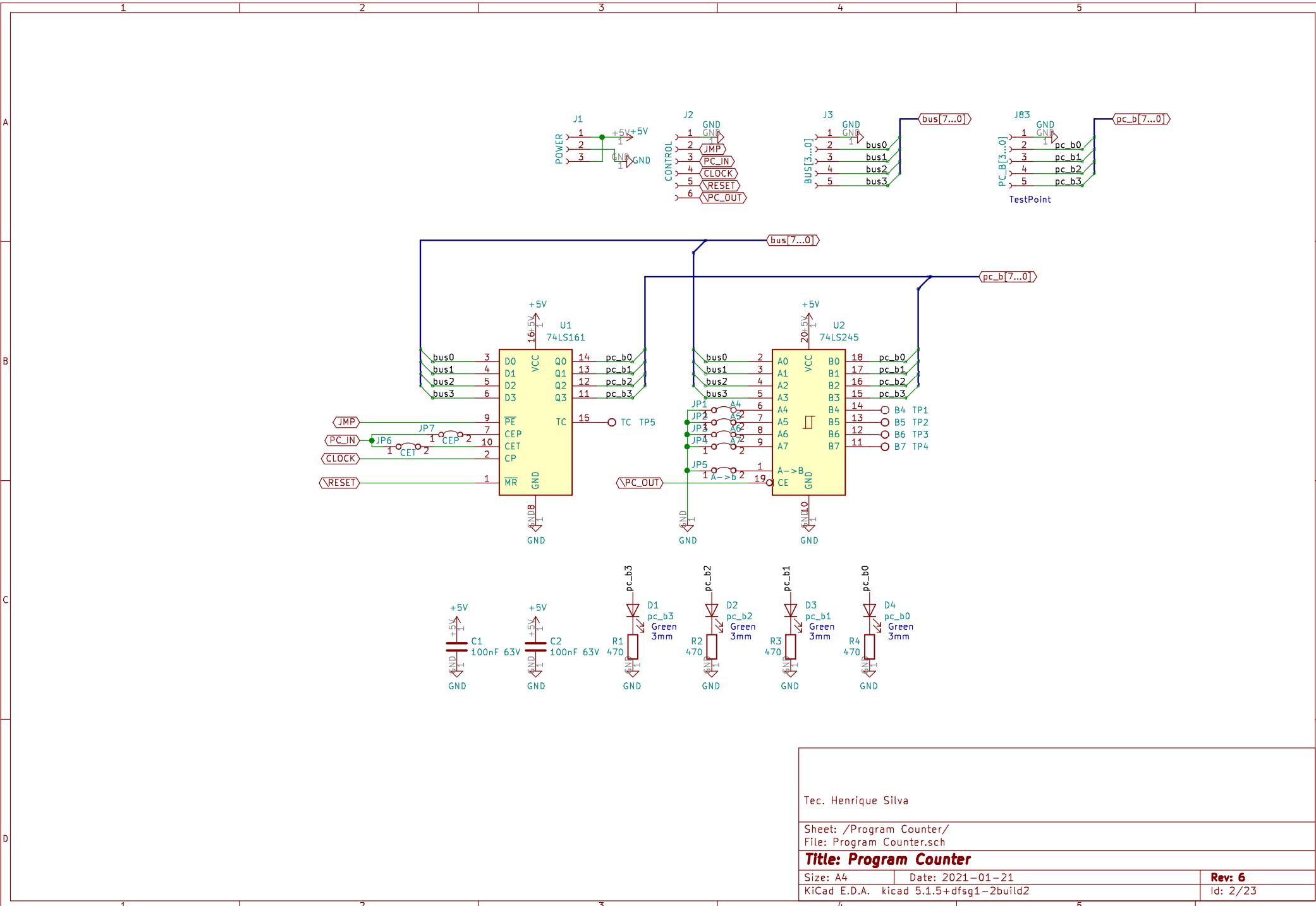
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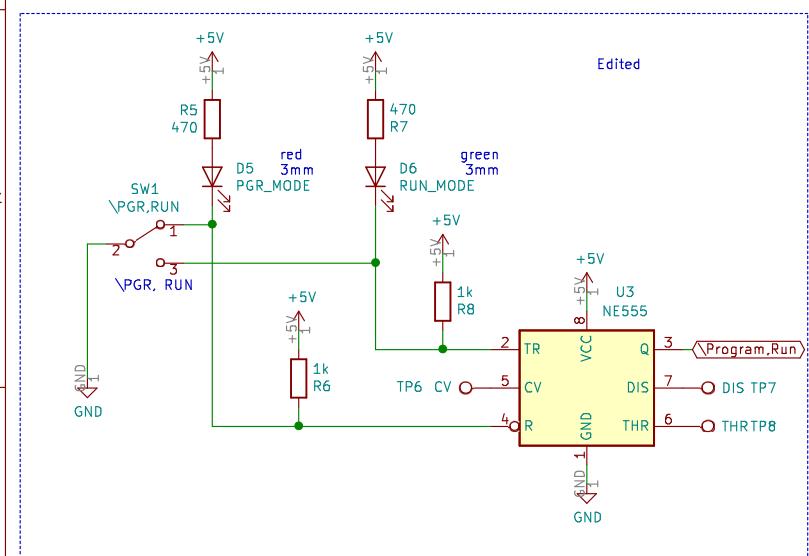
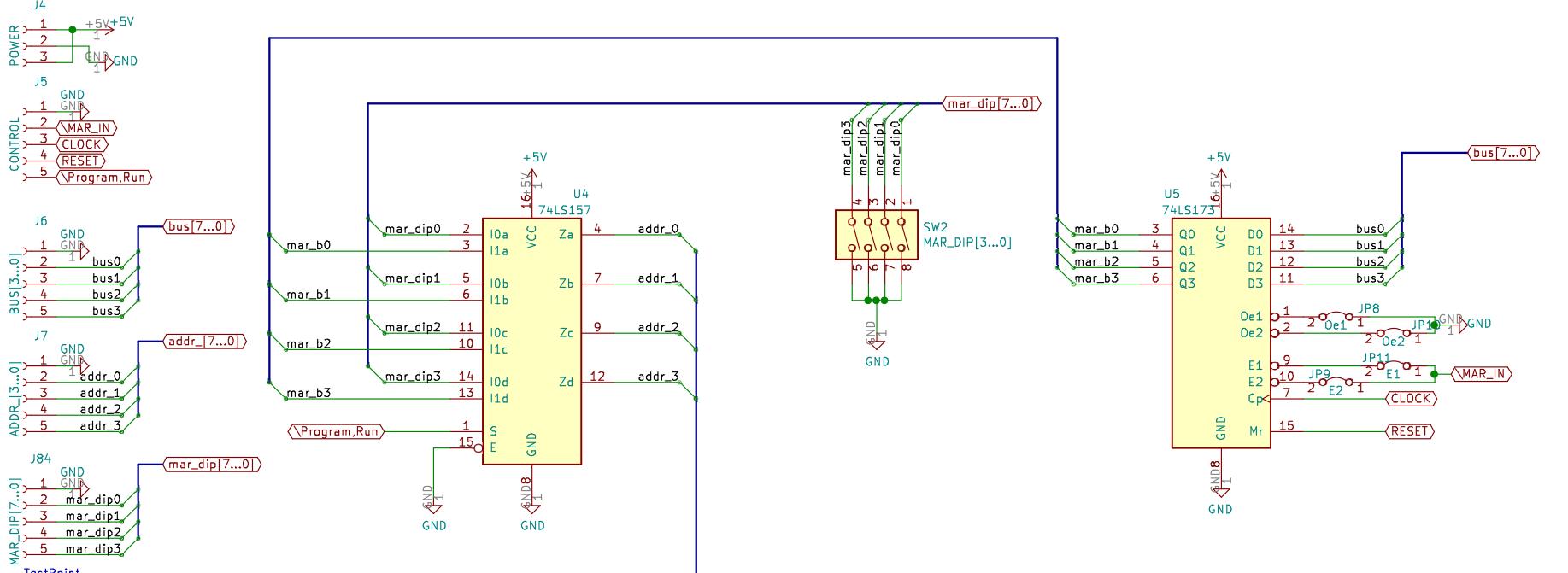
**Sheet: BUS**

File: BUS.sch

Tec. Henrique Silva

Sheet: /  
File: 8-bit computer.sch**Title: 8 bit computer**Size: A4 | Date: 2021-01-21  
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2**Rev: 5**  
Id: 1/23



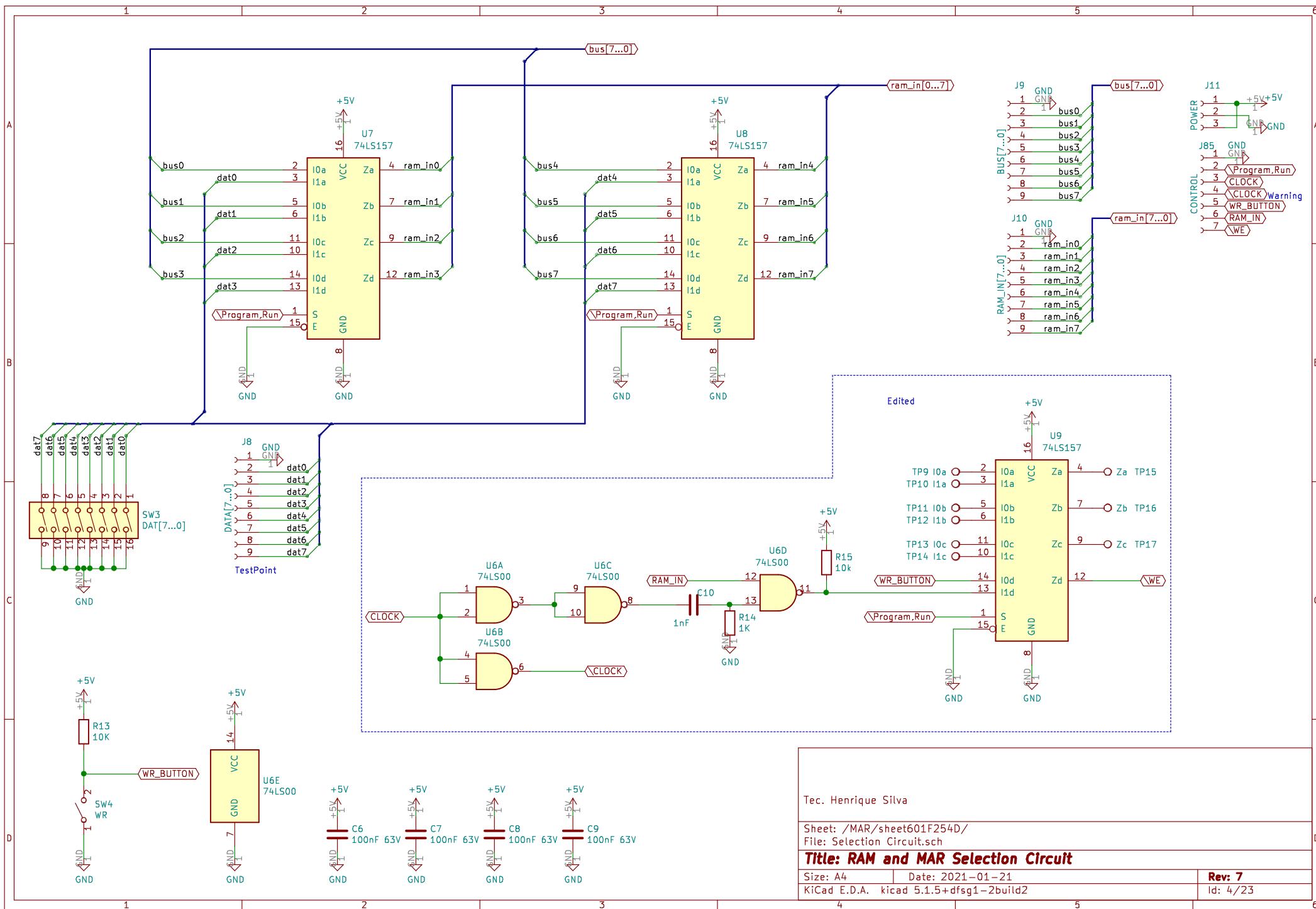


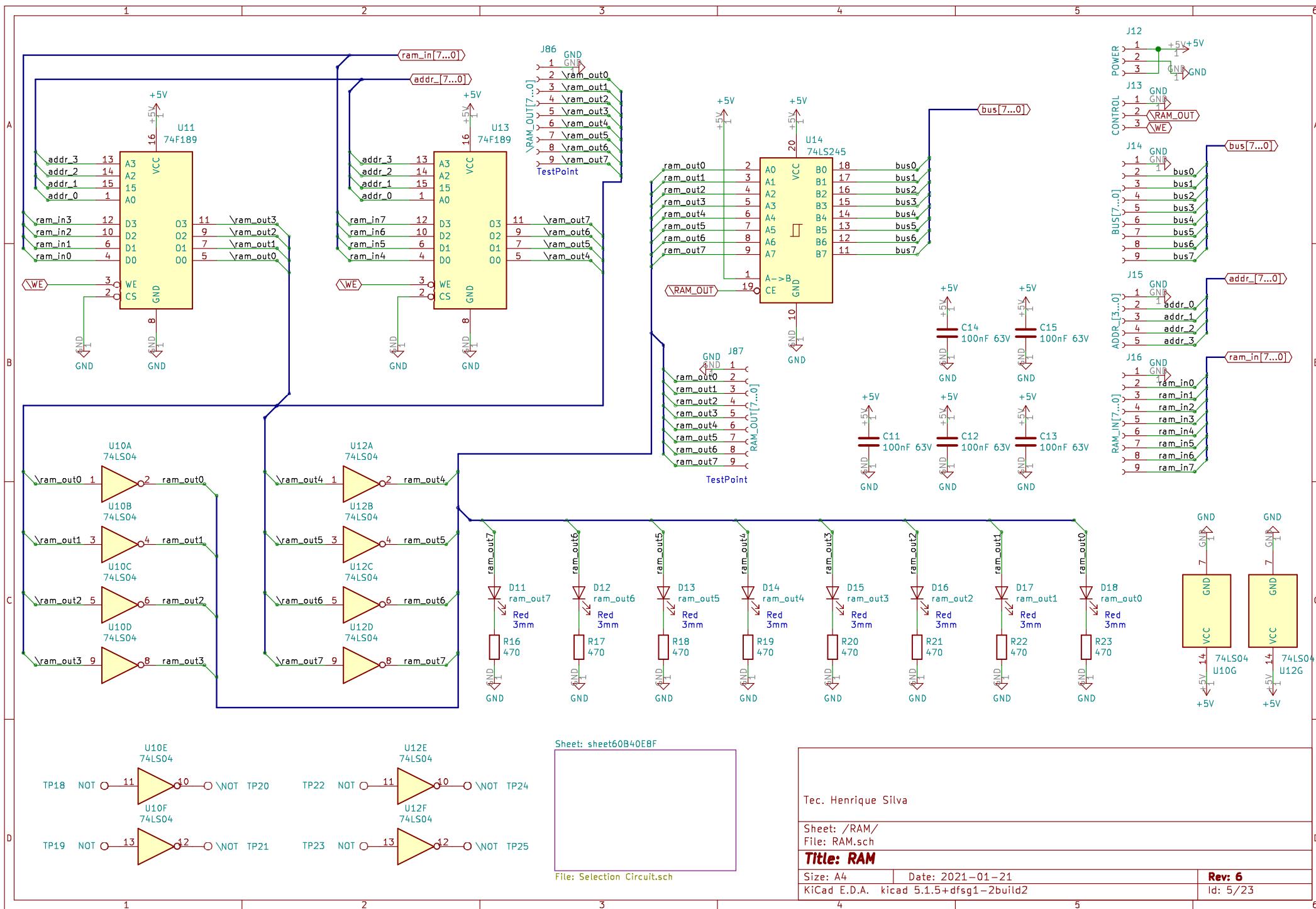
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File: Selection Circuit.sch

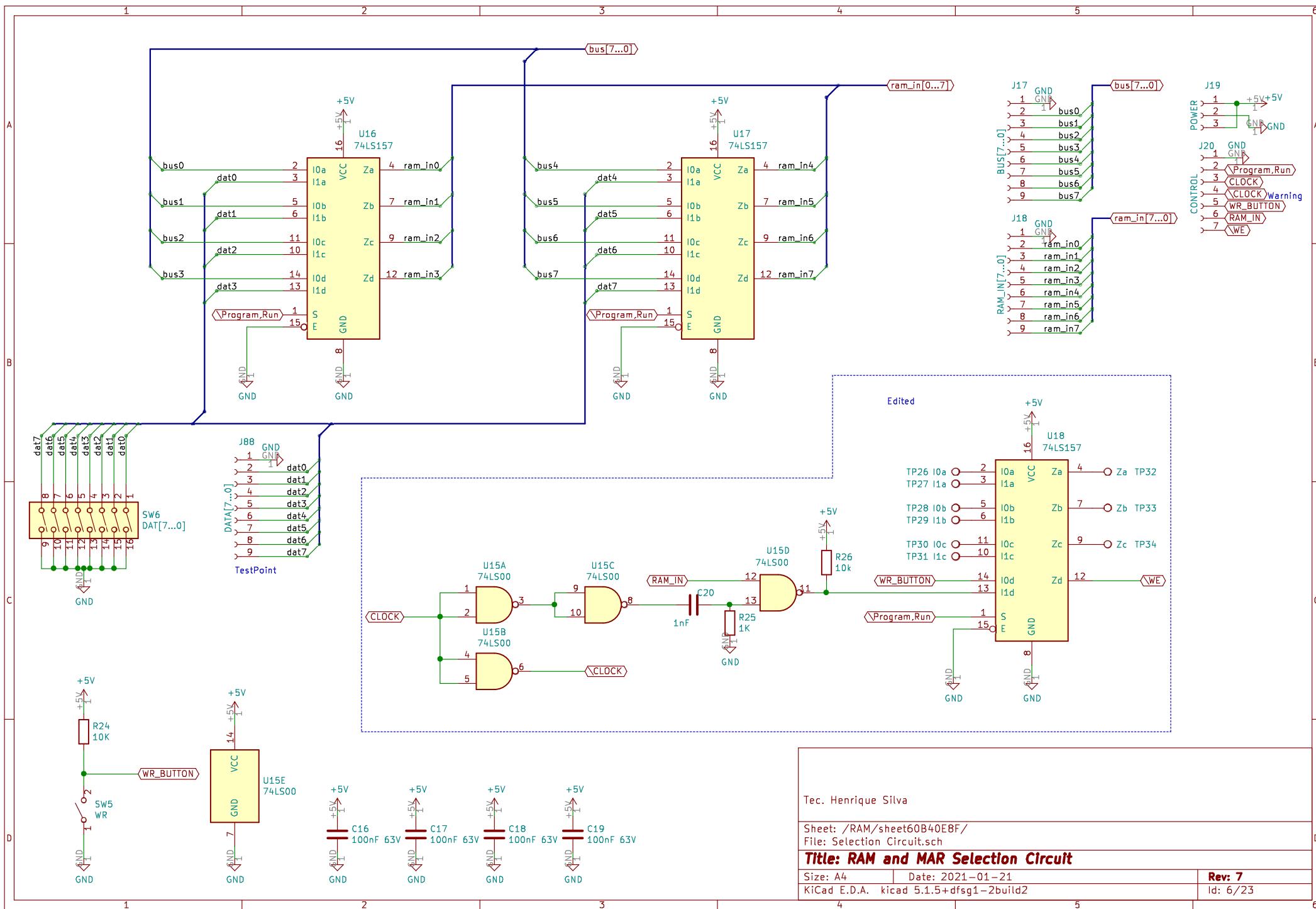
Tec. Henrique Silva

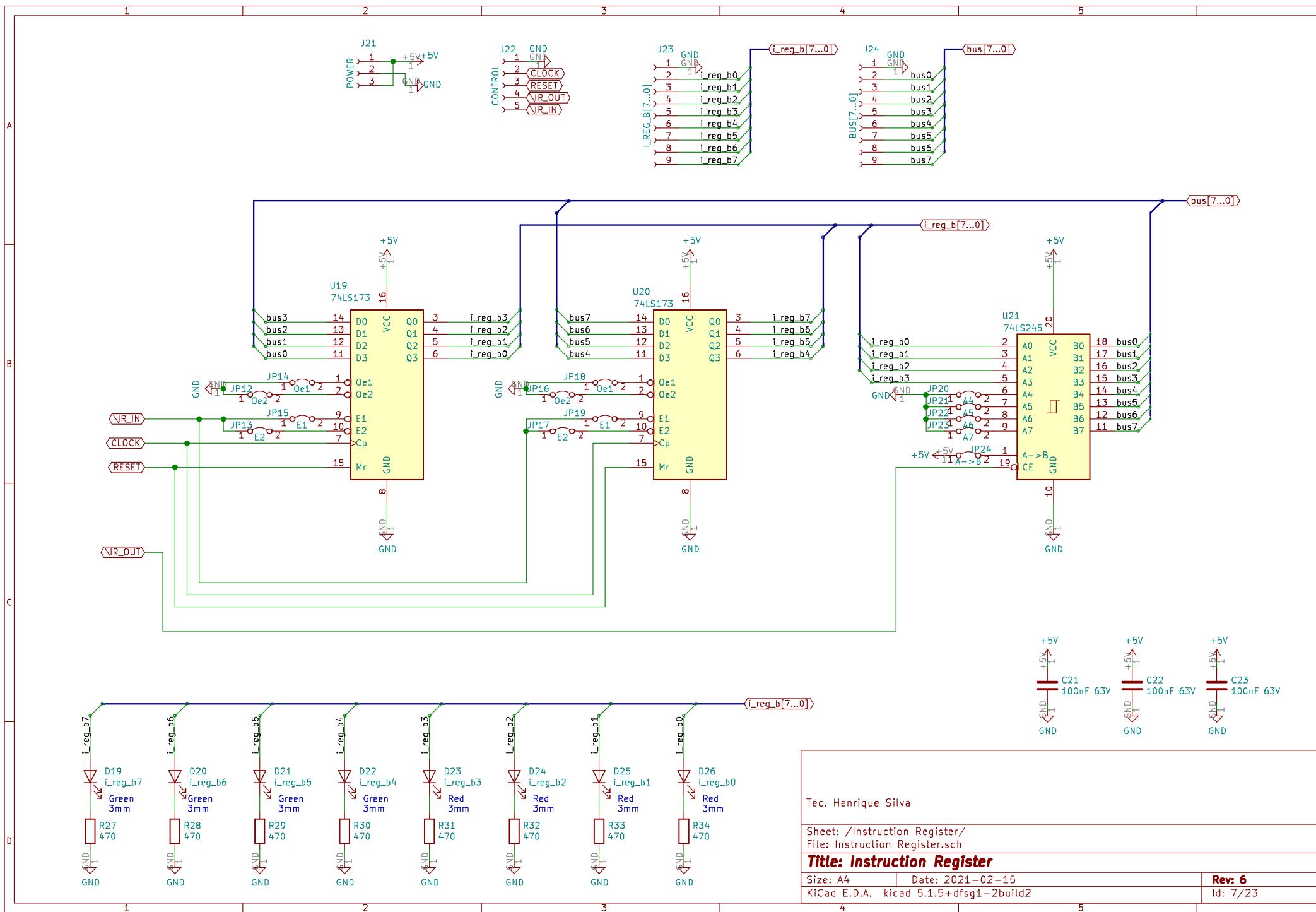
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**Title: MAR**  
Size: A4 | Date: 2021-01-21  
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

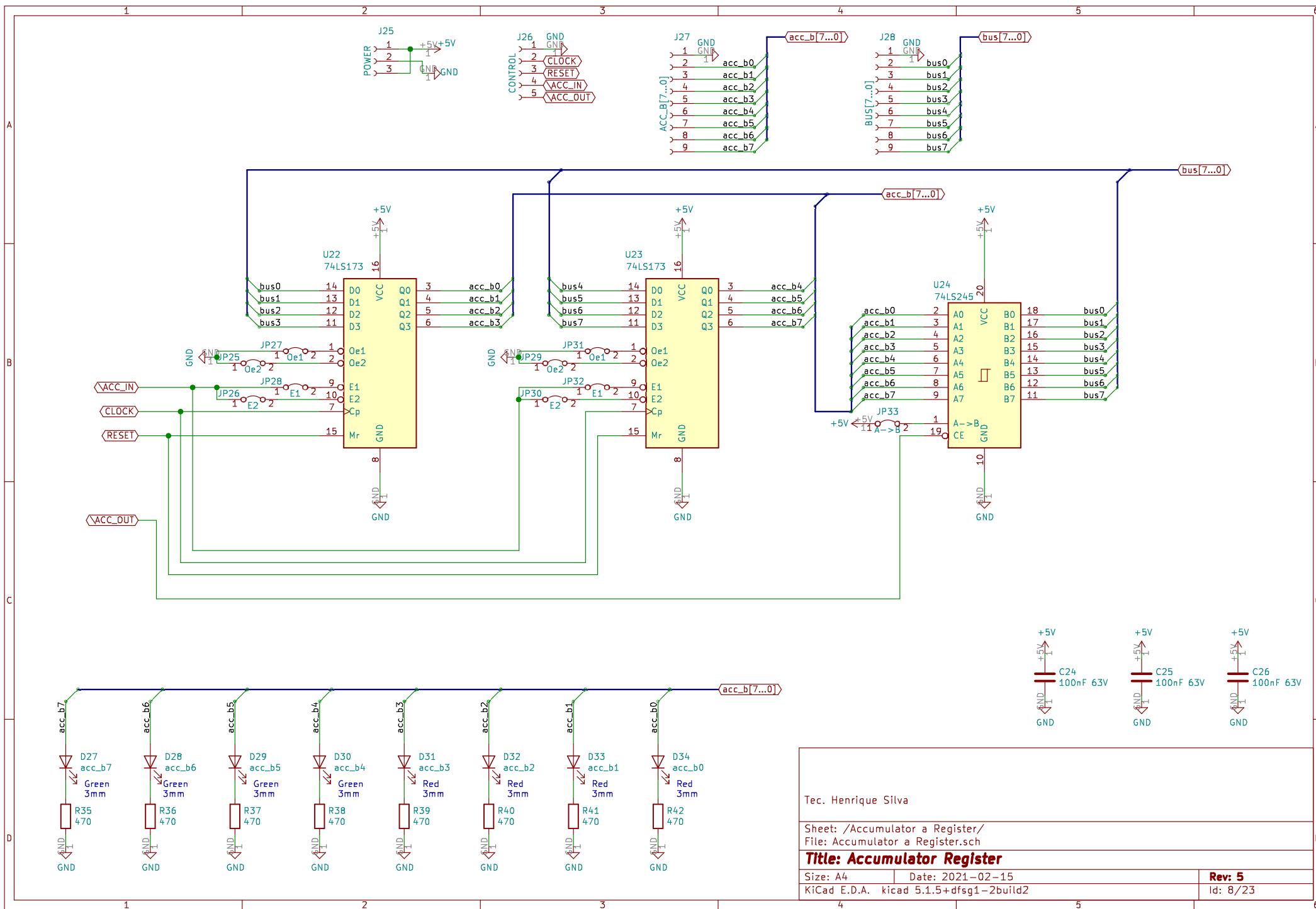
**Rev: 6**  
Id: 3/23

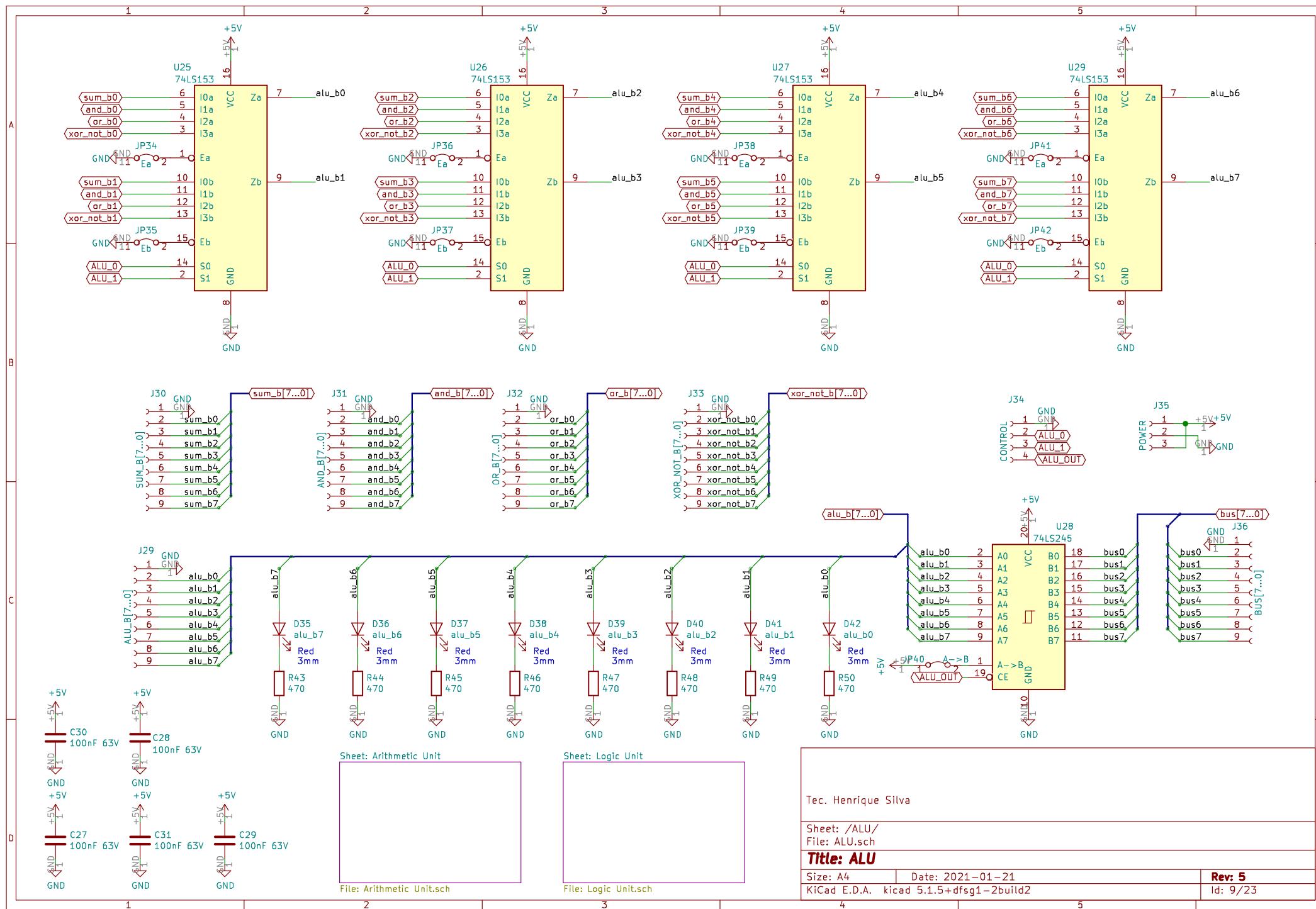


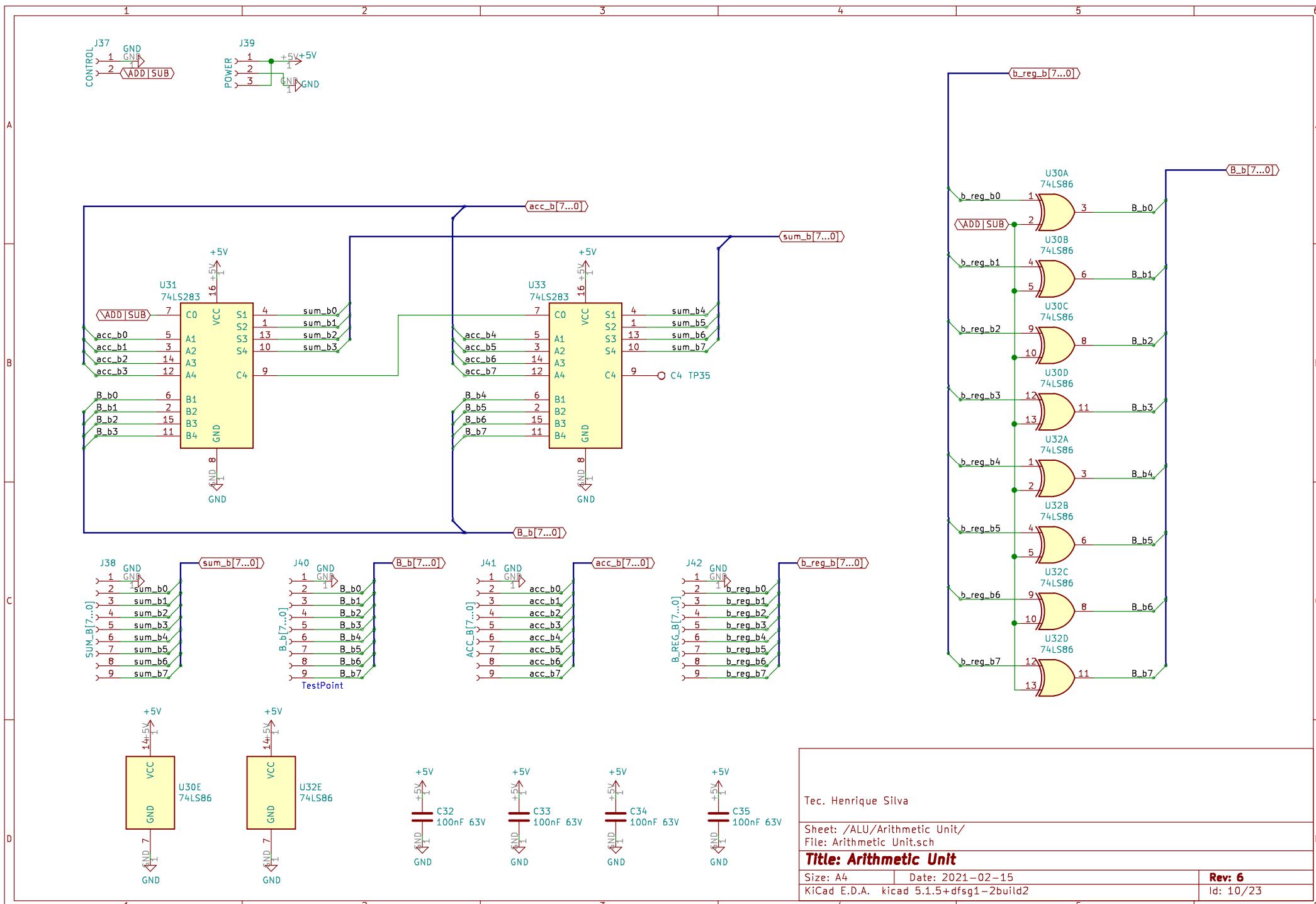


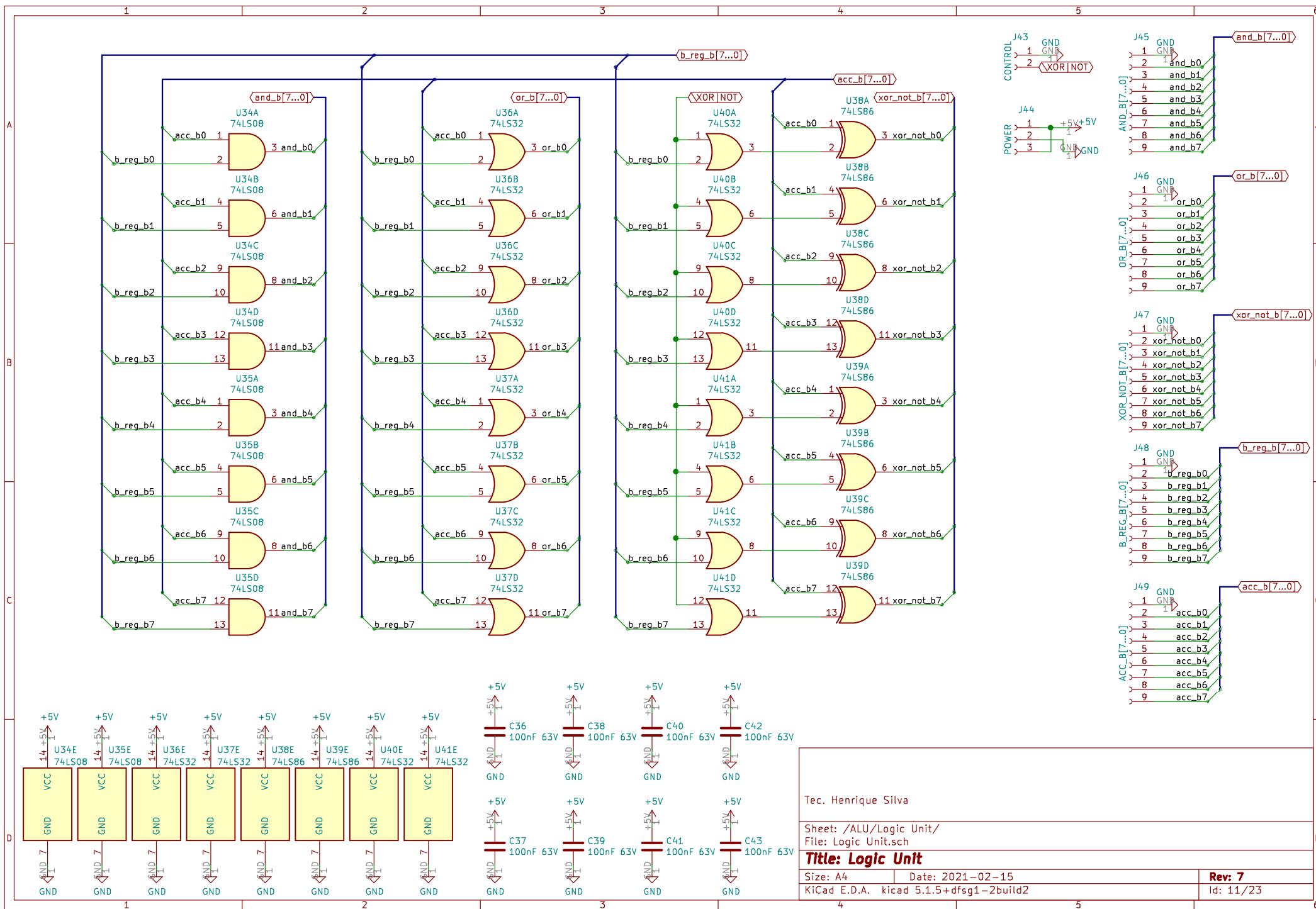


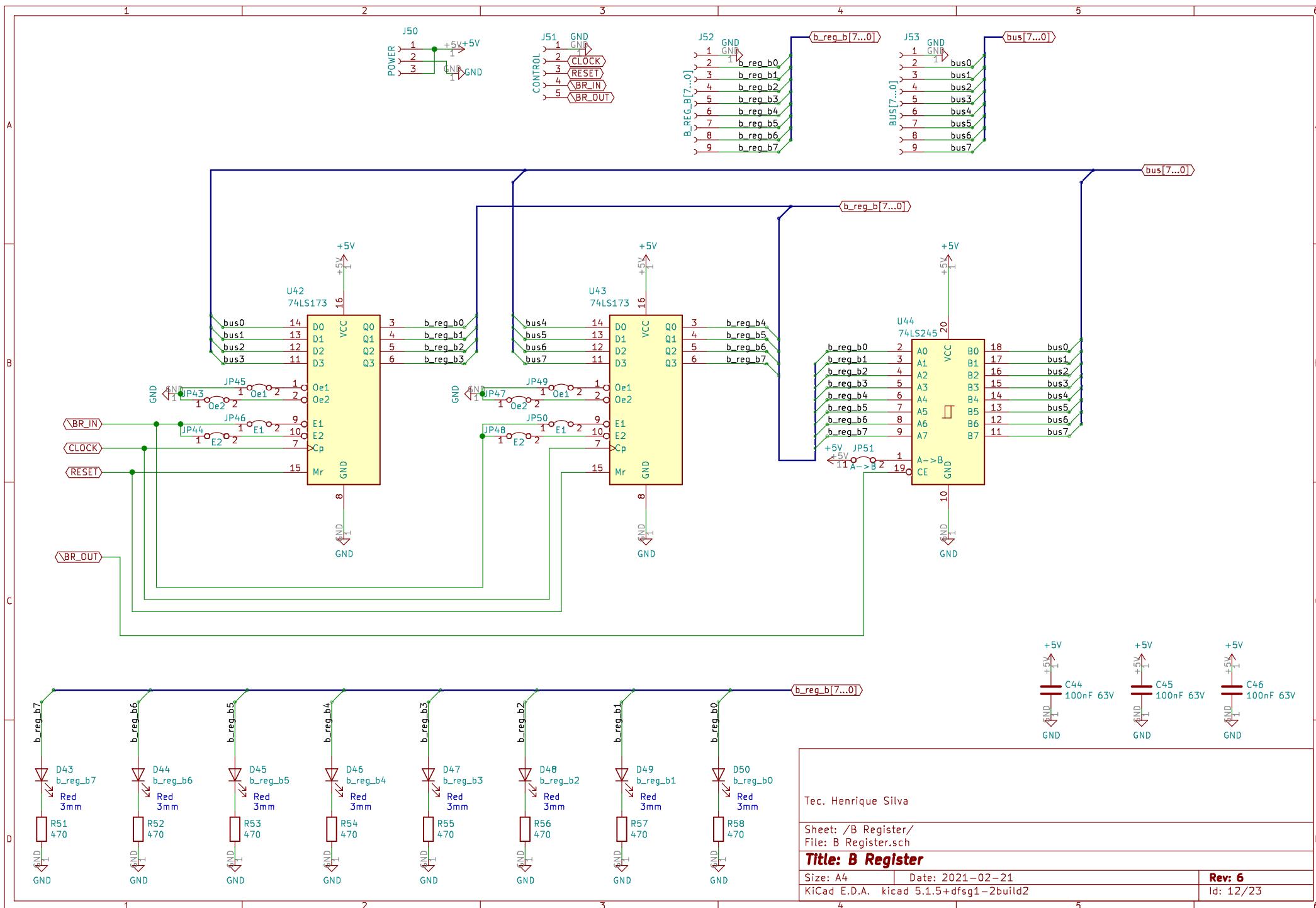


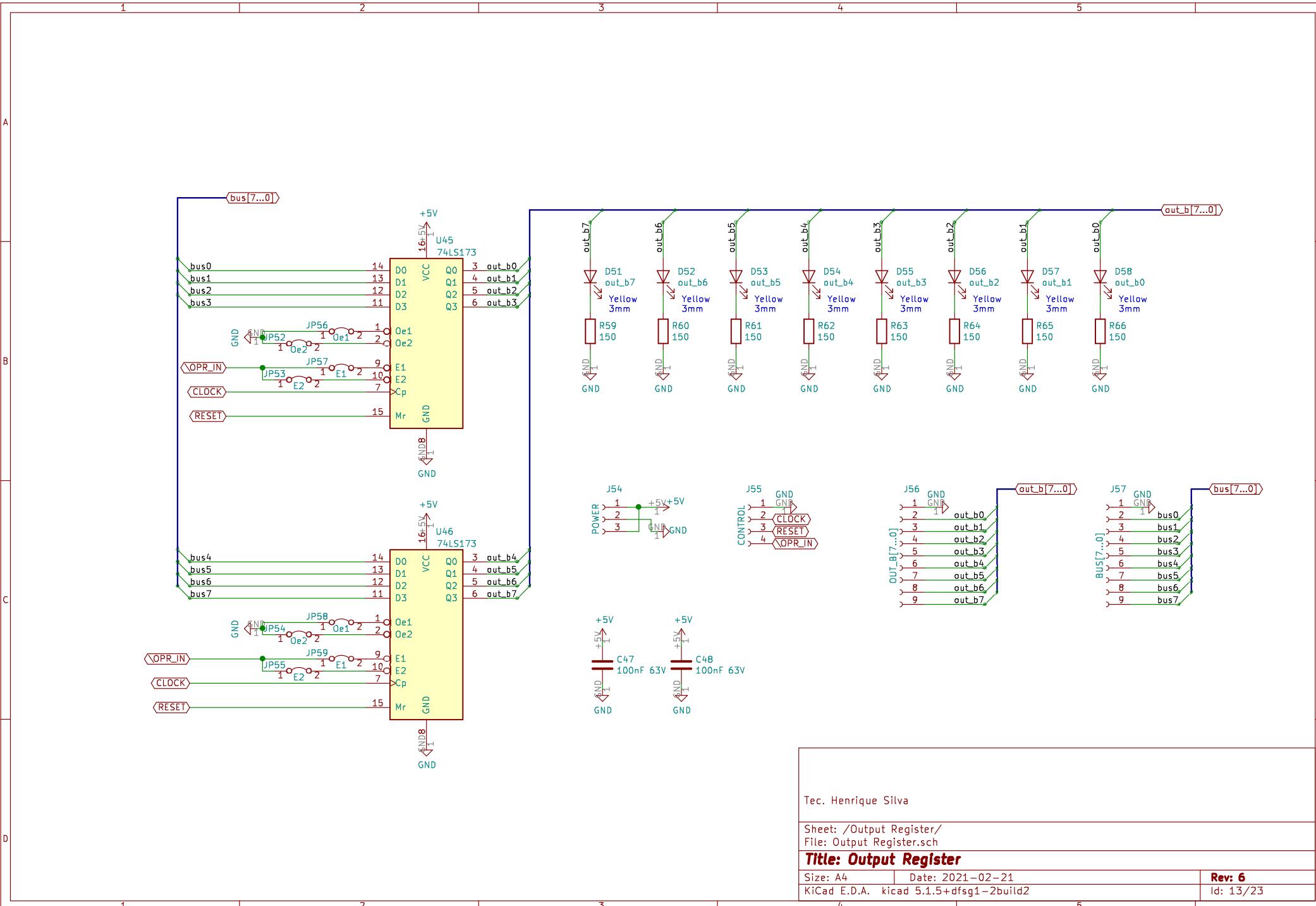


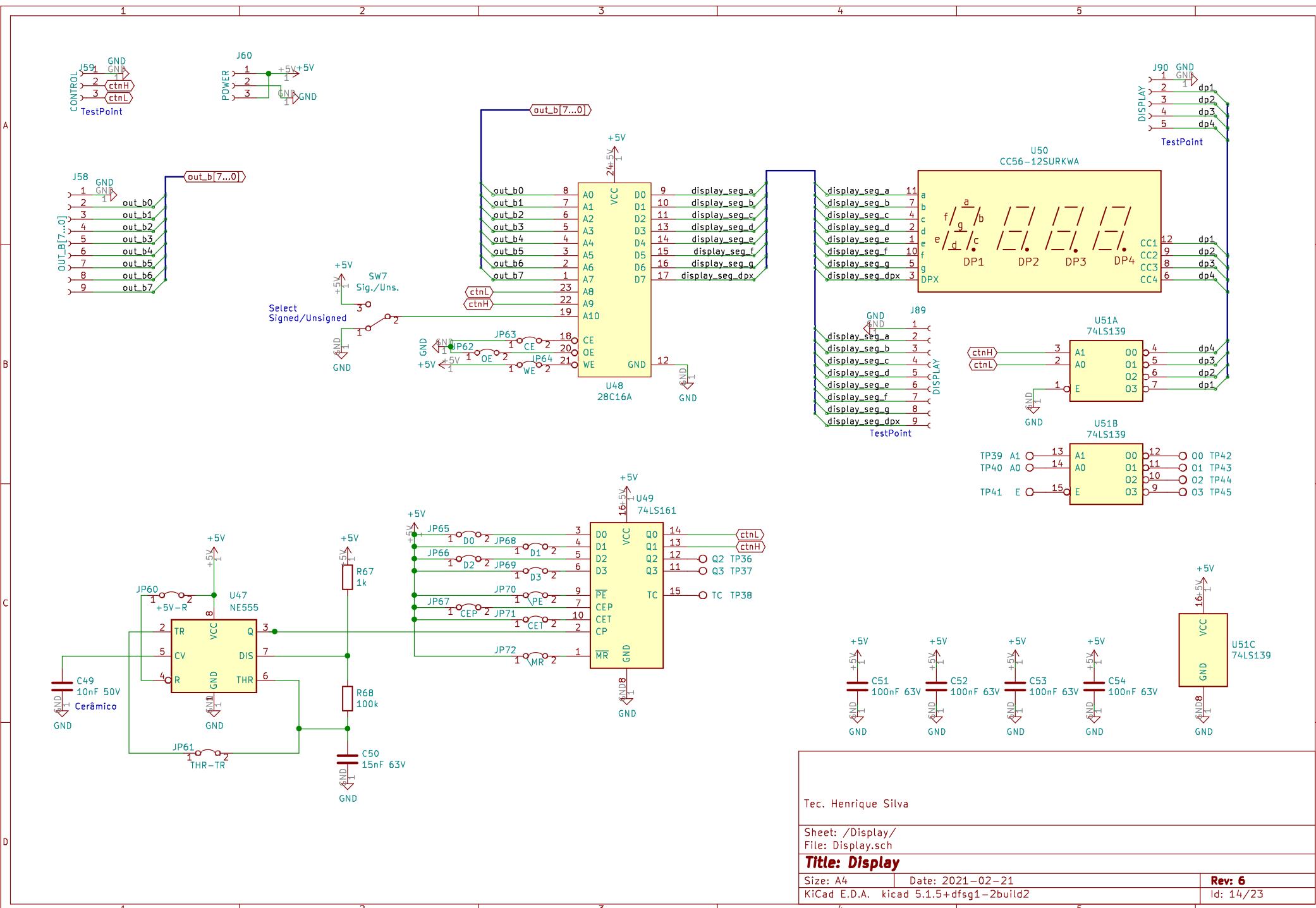












A

Sheet: Clock Circuit



File: Clock Circuit.sch

Sheet: Reset Circuit



File: Reset Circuit.sch

Sheet: Control Word



File: Control Word.sch

B

Sheet: Control Decoder



File: Control Decoder.sch

Sheet: Search and Execution Counter



File: Search and Execution Counter.sch

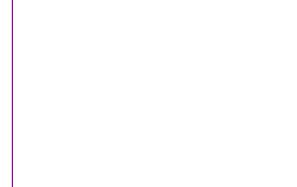
Sheet: Bus Simulator



File: Bus Simulator.sch

C

Sheet: Control Decoder Simulator



File: Control Decoder Simulator.sch

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Sheet: /Control/

File: Control.sch

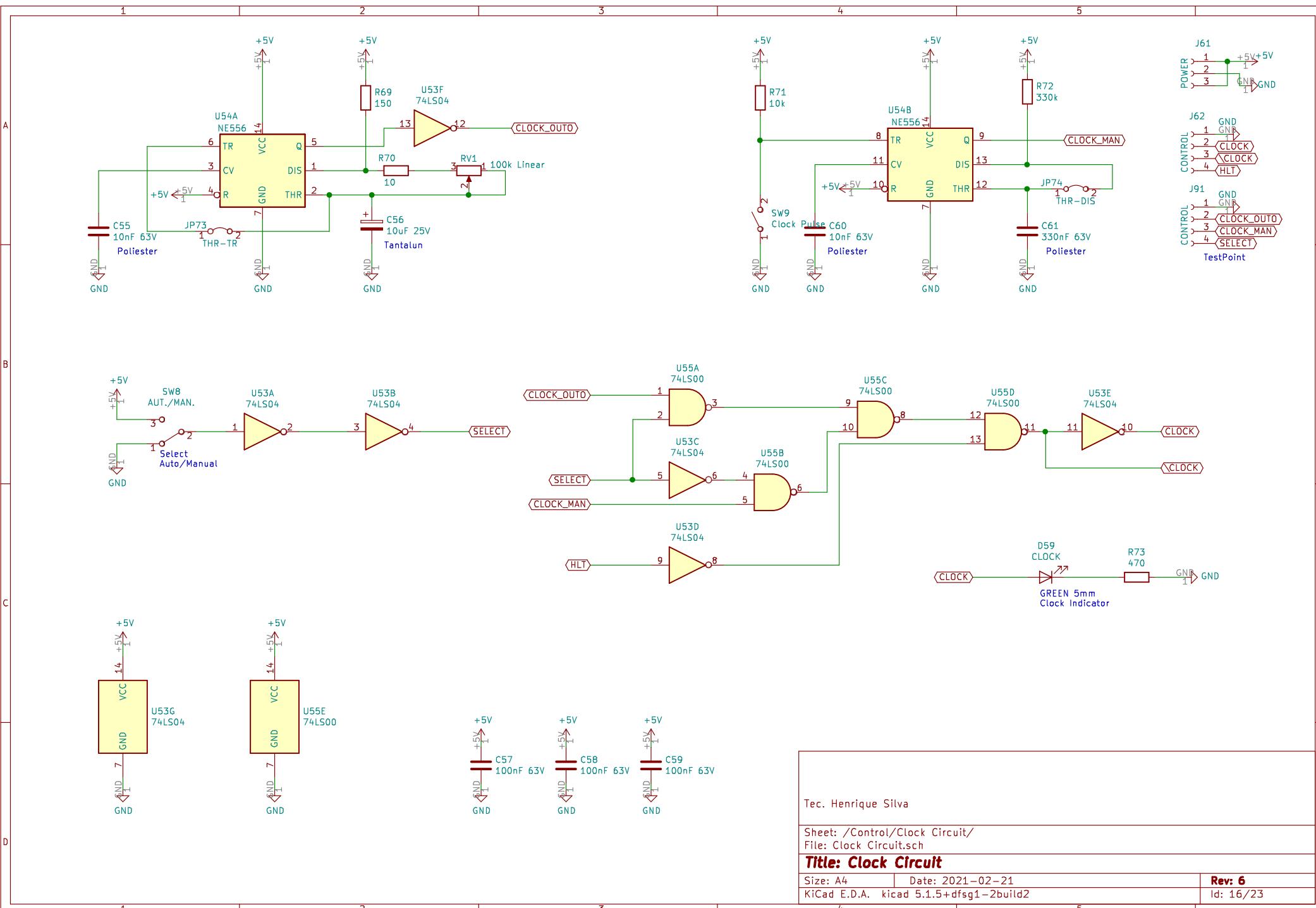
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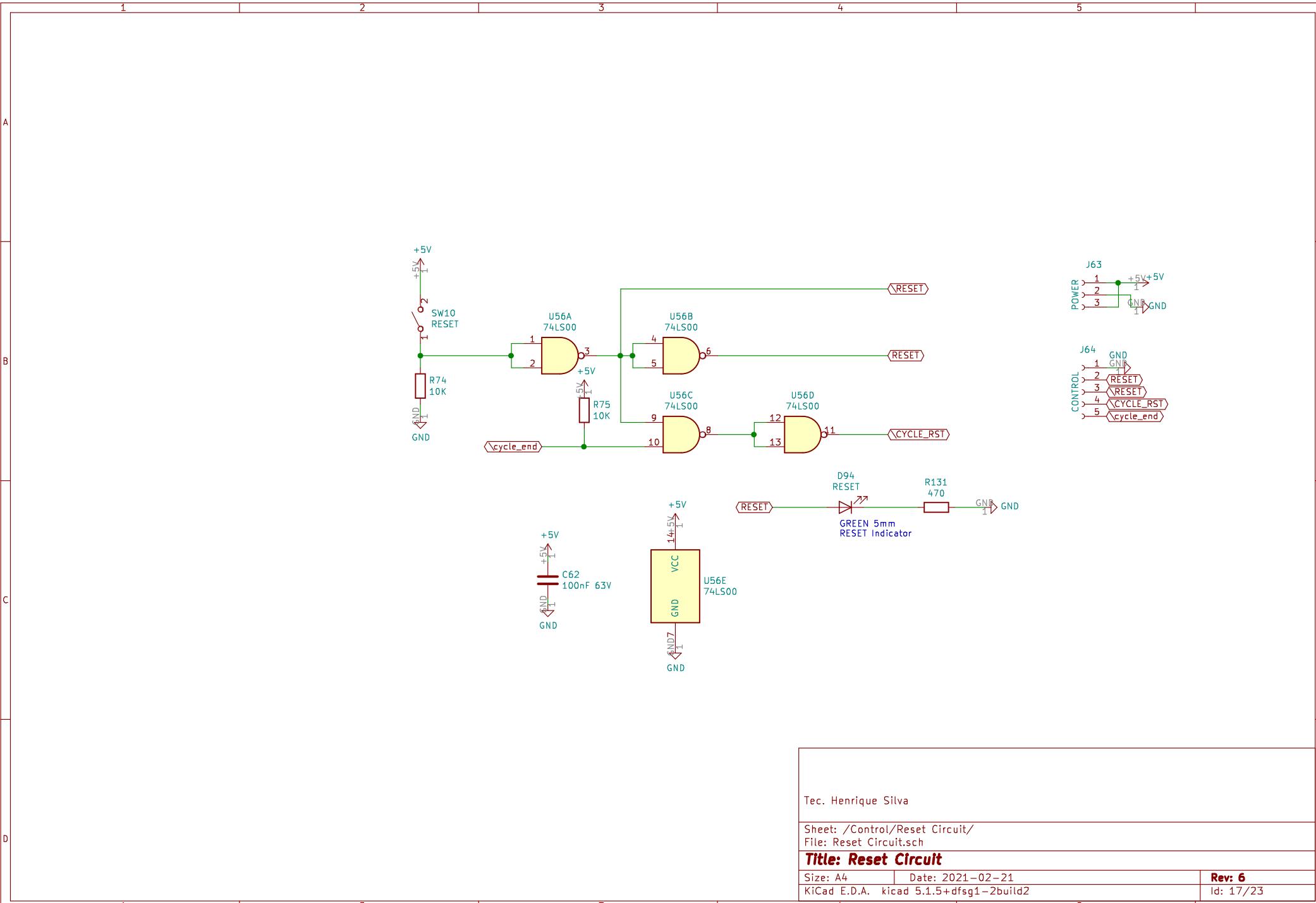
Size: A4 | Date: 2021-01-21

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**Rev: 5**

Id: 15/23





A

A

B

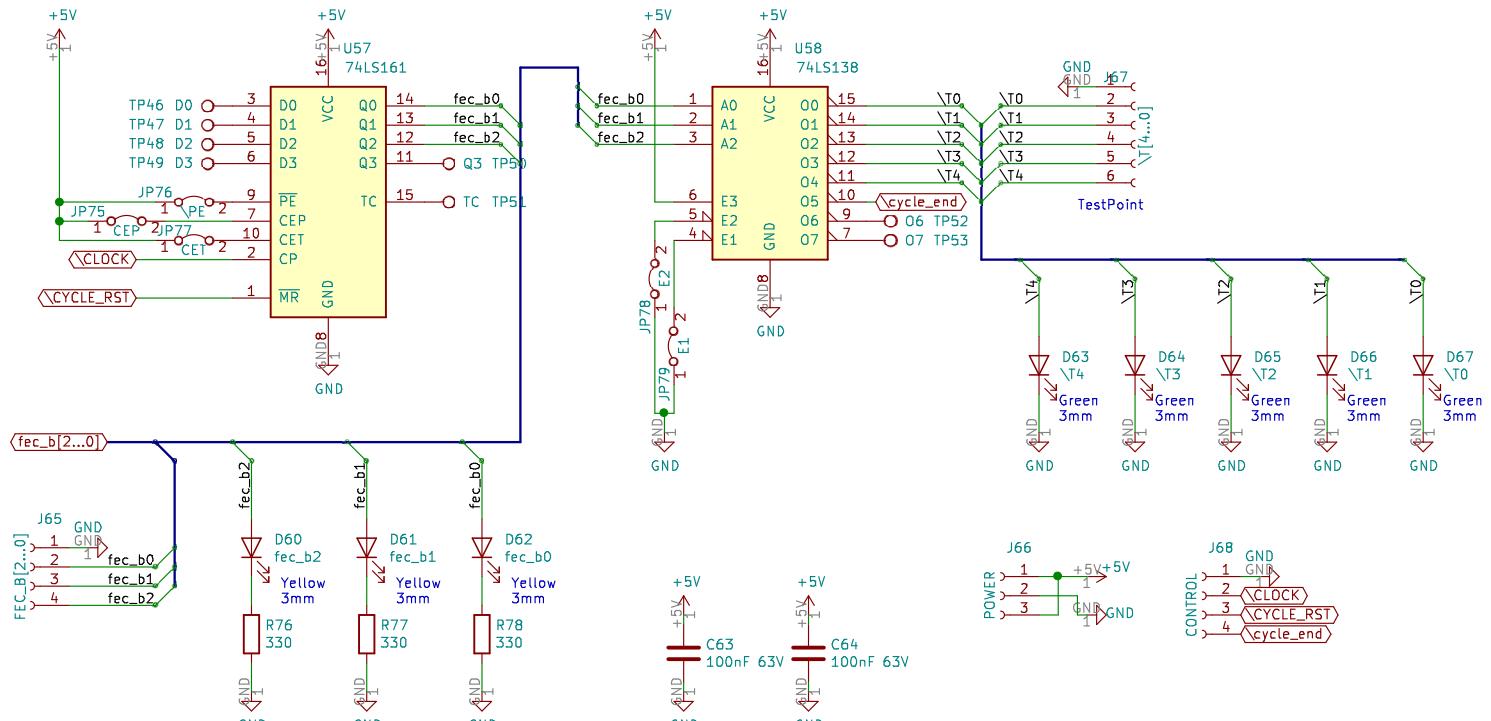
B

C

C

D

D



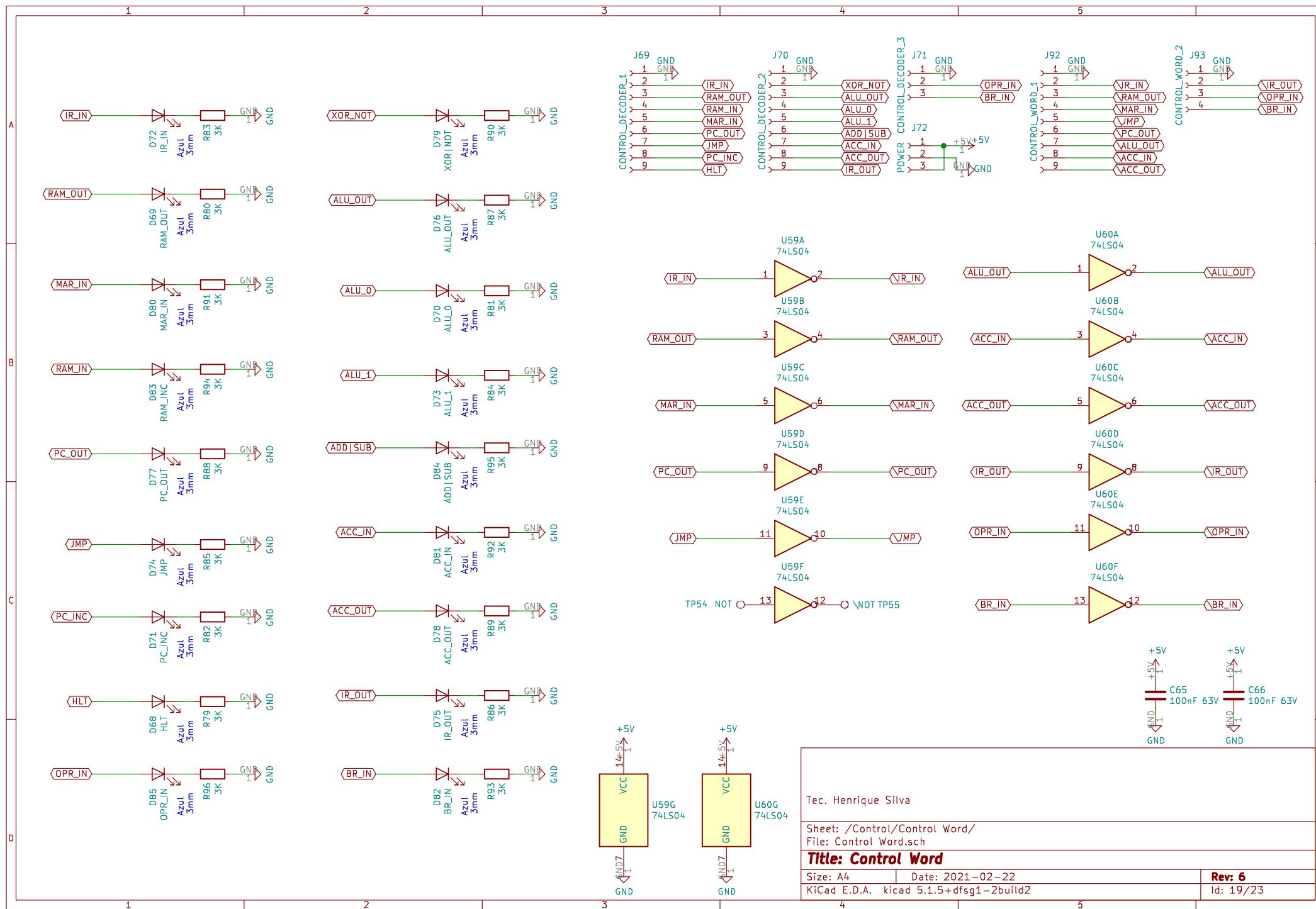
Tec. Henrique Silva

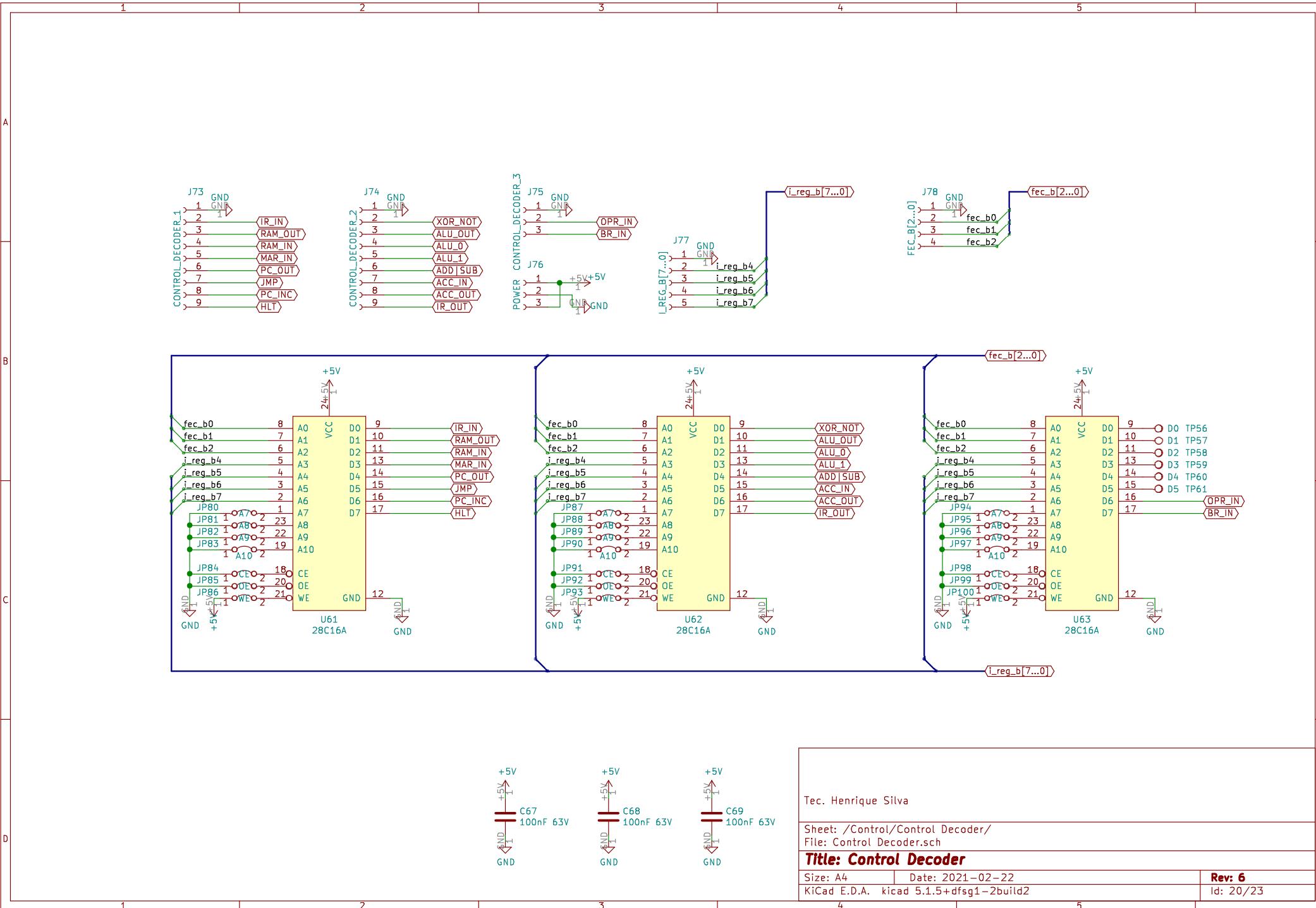
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File: Search and Execution Counter.sch

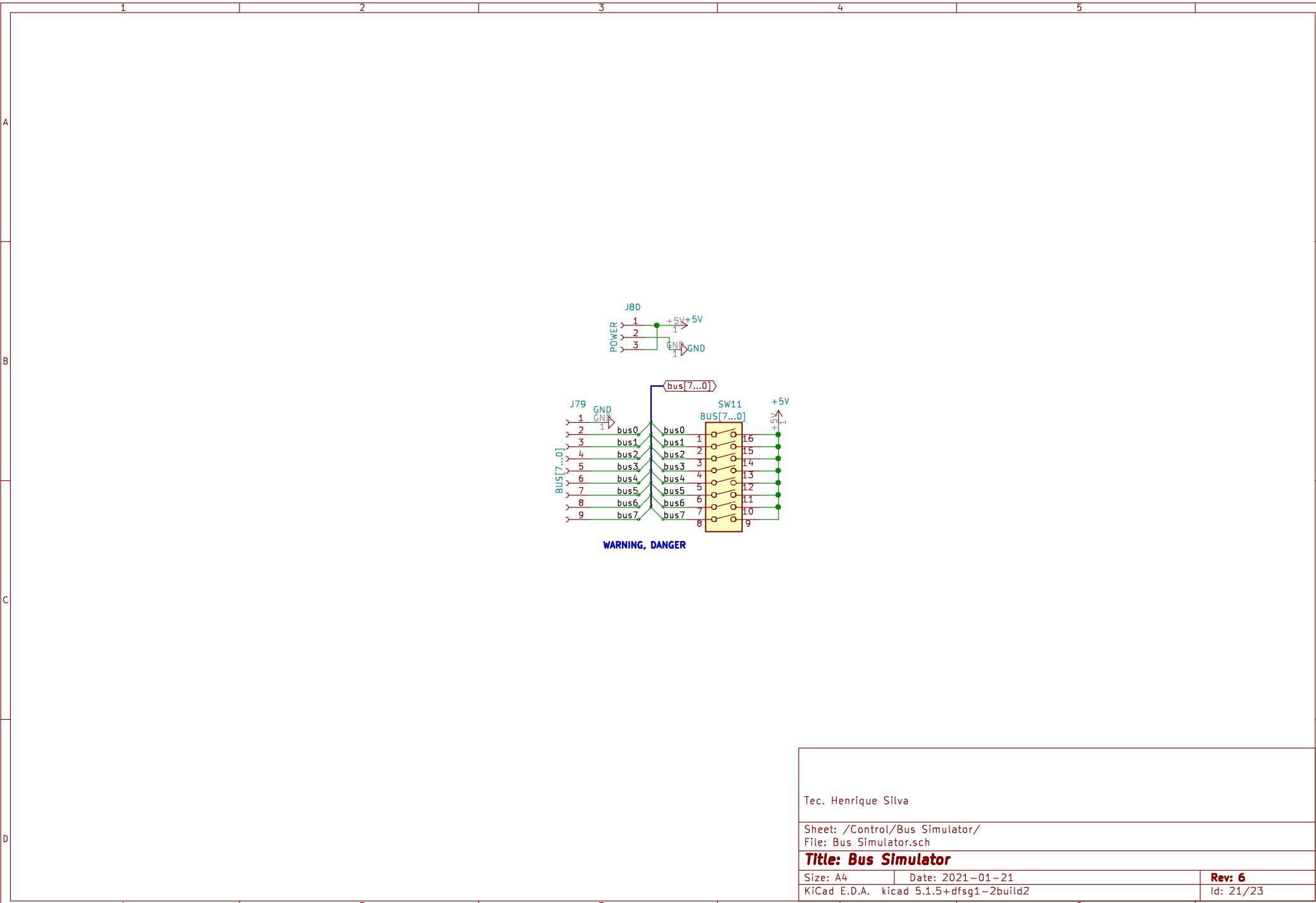
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Size: A4 | Date: 2021-02-22  
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

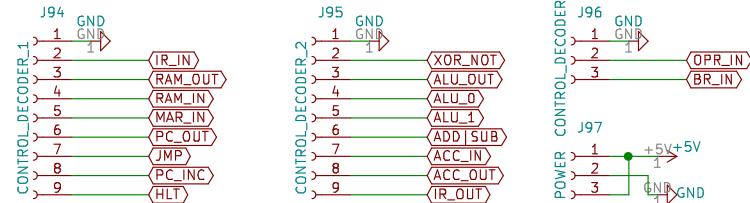
**Rev: 6**  
Id: 18/23



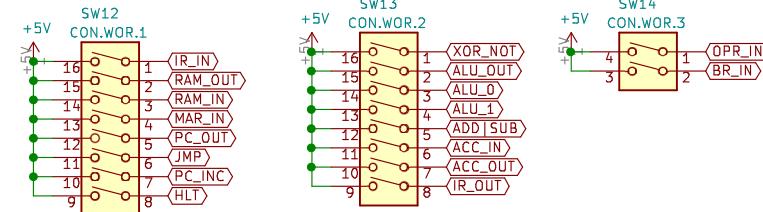




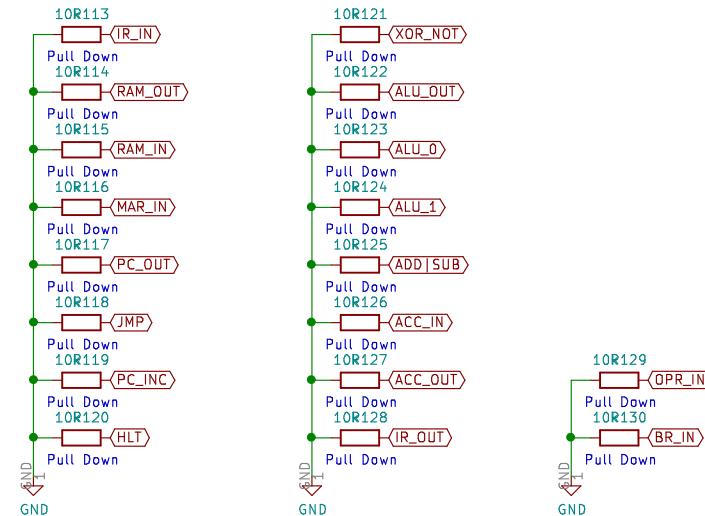
A



B



C



D

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Sheet: /Control/Control Decoder Simulator/  
 File: Control Decoder Simulator.sch

### Title: Control Decoder Simulator

Size: A4 Date: 2021-02-22

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Rev: 6

Id: 22/23

A

A

B

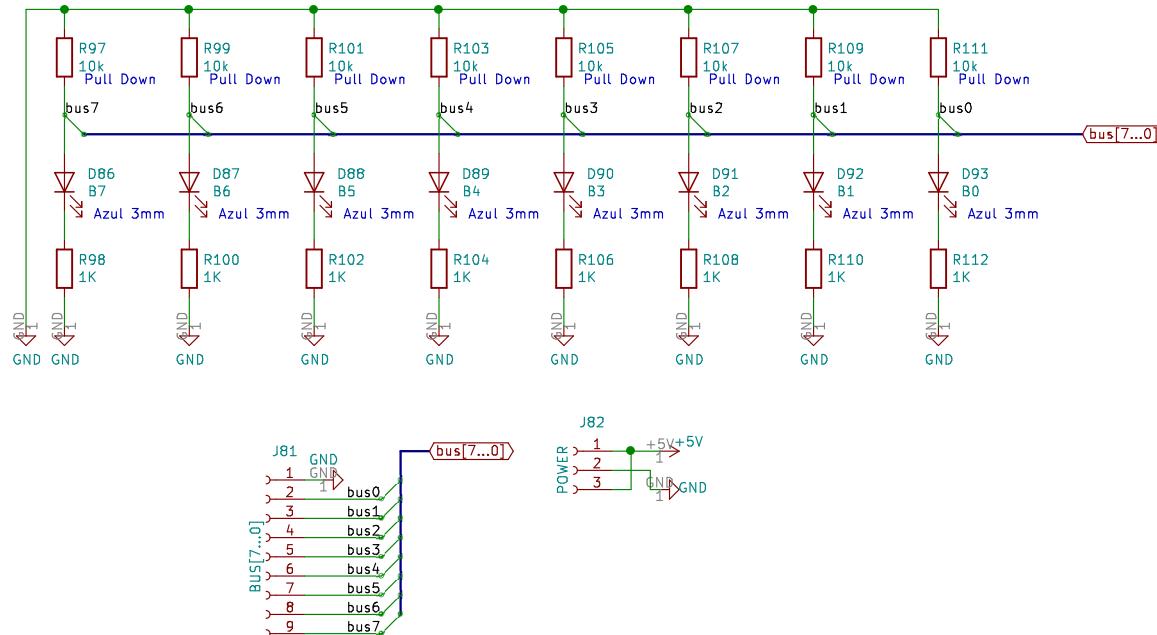
B

C

C

D

D



Tec. Henrique Silva

Sheet: /BUS/  
File: BUS.sch

**Title: BUS**

Size: A4 | Date: 2021-01-21  
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2

**Rev: 6**  
Id: 23/23