## **Self Assessment Sheet1: Operating System**

1. When a computer is first turned on or restarted, a special type of absolute loader is executed called a:
a) "Compiler and Go" Loader b) Boot Loader c) Bootstrap Loader d) Relating Loader
<ul> <li>2. In operating system, each process has its own:</li> <li>a) address space and global variables</li> <li>b) open files</li> <li>c) pending alarms, signals and signal handlers</li> <li>d) all of the mentioned</li> </ul>
<ul><li>3. A process can be terminated due to:</li><li>a) normal exit b) fatal error c) killed by another process d) all of the mentioned</li></ul>
<ul><li>4. A process stack does not contain:</li><li>a) function parameters b) local variables c) return addresses d) PID of child process</li></ul>
<ul><li>5. Which system call returns the process identifier of a terminated child?</li><li>a) wait</li><li>b) exit</li><li>c) fork</li><li>d) get</li></ul>
6. A parent process calling system call will be suspended until children processes terminate.
a) wait b) fork c) exit d) exec
7. The child process completes execution, but the parent keeps executing, then the child process is known as:
a) Orphan b) Zombie c) Body d) Dead
<ul> <li>8. Inter process communication:</li> <li>a) allows processes to communicate and synchronize their actions when using the same address space.</li> <li>b) allows processes to communicate and synchronize their actions without using the same address space.</li> <li>c) allows the processes to only synchronize their actions without communication.</li> <li>d) None of these</li> </ul>
<ul><li>9. Concurrent access to shared data may result in:</li><li>a) data consistency</li><li>b) data insecurity</li><li>c) data inconsistency</li><li>d) None of these</li></ul>
10. When several processes access the same data concurrently and the outcome of the execution depends on the particular order in which the access takes place, is called: a) dynamic condition b) race condition c) essential condition d) critical condition
<ul><li>11. The segment of code in which the process may change common variables, update tables write into files is known as:</li><li>a) program</li><li>b) critical section</li><li>c) non – critical section</li><li>d) synchronizing</li></ul>
12. Part of a program where the shared memory is accessed and which should be executed invisibly, is called: a) semaphore b) directory c) critical section d) mutual exclusion
13Which of the following conditions must be satisfied to solve the critical section problem I.Aging II.Mutual Exclusion III.Deadlock IV.Progress Requirement V.Bounded Waiting a) I, IV, V b) II, III, V c) I, III, V d) II, IV, V

their critical section is		•	·	neously inside
a) 8	b) 1	c) 16	d) 0	
15. If a process is e critical section. This can a) mutual exclusion c) synchronous exclusion		b) critical ex	•	be executing in their
16. Bounded waiting	implies that there exis	sts a bound on the r	number of times a	process is allowed to
b) when another prod	on: s made a request to encess is in its critical sectors has made a request to e	tion		quest is granted
17. Which one of the a) thread	following is a synchron b) pipe	nization tool? c) semaphore	d) socket	
18. A semaphore is a a) that can not drop b c) that can not drop b		le: b) that can not be r d) that can not be r		
	from only one process rom multiple processes		a binary mutex. :hese	
20. The two kinds of a) mutex	semaphores are : (choo b) binary	ose two) c) counting	d) decimal	
	from only one process rom multiple processes		a binary mutex these	
22. Mutual exclusion a) mutex locks	can be provided by the b) binary semaphores		and (b) d)	none
23. The dining – philo a) 5 philosophers and c) 3 philosophers and	•	b) 4 philoso	phers and 5 chops phers and 5 chops	
24. The bounded buf a) Readers – Writers c) Producer – Consu	•		Philosophers probl hese	em
b) it is invalid	value is negative: e number of processes be further performed on	·		ed on it
26. The TestAndSet a) after a particular p	instruction is executed: rocess b) periodica		d) None of the	ese

27. Which one of the following is the address generated by CPU? a) physical address b) absolute address c) logical address d) none of the mentioned
28. The principle of locality of reference justifies the use of: a) harddisk b) virtual memory c) ROM d) cache memory
<ul><li>29. Physical memory is broken into fixed-sized blocks called</li><li>a) frames b) pages c) backing store d) None of these</li></ul>
30. Logical memory is broken into blocks of the same size called a) frames b) pages c) backing store d) None of these
31. Memory management technique in which system stores and retrieves data from secondary storage for use in main memory is called: a) fragmentation b) paging c) mapping d) none of the mentioned
32. The table contains the base address of each page in physical memory. a) process b) memory c) page d) frame
33. For every process there is a a) page table b) copy of page table c) pointer to page table d) All of these
<ul> <li>34. The Memory Buffer Register (MBR):</li> <li>a) is a hardware memory device which denotes the location of the current instruction being executed.</li> <li>b) is a group of electrical circuits (hardware), that performs the intent of instructions fetched from memory.</li> <li>c) contains the address of the memory location that is to be read from or stored into.</li> <li>d) contains a copy of the designated memory location specified by the MAR after a "read" or the new contents of the memory prior to a "write".</li> </ul>
35. The Memory Address Register: a) is a hardware memory device which denotes the location of the current instruction being executed. b) is a group of electrical circuits (hardware), that performs the intent of instructions fetched from memory. c) contains the address of the memory location that is to be read from or stored into. d) contains a copy of the designated memory location specified by the MAR after a "read" or the new contents of the memory prior to a "write".
<ul><li>36. A page fault:</li><li>a) is an error is a specific page.</li><li>b) occurs when a program accesses a page of memory.</li><li>c) is an access to a page not currently in memory.</li><li>d) is a reference to a page belonging to another program.</li></ul>
37. Contiguous allocation has two problems and that linked allocation solves. a)external fragmentation b)internal fragmentation c)size declaration d)memory allocation
38. In the linked allocation, the directory contains a pointer to the : I. first block II. last block a) I only b) II only c) Both I and IId) Neither I nor II
39. There is no with linked allocation.

a) internal fragmentation	b) external fragmen	ntation	c) starvation	d) All of these
40. In internal fragmentation, a) is being used b) is no				ne of these
<ul><li>41. External fragmentation ex</li><li>a) enough total memory exist</li><li>b) the total memory is insuffic</li><li>c) a request cannot be satisfied) None of these</li></ul>	ts to satisfy a requestient to satisfy a req	uest	_	
42. A solution to the problem a) compaction b) larger me			mory space d	) None of these
<ul><li>43. External fragmentation w</li><li>a) first fit is used</li><li>b) best</li><li>d) no matter which algorithm</li></ul>	t fit is used c) w		used	
44. The memory allocation so a) segmentation b) swa				
45. With paging there is no _ a) internal b) external	fragmenta c) either type of	tion. d) Nor	ne of these	
46. Fragmentation of the file a) occurs only if the file syste c) can be temporarily remove	m is used improper			
47. Which of the following is a) non-volatile b) larger stor				
48. The three major methods a) contiguous b) links				are:
<ul><li>49. In contiguous allocation :</li><li>a) each file must occupy a se</li><li>b) each file is a linked list of c</li><li>c) all the pointers to scattered</li><li>d) None of these</li></ul>	disk blocks			
50. In linked allocation: a) each file must occupy a set b) each file is a linked list of c c) all the pointers to scattered d) None of these	disk blocks			
51. In indexed allocation: a) each file must occupy a set b) each file is a linked list of c c) all the pointers to scattered d) None of these	disk blocks			

52. The first fit and best fit algorithms suffer from : (choose all that apply)

a) internal fragmentation b) external fragmentation c) starvation d) All of these
53. In which of the storage placement strategies a program is placed in the largest available hole in the main memory?  a) best fit b) first fit c) worst fit d) none of the above
<ul> <li>54. The CPU, after receiving an interrupt from an I/O device:</li> <li>a) halts for a predetermined time</li> <li>b) hands over control of address bus and data bus to the interrupting device</li> <li>c) branches off to the interrupt service routine immediately</li> <li>d) branches off to the interrupt service routine after completion of the current instruction</li> </ul>
55. Whenever a process needs I/O to or from a disk it issues a  a) system call to the CPU b) system call to the operating system c) a special procedure d) All of these
56. To create a file: a) allocate the space in file system b) make an entry for new file in directory c) both (a) and (b) d) none of the mentioned
57. Consider a disk queue with requests for I/O to blocks on cylinders : 98 183 37 122 14 124 65 67 i) Considering FCFS (first cum first served) scheduling, the total number of head movements is, if the disk head is initially at 53 : a) 600 b) 620 c) 630 d) 640
ii) Considering SSTF (shortest seek time first) scheduling, the total number of head movements is, if the disk head is initially at 53: a) 224 b) 236 c) 245 d) 240
58.A disk scheduling algorithm in an operating system causes the disk arm to move back and forth across the disk surface in order to service all requests in its path. This is a:  a) FCFS b) SCAN c) C-SCAN d) C-LOOK
59. In the algorithm, the disk arm goes as far as the final request in each direction, then reverses direction immediately without going to the end of the disk.  a) LOOK b) SCAN c) C-SCAN d) C-LOOK
60. FAT stands for : a) File Attribute Transport b) File Allocation Table c) Fork At Time d) None of these
61. An OS uses a paging system with 1Kbyte pages. A given process uses a virtual address space of 128K and is assigned 16K of physical memory. How many entries does its page table contain?
a) 16 b) 1024 c) 128 d) 512
62. An OS has 64MB physical memory and generates 32 bit logical address. Number of physical pages and virtual pages for page size 4KB is: a) 16K, 1M b) 16M, 1M c) 4K, 16M d) 4M, 16M
63. Suppose a logical address space of 32 pages of 2048 words. There are 64 frames in the main memory. According to the given information, calculate size of physical address and virtual address. (Hint: by default 1 word = 1 byte)

64. A program is executing in a pure demand paging system with 100 records per page with 1 free main memory frame. The address sequence that is generated by tracing this program is recorded as follows: 0100, 0200, 0430, 0499, 0510, 0530, 0560, 0120, 0220, 0240, 0260, 0320, 0370 .What are the number of page faults?					
a) 8 b) 11 c) 7 d) 12					
65. Consider main memory of 2MB and page size has 256B. How many pages are allocated to a process of size 5000B?					
a) 20 b) 19 c) 10 d)11					
66. Consider one-level paging system having TLB lookup time of 5 nsec. Memory access time is 100 nsec. Find effective memory access time for TLB hit ratio of 98%.  a) 125 nsec b) 107 nsec c) 105 nsec d) 121 nsec					
67. Suppose TLB used in one level paging system with each look-up time of TLB 40 msec. Memory reference takes 120 msec. If the effective memory reference time is 180 msec then page table references are found in TLB is a) 0.85 b) 0.87 c) 0.76 d) 0.833					
68. Consider a system with 2-levels of paging and a TLB with hit rate of 95% and TLB access time of 1ns. Find the effective memory access time if there's a data cache whose hit rate is 85% and cache access time is 1ns, and main memory access time is 100ns.  a) 27 ns  b) 25 ns  c) 30 ns  d) 20ns					
69. A processor uses 2 level paging for virtual address to physical address translation. page tables for both levels are stored in physical memory. There is physically addressed cache. TLB hit ratio = 96%, TLB Access Time=1ns, Cache hit ratio=90%, Cache Access Time= 1ns, Physical M/M Access Time: 10 ns. What is effective memory access time?  a) 2.88 ns  b) 3.4 ns  c) 3.8 ns  d) 2.84 ns					
70. Consider a 32-bit virtual address is used for paging with page size of 1024 bytes. Two-level paging is implemented with equal number of entries in every page table of the system. If page table entry size is 2 bytes. The maximum size (in MB) of main memory supported by the above system is					
a)512MB b)128MB c)64MB d) 256MB					
Answers: 1) b. 2) d. 3) d. 4) d. 5) a. 6) a. 7) b. 8) b. 9) c. 10) b. 11) b. 12) c. 13) d. 14) b. 15) a. 16) a. 17) c. 18) a. 19) c. 20) b,c. 21) a. 22) c. 23) a. 24) c. 25) a. 26) c. 27) c. 28) d. 29) a. 30) b. 31) b. 32) c. 33) a. 34) d. 35) c. 36) c. 37) a,c. 38) c. 39) b. 40) b. 41) a. 42) a. 43) d. 44) a. 45) b. 46) c. 47) d. 48) a,b,c. 49) a. 50) b. 51) c. 52) b. 53) c. 54) d. 55) b. 56) c. 57) i.d. ii.b. 58) b. 59) a. 60) b. 61) c. 62) a. 63) d. 64) c. 65) a. 66) b. 67) d. 68) a. 69) c. 70) d.					

c) 32 bits, 16 bits

d) 17 bits, 16 bits

a) 16 bits, 17 bits

b) 16 bits, 32 bits