

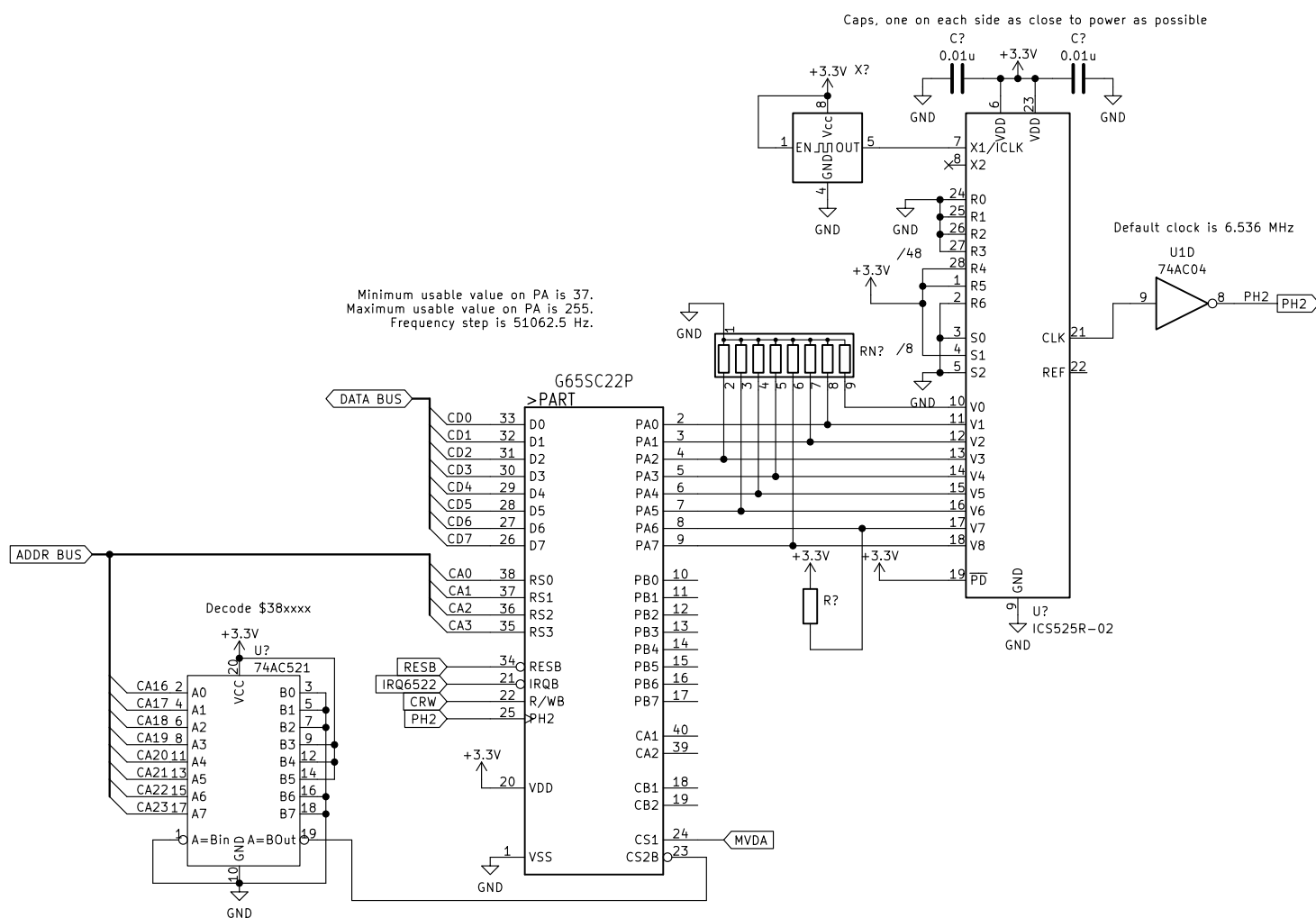
Finitron

Sheet: /
File: RF65816SBC.kicad_sch

Title: 65816 SBC CPU

Size: A Date:
KiCad E.D.A. kicad (6.0.6)

Rev: 1.2
Id: 1/6



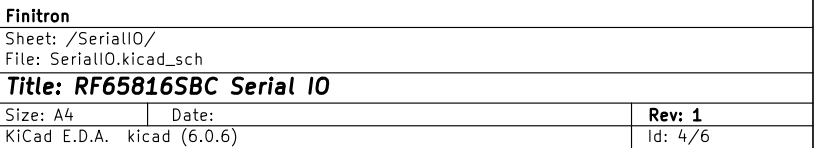
Circuit for lower frequency synthesis < 3.98 MHz

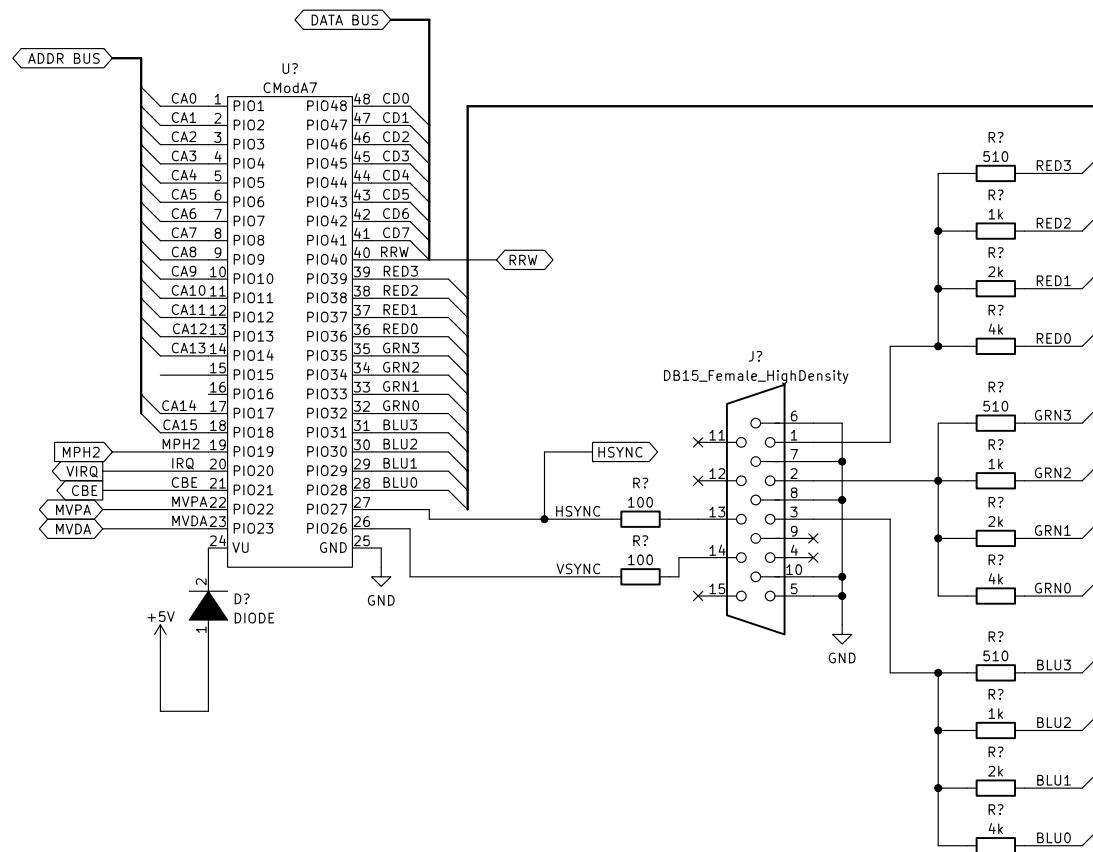
Default frequency = 1.000 MHz

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File: Clkgen2.kicad_sch

Title:		Rev:
Size: A4	Date:	
KiCad E.D.A. kicad (6.0.6)		Id: 3/6

Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (6.0.6)		Id: 3/6





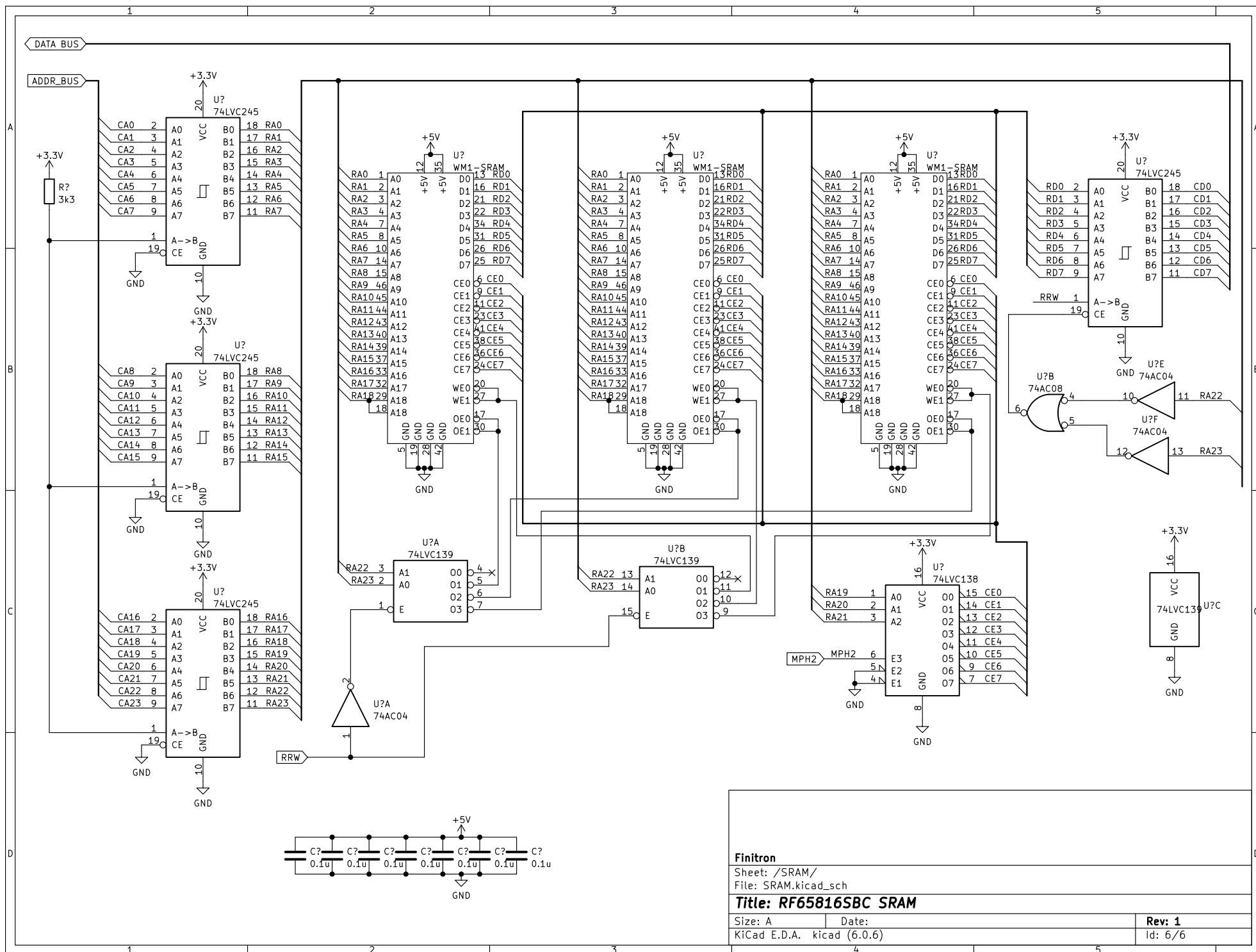
Finitron

Sheet: /VGAIO/
File: VGAIO.kicad_sch

Title: RF65816SBC VGA

Size: A4
KiCad E.D.A. kicad (6.0.6)

Date:
Rev: 1
Id: 5/6



Sheet: /SRAM/
File: SRAM.kicad_sch

Size: A	Date:
KiCad E.D.A. kicad (6.0.6)	

Rev: 1
Id: 6/6