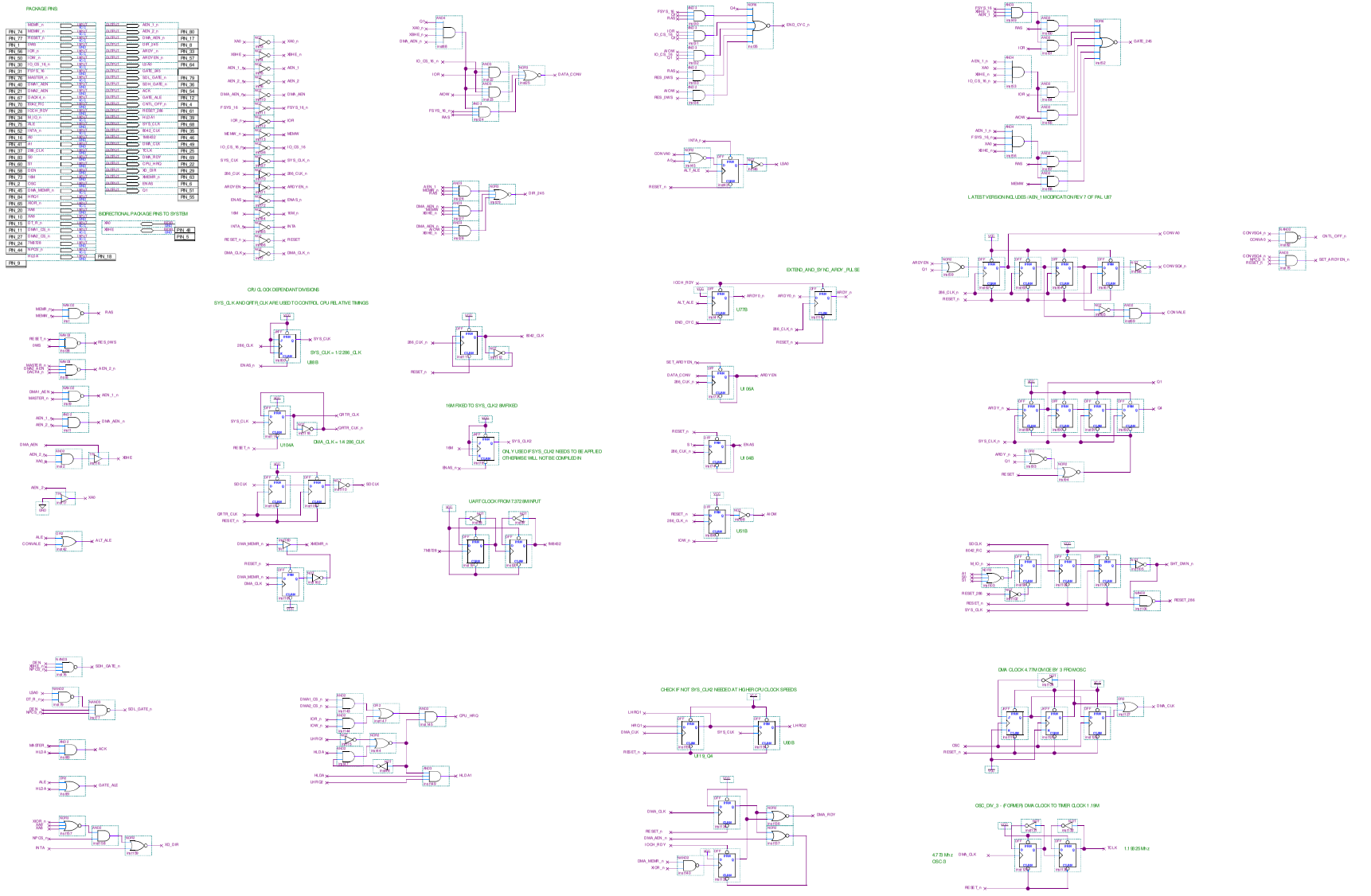
PACKAGE PINS
BIDIRECTIONAL PACKAGE PINS TO SYSTEM
CPU CLOCK DEPENDANT DIVISIONS
SYS_CLK AND GRFR_CLK ARE USED TO CONTROL CPU RELATIVE TIMINGS
SYS_CLK = 1/2 286_CLK
DMA_CLK = 1/4 286_CLK
16M FIXED TO SYS_CLK2
ONLY USED IF SYS_CLK2 NEEDS TO BE APPLIED OTHERWISE WILL NOT BE COMPILED IN
UART CLOCK FROM 7.0728M INPUT
EXTEND_AND_SYNC_ARDY_PULSE
LATEST VERSION INCLUDES /AEN_1 MODIFICATION REV 7 OF PAL U87
CHECK IF NOT SYS_CLK2 NEEDED AT HIGHER CPU CLOCK SPEEDS
DMA CLOCK 4.77M DIVIDE BY 3 FROM OSC
OSC_DIV_3 - (FORMER) DMA CLOCK TO TIMER CLOCK 1.19M