

# Arduino - ez\_SIPO8lib Crib Sheet

**Library Class Initiation** {download a copy of the full User Guide here from [github](#)}

Class Name: **SIPO8**

Class Initiation Syntax: **SIPO8** my\_SIPOs (Num\_SIPOs, Num\_timers);

where 'my\_SIPOs' is any name you wish to use for your project and 'Num\_SIPOs' and 'Num\_timers' are the number of SIPO ICs and timers you wish to define. For example:

```
1.  SIPO8 my_SIPOs( 1, 0); // define 1 SIPO, 0 timers
2.  #define Max_SIPOs      8
    #define Max_timers    4
    SIPO8 my_SIPOs(Max_SIPOs, Max_timers);
3.  etc
```

declare the class instance early in your code,  
for example after any SIPO data but prior  
to the setup() function

## Available User Accessible Library Macros Definitions

Macro Name - #define	Value	Associated Functions
<code>pins_per_SIPO</code>	8	The number of bits/output pins of a virtual/physical SIPO and on which the core design of the library is based.
<code>create_bank_failure</code>	-1	The return value from the <code>create_bank</code> function, if a request to create a new bank for the specified number of SIPOs cannot be met, e.g. too few remaining unallocated SIPOs in the unused pool.
<code>pin_read_failure</code>	-1	The return value from the <code>read_array_pin</code> & <code>read_bank_pin</code> functions, if a specified pin is not an active pin.
<code>pin_invert_failure</code>	-1	The return value from the <code>invert_array_pin</code> & <code>invert_bank_pin</code> functions, if a specified pin is not an active pin.
<code>pin_set_failure</code>	-1	The return value from the <code>set_array_pin</code> & <code>set_bank_pin</code> functions, if a specified pin is not an active pin.
<code>bank_not_found</code>	-1	The return value from the <code>get_bank_from_pin</code> , <code>set_bank_SIPO</code> , <code>invert_bank_SIPO</code> &

Macro Name - #define	Value	Associated Functions
		<code>read_bank_SIPO</code> functions, if a specified pin/SIPO does not occupy a defined SIPO bank.
<code>SIPO_not_found</code>	-2	The return value from the <code>set_bank_SIPO</code> , <code>invert_bank_SIPO</code> & <code>read_bank_SIPO</code> functions, if a specified SIPO not in range for given bank.
<code>timer0</code>	0	Eight predefined macros are provided that may be used for 1 - 8 configured timers. Beyond eight, the end user will either reference timers explicitly within the limit of the number of timers set up at class initiation , e.g. 9, 10, etc, or by using his/her own declared macros for such purposes.
<code>timer1</code>	1	
<code>timer2</code>	2	
<code>timer3</code>	3	
<code>timer4</code>	4	
<code>timer5</code>	5	
<code>timer6</code>	6	
<code>timer7</code>	7	
<code>elapsed</code>	<code>true</code>	Return value of the <code>SIPO8_timer_elapsed</code> function if the defined elapse period has completed for the specified timer.
<code>not_elapsed</code>	<code>!elapsed</code>	Return value of the <code>SIPO8_timer_elapsed</code> function if the defined elapse period for the specified timer has not yet completed.
<code>active</code>	<code>true</code>	Used internally by the SIPO8 timer functions to judge if the specified timer is active.
<code>not_active</code>	<code>!active</code>	Used internally by the SIPO8 timer functions to judge if the specified timer is inactive.

### Available User Accessible Library Variables

SIPO8 Control Sruct(ure)	Purpose
<code>struct SIPO_control {</code>	For each bank created ( <code>create_bank</code> function) maintains a record of:
<code>uint8_t bank_data_pin;</code>	Digital pin allocated to the data input line to a SIPO bank
<code>uint8_t bank_clock_pin;</code>	Digital pin allocated to the clock input line to a SIPO bank
<code>uint8_t bank_latch_pin;</code>	Digital pin allocated to the latch input line to a SIPO bank
<code>uint8_t bank_num_SIPOs;</code>	The number of 8bit SIPO ICs grouped under a bank
<code>uint16_t bank_low_pin;</code>	The first output pin number for the bank (absolute pin address)
<code>uint16_t bank_high_pin;</code>	The last output pin number for the bank (absolute pin address)
<code>} * SIPO_banks;</code>	<code>SIPO_banks</code> is the active/working data structure used throughout the library for managing and controlling SIPO data. It is available for direct user code inspection.

SIPO8 Timer Struct(ure)	Purpose
<code>struct timer_control {</code>	For each timer created maintains a record of:
<code>bool timer_status;</code>	Current status of a timer - <code>active</code> or <code>not_active</code>
<code>uint32_t start_time;</code>	The millis time when a timer is started
<code>} * timers;</code>	<code>timers</code> is the active/working data structure used throughout the library for managing and controlling timers. It is available for direct user code inspection.

Pin Status Bytes (PSB)	Purpose/definition
<code>uint8_t</code> <code>* pin_status_bytes;</code>	<p>An array sized and created to be the number of 8 bit unsigned bytes required to map the maximum number of SIPO ICs defined at class initiation. That is, one status byte per required 8bit SIPO IC. This structure is the virtual SIPO output pin map and represents the entire SIPO output pin array pool.</p> <p>The <code>pin_status_bytes</code> array is used to record the status (<code>HIGH</code> or <code>LOW</code>) of each physical SIPO output pin. It is available for direct user code inspection.</p>

Other User Accessible Variables/Declarations	Purpose/definition
<code>uint8_t max_SIPOs</code>	The maximum number of SIPO ICs defined at class initiation by the user code.
<code>uint16_t max_pins</code>	<p>The maximum number of SIPO output pins defined for the SIPO pool. Note that this is not the number of active SIPO output pins, but is the maximum number of SIPO output pins available for allocation to SIPO banks via calls to the <code>create_bank</code> function.</p> <p>Therefore, <code>max_pins</code> <math>\geq</math> <code>num_active_pins</code>.</p>
<code>uint16_t num_active_pins</code>	The number of <u>active</u> SIPO output pins allocated from the SIPO output pin pool. Allocated by the <code>create_bank</code> function - see above.
<code>uint8_t</code> <code>num_pin_status_bytes</code>	The number of 8 bit unsigned bytes allocated to map the maximum number of SIPO output pins. For example, if the max number of SIPO output pins is 128, then this value will be $128/8 = 16$ bytes.
<code>uint8_t num_banks</code>	The total number of SIPO banks created by using <code>create_bank</code> function.
<code>uint8_t bank_SIPO_count</code>	The total number of SIPO ICs in use by defined SIPO banks. Note that <code>bank_SIPO_count</code> $\leq$ <code>max_SIPOs</code> .
<code>uint8_t max_timers</code>	The number of timers defined at time of class initiation by the user code.

## Available User Accessible Library Functions

Function/Method	Parameters	Returned Value(s)	Comments
<b>SIPO8 Class Constructor Function</b>			
<b>SIPO8</b>	uint8_t Max_SIPOs, uint8_t Max_timers	n/a	The constructor function for the SIPO8 library class. Creates the internal structures required to map every output pin of the specified number of SIPO ICs. If memory cannot be mapped (insufficient free memory) the function will terminate after providing an error message to the serial monitor (set for 9600 baud).
<b>SIPO Array Pool Functions (Absolute Addressing, pin addresses - 0 to (number_active_pins_in_array - 1))</b>			
void set_all_array_pins	bool pin_status	n/a	Sets <u>every active</u> SIPO output pin to the given status, LOW or HIGH. It is equivalent to set_banks(pin_status).
void invert_all_array_pins	none	n/a	Inverts the existing status of <u>every active</u> SIPO output pin. The function is equivalent to invert_banks.
int set_array_pin	uint16_t pin, bool pin_status	pin number, or pin_set_failure	Sets the specified SIPO output pin to the given status, LOW or HIGH. The pin must be an active pin. If successful returns the pin address, otherwise returns -1 (pin_set_failure).
int invert_array_pin	uint16_t pin	status of inverted pin, or pin_invert_failure	Inverts the existing status of the specified SIPO output pin. The pin must be an active pin. If successful returns the pin's new inverted status, otherwise returns -1 (pin_invert_failure).
int read_array_pin	uint16_t pin	status of pin, or pin_read_failure	Reads the status of the specified pin in the <u>active</u> array. If successful returns the pin's existing status, otherwise returns -1 (pin_read_failure).
void xfer_array	bool msb_or_lsb	n/a	Shifts out/xfers the entire array of <u>active</u> virtual SIPO output pins to the physically connected SIPO ICs. The function is equivalent to the function xfer_banks(msb_or_lsb).
<b>SIPO Bank Functions (Relative Addressing, pin addresses - 0 to (number_active_pins_in_bank - 1))</b>			
int create_bank	uint8_t data_pin, uint8_t clock_pin, uint8_t latch_pin, uint8_t num_SIPOs	the bank's id, or create_bank_failure	Function creates a bank of SIPOs, assigning virtual output pins from the virtual pin array to the bank. Returns bank's id if successful, otherwise -1

Function/Method	Parameters	Returned Value(s)	Comments
			( <a href="#">create_bank failure</a> ).
<code>void set_bank</code>	<code>uint8_t bank,</code> <code>bool pin_status</code>	n/a	Function sets every SIPO output pin defined by the bank to the given status, <a href="#">LOW</a> or <a href="#">HIGH</a> .
<code>void set_banks</code>	<code>uint8_t from_bank,</code> <code>uint8_t to_bank,</code> <code>bool pin_status</code>	n/a	Function sets every SIPO output pin in every bank in the given range of banks, to the given status, <a href="#">LOW</a> or <a href="#">HIGH</a> .
<code>void set_banks</code>	<code>bool pin_status</code>	n/a	Function set every SIPO output pin in every bank to the given status, <a href="#">LOW</a> or <a href="#">HIGH</a> . It is equivalent to <a href="#">set_all_array_pins</a> ( <code>pin_status</code> ).
<code>void invert_bank</code>	<code>uint8_t bank</code>	n/a	Function will invert the existing pin status of every pin in the specified bank.
<code>void invert_banks</code>	<code>uint8_t from_bank,</code> <code>uint8_t to_bank</code>	n/a	Function will invert the existing pin status of every pin in each of the specified banks.
<code>void invert_banks</code>	none	n/a	Function will invert every SIPO output pin in every bank. Function is equivalent to <a href="#">invert_all_array_pins</a> .
<code>int set_bank_pin</code>	<code>uint8_t bank,</code> <code>uint8_t pin,</code> <code>bool pin_status</code>	pin's absolute address, or <a href="#">pin_set_failure</a>	Function sets the given pin ( <u>relative</u> address) in the given bank to the given status, <a href="#">LOW</a> or <a href="#">HIGH</a> . Note that if successful, the return value is the pin's <u>absolute</u> address in the array, otherwise it is -1 ( <a href="#">pin_set failure</a> ).
<code>int invert_bank_pin</code>	<code>uint8_t bank,</code> <code>uint8_t pin</code>	pin's inverted status, or <a href="#">pin_invert_failure</a>	Function inverts the given pin ( <u>relative</u> address) in the given bank. Note that if successful, the return value is the pin's inverted status_, otherwise it is -1 ( <a href="#">pin_invert failure</a> ).
<code>int read_bank_pin</code>	<code>uint8_t bank,</code> <code>uint8_t pin</code>	status of pin, or <a href="#">pin_read_failure</a>	Function reads the status of the specified pin in the specified bank. Note that if successful, the return value is the pin's status_, otherwise it is -1 ( <a href="#">pin_read failure</a> ).
<code>int set_bank_SIPO</code>	<code>uint8_t bank,</code> <code>uint8_t SIPO_num,</code> <code>uint8_t SIPO_value</code>	<code>status_byte</code> address, or <a href="#">SIPO_not_found</a> , or <a href="#">bank_not_found</a>	Function sets the specified SIPO (block of eight output pins) to the specified 8bit value. If successful the return value is the address of the <code>status_byte</code> holding the SIPO's 8bit status values, otherwise, if the specified SIPO does not exist in the bank -2 ( <a href="#">SIPO_not_found</a> ), or otherwise -1 if the specified bank does not exist ( <a href="#">bank_not_found</a> ).
<code>int invert_bank_SIPO</code>	<code>uint8_t bank,</code>	<code>status_byte</code> address,	Function inverts the specified SIPO (block of eight

Function/Method	Parameters	Returned Value(s)	Comments
	<code>uint8_t SIPO_num</code>	or <code>SIPO_not_found</code> , or <code>bank_not_found</code>	output pins). If successful the return value is the address of the <code>status_byte</code> holding the SIPO's 8bit status values, otherwise, if the specified SIPO does not exist in the bank -2 ( <code>SIPO_not_found</code> ), or otherwise -1 if the specified bank does not exist ( <code>bank_not_found</code> ).
<code>int read_bank_SIPO</code>	<code>uint8_t bank</code> , <code>uint8_t SIPO_num</code>	<code>status_byte</code> value, or <code>SIPO_not_found</code> , or <code>bank_not_found</code>	Function reads the specified SIPO (block of eight output pins). If successful the return value is the value of the SIPO's <code>status_byte</code> (8bits), otherwise, if the specified SIPO does not exist in the bank -2 ( <code>SIPO_not_found</code> ), or otherwise -1 if the specified bank does not exist ( <code>bank_not_found</code> ).
Other SIPO Functions			
<code>int get_bank_from_pin</code>	<code>uint16_t pin</code>	<code>bank_id</code> , or <code>bank_not_found</code>	Function returns the bank id the specified pin (absolute address) resides in. If the pin cannot be located in a bank the function returns -1 ( <code>bank_not_found</code> ).
<code>int num_pins_in_bank</code>	<code>uint8_t bank</code>	number of pins, or <code>bank_not_found</code>	Function returns the number of SIPO pins the given bank maps/owns. If the bank id is not valid the function returns -1 ( <code>bank_not_found</code> ).
<code>void xfer_bank</code>	<code>uint8_t bank</code> , <code>bool msb_or_lsb</code>	n/a	The function shifts out/xfers all of the SIPO pins within the specified bank.
<code>void xfer_banks</code>	<code>uint8_t from_bank</code> , <code>uint8_t to_bank</code> , <code>bool msb_or_lsb</code>	n/a	The function shifts out/xfers all of the SIPO [pins of each of the specified banks/
<code>void xfer_banks</code>	<code>bool msb_or_lsb</code>	n/a	The functions shifts out/xfers all of te SIPO pins for ALL banks. The function is equivalent to the <code>xfer_array(msb_or_lsb)</code> function.
<code>void print_pin_statuses</code>	none	n/a	A useful function for development/debugging - prints the pin status of every <u>active</u> SIPO pin in every bank.
<code>void print_SIPO_data</code>	none	n/a	A useful function for development/debugging - prints the SIPO8 library data set up by the class initiation process. The output confirms that the sketch's configuration is setup as intended.
<code>void SIPO8_start_timer</code>	<code>uint8_t timer</code>	n/a	The function marks the specified timer as active.
<code>void SIPO8_stop_timer</code>	<code>uint8_t timer</code>	n/a	The function marks the specified timer as inactive.
<code>bool</code>	<code>uint8_t timer</code> ,	<code>elapsed</code> or	The function determines if the specified timer has

Function/Method	Parameters	Returned Value(s)	Comments
<code>SIPO8_timer_elapsed</code>	<code>uint32_t</code> <code>elapsed_time</code>	<code>!elapsed</code>	reach/surpassed the given elapsed time parameter. The return value is <code>true</code> if timer has elapsed ( <code>elapsed</code> ), otherwise <code>false</code> ( <code>!elapsed</code> ). If the specified timer is not a valid timer the return value is also <code>false</code> ( <code>!elapsed</code> ).
<b>Private Functions</b>			
<code>void shift_out_bank</code>	<code>uint8_t</code> data_pin, <code>uint8_t</code> clock_pin, <code>uint8_t</code> status_bits, <code>bool</code> msb_or_lsb	n/a	A standard shift out function - transfers the given status_bits to the 3-wire digital interface defined for the associated bank. Function is called from the xfer functions.
<code>void SIPO_lib_exit</code>	<code>uint8_t</code> reason	n/a	The function is used exclusively by the class constructor if the required memory requirements cannot be met from the available free memory.

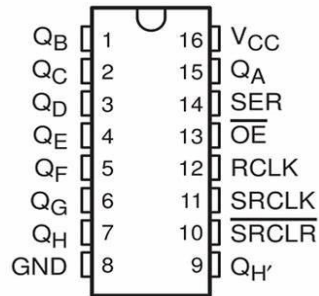
## SIPO Mapping Table/Documentation

Project:								Date:			
User Defined Parameters				create_bank Results							
Control Pins			Num SIPO ICs in Bank	Bank Num	First Pin	Last Pin					
Data	Clock	Latch									

(add more rows as needed)

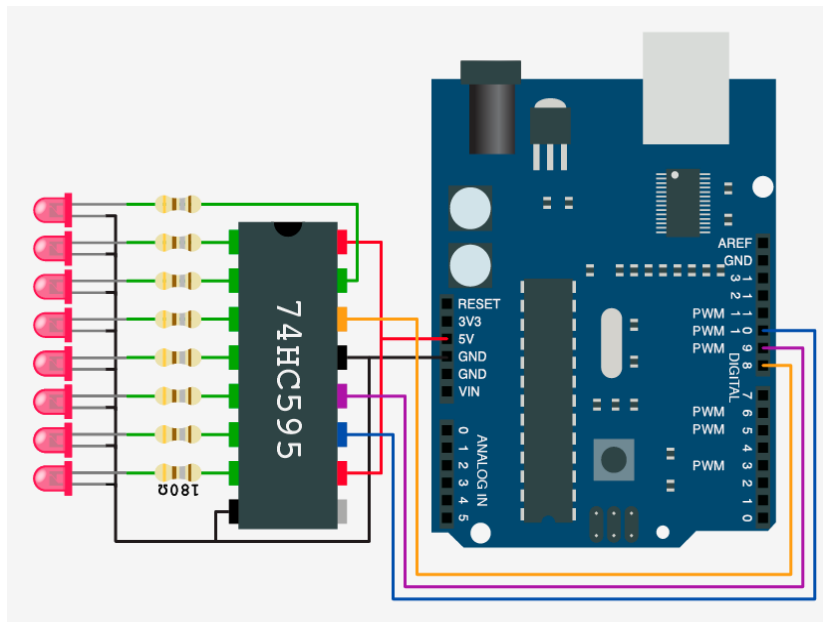


## SIPO 8bit IC Pin Outs/Wiring Scheme

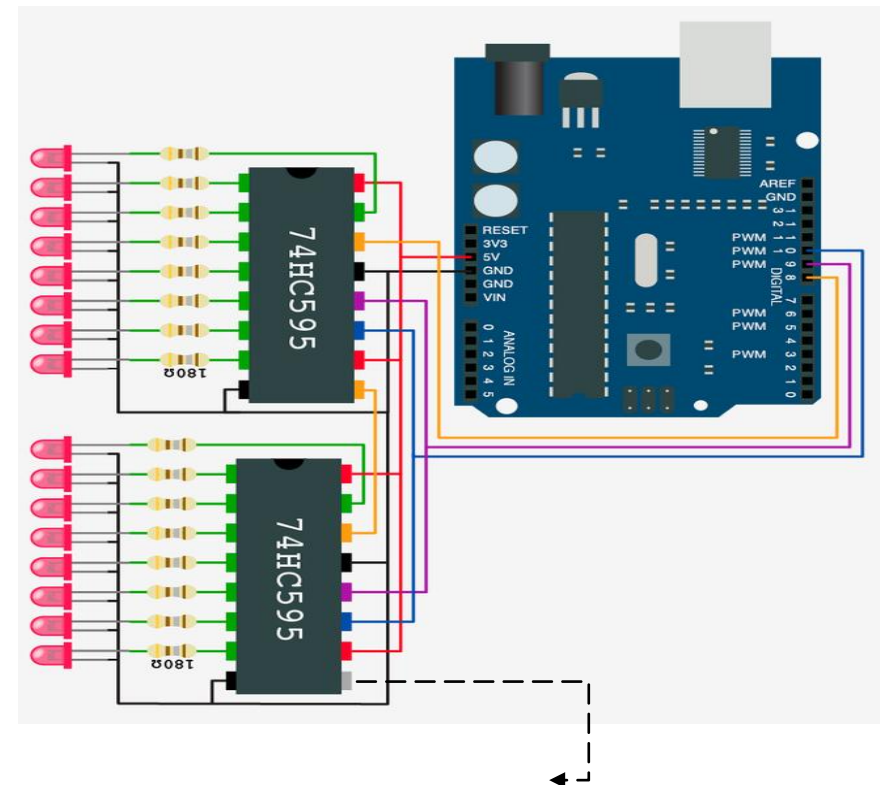


Pin Outs 1 - 74HC595

Example pin assignments	
74HC595 pins	Arduino pins
14, SER, DATA IN pin	8
12, RCLK, LATCH pin	9
11, SRCLK, CLOCK pin	10



Single SIPO IC



Dual SIPO ICs, cascaded,

next SIPO IC to pin 14