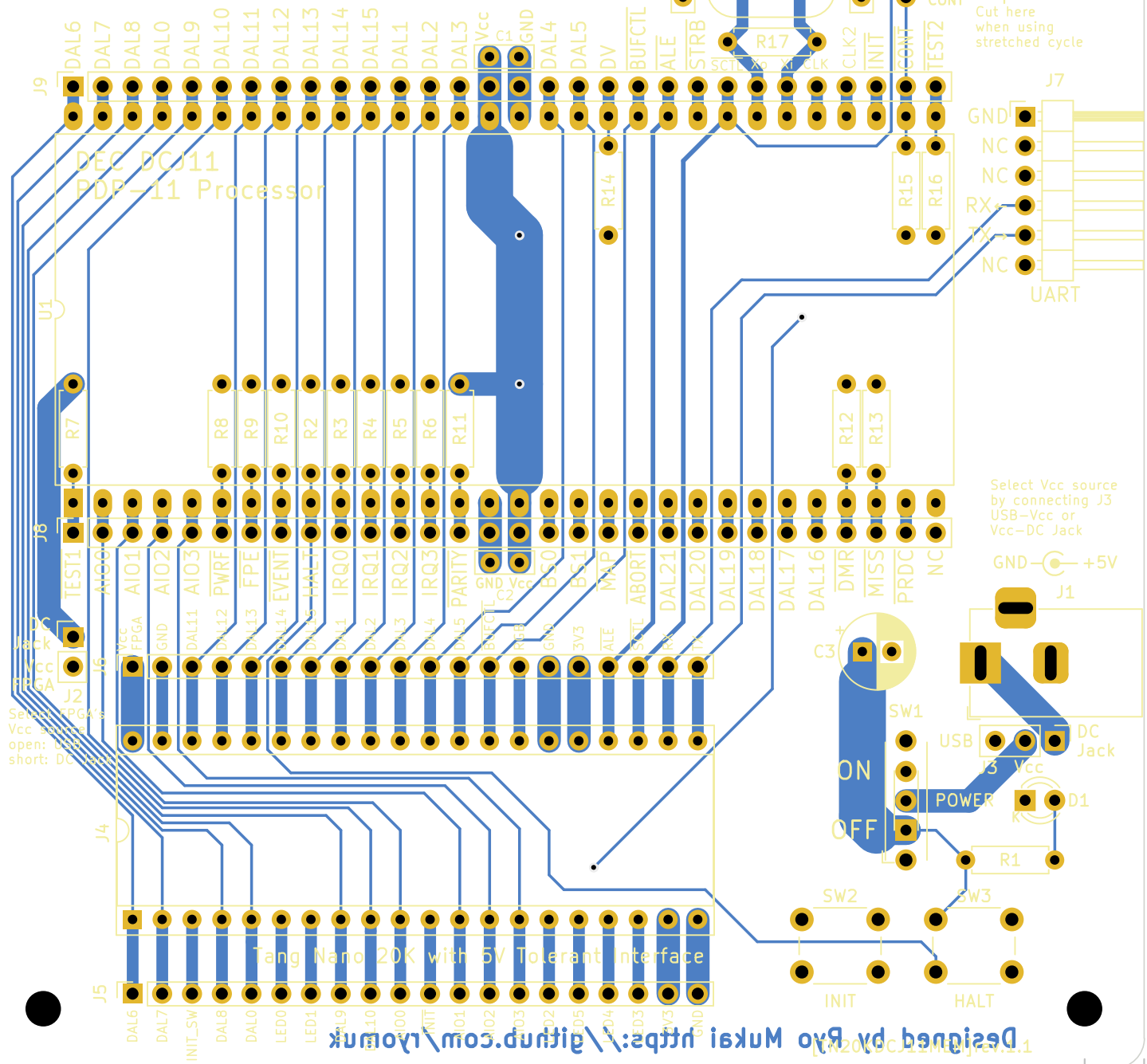


3900.0000 mils

3900.0000 mils

# Memory System and UART for DEC DCJ11 on TangNano20K with 5V Interface



Cut here when using stretched cycle

Select Vcc source by connecting J3  
USB-Vcc or Vcc-DC Jack

Set the FPGA's Vcc source:  
open: USB  
short: DC Jack

Designed by Ryo Muraishi  
http://github.com/ryomuraishi/decj11  
[TangNano20K] rev.1.1

200,000 mils  
200,000 mils