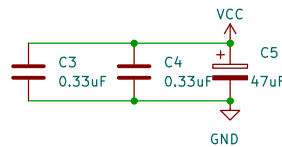
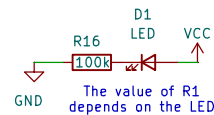
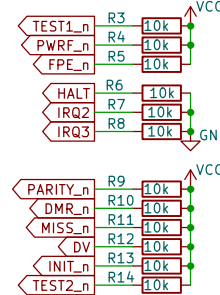
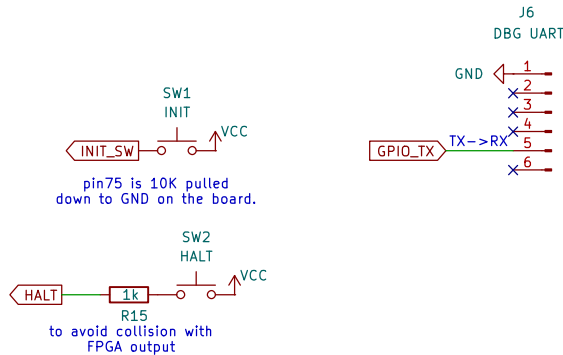
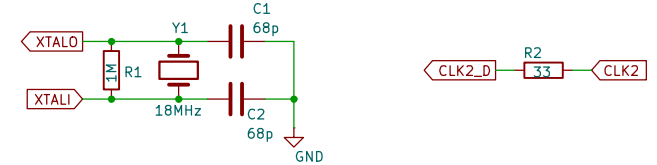
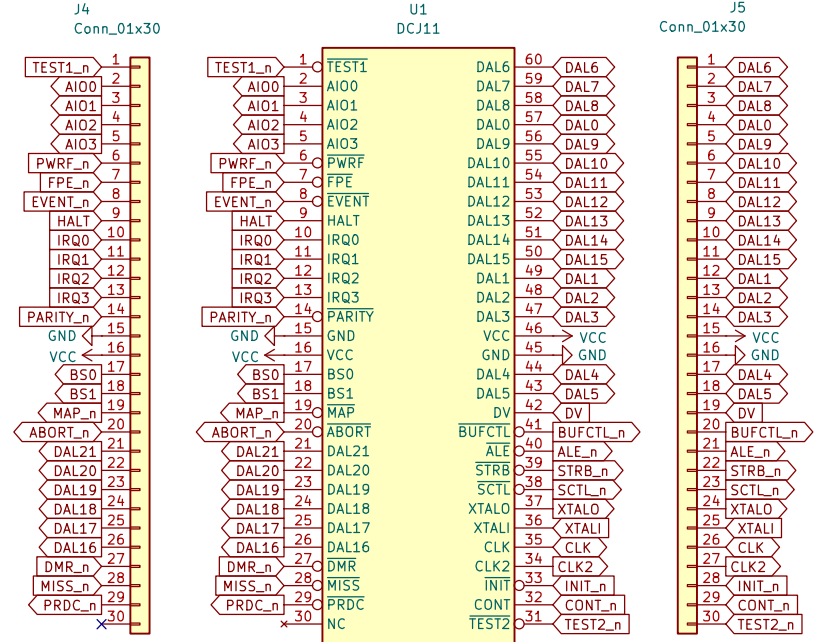
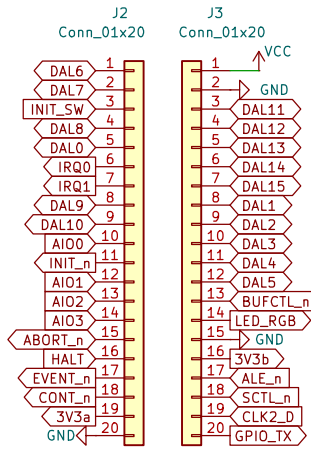


Some pins are pulled up/down on the board.
pin75: 10K (and 100nF early version) to GND
pin26: 5.1K to GND (I/H=0.6mA)
pin15 to 20: 510R and LED to 5V (I/L=3.0mA)
pin52, 53: 2.2K to 3.3V, FET and 2.2K to 5V (I/L=4.4mA)



modified to use CLK2

Ryo Mukai

Sheet: /
File: TangNanoDCJ11MEM.kicad_sch

Title: Memory System and UART for DCJ11 using Tang Nano 20K

Size: A4 Date: 2025-08-26

KiCad E.D.A. 8.0.5

Rev: 2.0

Id: 1/1