

FIG.1 MEMORY ADDRESS AND DISPLAY AREA

ELECTRICAL CHARACTERISTICS

				1.	
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Display Supply Voltage	V <sub>o</sub>	10.8	12.0	13,2	V
*Display Supply Current	ro		240	280	mA
Logic Supply Voltage	$v_{ec}$	4.75	5,0	5.25	٧
Logic Supply Current	Icc	-	10	15	mA
High Level Input Voltage	VIR	0.7900	•	5,25	V
Low Level Input Voltage	V <sub>IL</sub>	. 0	-	0.2VCC	v
High Level Output Voltage (I <sub>OR</sub> = -50 uA)	Ko <sup>V</sup>	4.45	<b>-</b> .	••	V
Low Level Output Voltage (Ior = 1.6 mA)	$\Lambda^{\Diamond \mathbf{r}}$	-	•	0.05	v

<sup>\*</sup> Measured under character display(See attached sheet)

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一种通数设建设置。

System Side Housing MapH4-1 (Burndy) (2) Power Supply Fin Connection (CN2)

Module Side MMP48-1 (Burndy) 11 Connector

V<sub>cc</sub> (+5V) Stanal

GRO

Vo (+12V)

28

Langis

DI OTA

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