

## N7 2013

LCD (U2)

1	P	
2		
3		
4		
5	GND	
6	T	
7	T	
8	GND	
9	T	PAIR
10	T	
11	GND	
12	T	PAIR
13	T	
14	GND	
15	T	PAIR
16	T	
17	GND	
18	T	PAIR
19	T	
20	GND	
21	T	PAIR
22	T	
23	GND	
24	T	
25	T	
26	P	
27		
28	GND	
29	T	
30	T	
31	P	

STACK PCB CONN. CON1

2	GND
4	9
6	10
8	GND
10	12
12	13
14	GND
16	15
18	16
20	GND
22	18
24	19
26	GND
28	21
30	22
32	GND
34	24

1	1,2,3,4
3	
5	
7	
9	GND
11	6
13	7
15	GND
17	29
19	25
21	GND
23	26,27
25	
27	GND
29	GND
31	31
33	30

T - Thin track

P - thick track/Power track (?)

## N7 2012

**LCD (U1)**

1	n.c.	
2	P	VCC_LED
3		
4		
5		
6	n.c.	
7	GND	
8	T	LCD_BL_PWM
9	GND	
10		
11		
12		
13	n.c.	
14	n.c.	
15	GND	
16	T	PAIR TXECK1
17	T	
18	GND	
19	T	PAIR TXE2
20	T	
21	GND	
22	T	PAIR TXE1
23	T	
24	GND	
25	T	PAIR TXE0
26	T	
27	GND	
28	n.c.	
29	P	VCC_LCD3V3
30		
31		

**STACK PCB CONN. CON3711**

2	GND	1	29,30,31
4	26	3	
6	25	5	
8	GND	7	GND
10	23	9	
12	22	11	
14	GND	13	
16	20	15	
18	19	17	
20	GND	19	n.c.
22	17	21	
24	16	23	2,3,4,5
26	GND	25	
28	n.c.	27	
30	8	29	

T - Thin track

P - thick track/Power track (?)