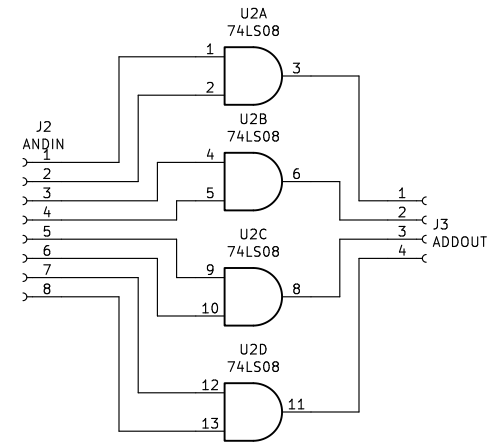
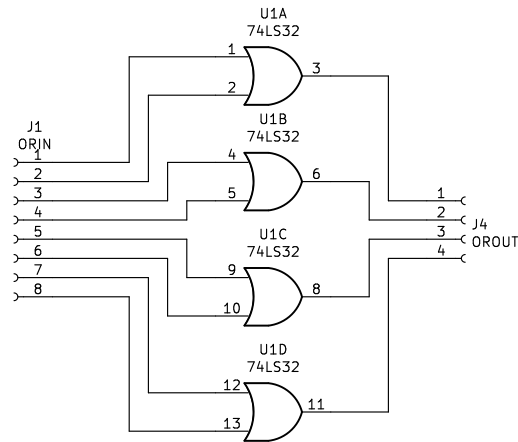
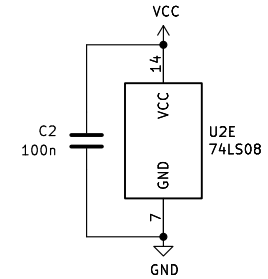
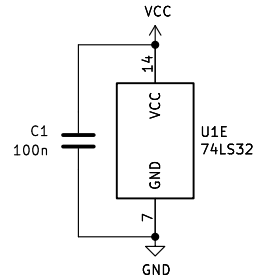


## Connector to Display

All the signals (and power) is exchanged via this connector to the display boards



## Logic Gates

The signals between logic gates and the the main connector must be done by hand by the user using jumper wires between the pads for each respective signal.

**NOTE: TWO SEPERATE SIGNALS SHOULD NEVER BE SHORTED TOGETHER TO AVOID DAMAGE IF THEY TRY TO DRIVE THE SHARED CONNECTION DIFFERENT WAYS!**

User configured logic for resetting counters  
Configured using wires soldered between headers

**Savo Bajic**

Sheet: /

File: reset\_module\_v3.kicad\_sch

**Title: Clock Reset Logic**

Size: USLetter Date: 2024-01-03

KiCad E.D.A. kicad 7.0.10-7.0.10-ubuntu22.04.1

**Rev: 3**

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