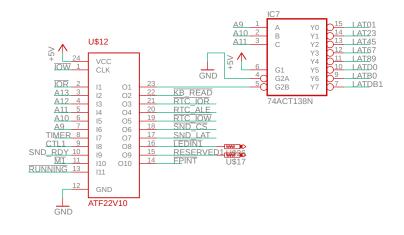
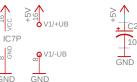


Bus connector, from CPU board

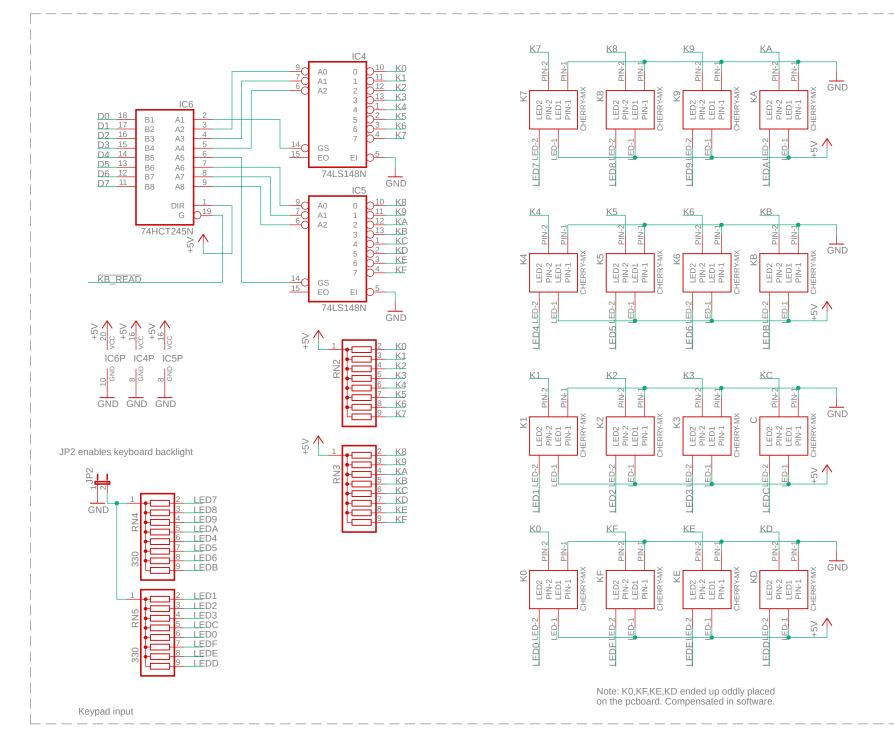


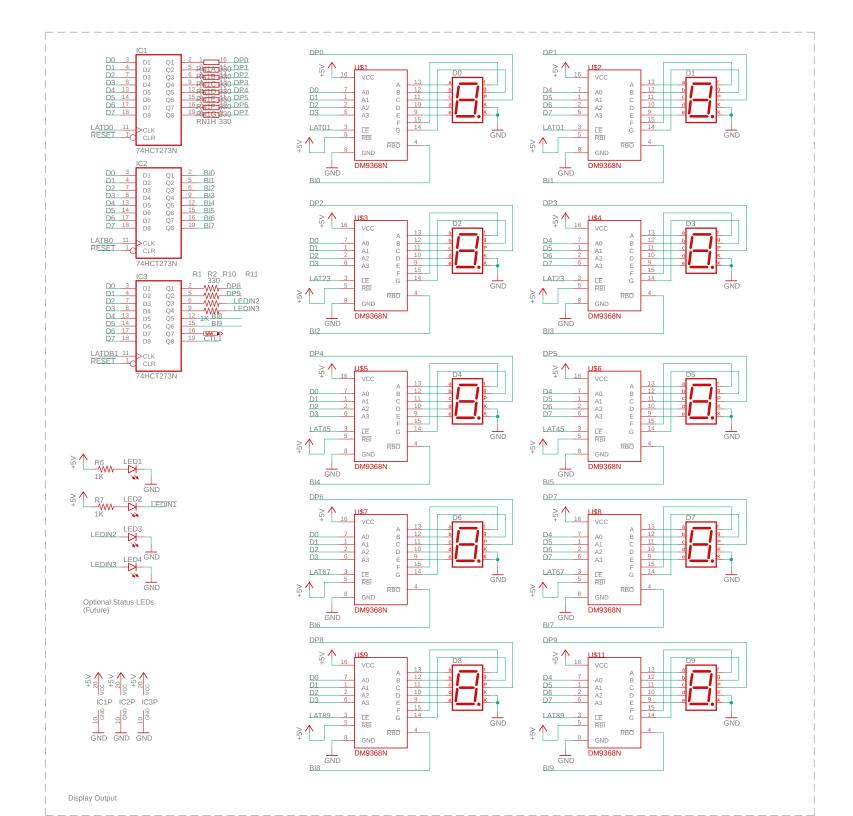


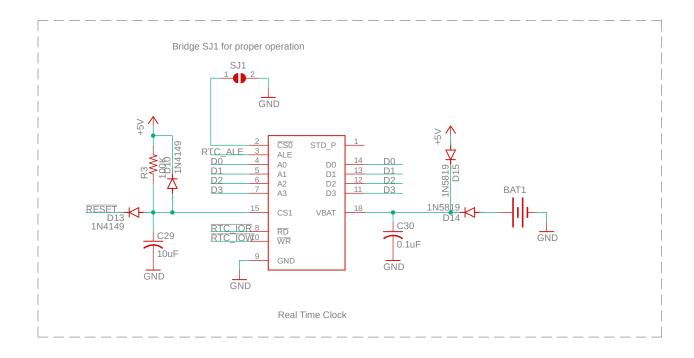


IO addressing PLD









## JP1 - Enable Speaker Output

