













ID	Task Name	Start	Finish	Duration	Oct 2012				Nov 2012				Dec 2012				Jan 2013				Feb 2013				Mar 2013				Apr 2013			
					30/9	7/10					4/11					2/12	9/12					6/1	13/1	20/1	27/1	3/2	10/2	17/2	24/2	3/3	10/3	17/3
1	Research	01/10/2012	12/10/2012	2w																												
2	Initial Hardware Development	12/10/2012	26/11/2012	6.4w																												
3	Comparison Algorithms Investigation	30/10/2012	27/11/2012	4.2w																												
4	Interim Report Writing	26/11/2012	10/12/2012	2.2w																												
5	Movement Development	12/12/2012	20/12/2012	1.4w																												
6	PCB Design	10/12/2012	24/01/2013	6.8w																												
7	Holiday and Exams	19/12/2012	24/01/2013	5.4w																												
8	PCB Construction and Test	25/01/2013	25/02/2013	4.4w																												
9	Porting to AT32	06/02/2013	25/02/2013	2.8w																												
10	2D FFT on AT32	25/02/2013	15/03/2013	3w																												
11	Movement Development Revisited	04/03/2013	15/03/2013	2w																												
12	Testing	15/03/2013	10/04/2013	3.8w																												
13	Report Writing	15/03/2013	23/04/2013	5.6w	