

Source: <https://github.com/semianalog/xmega-au>
CC0: <https://creativecommons.org/publicdomain/zero/1.0/>

DEV-XMEGA-AU

Title: XMEGA AU development board

Sheet: /RAM/

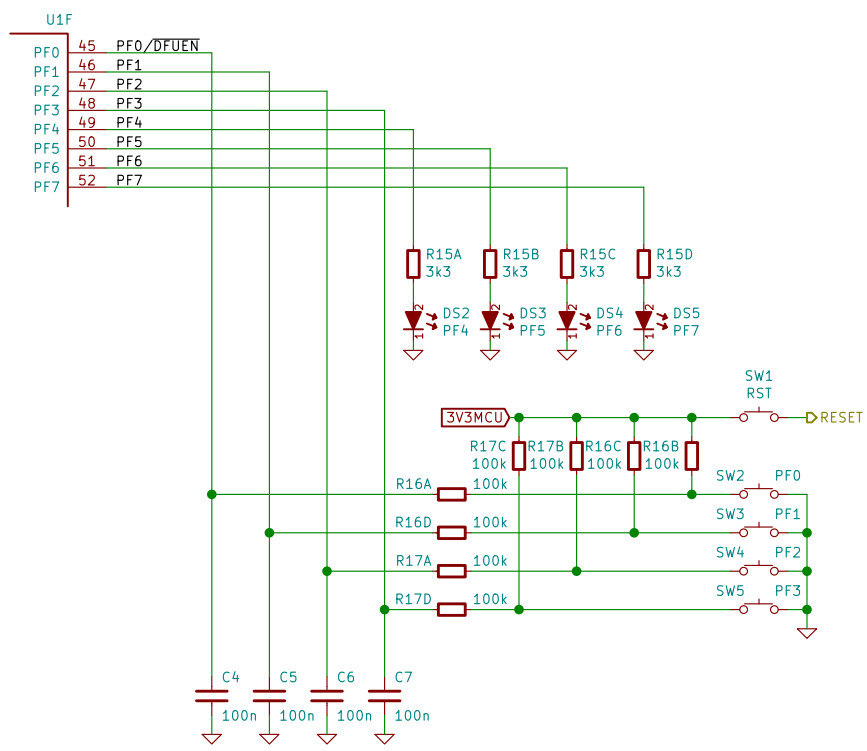
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Rev: A

Id: 2/5

Date: 2016-03-22



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DEV-XMEGA-AU

Title: XMEGA AU development board

Sheet: /UserIO/

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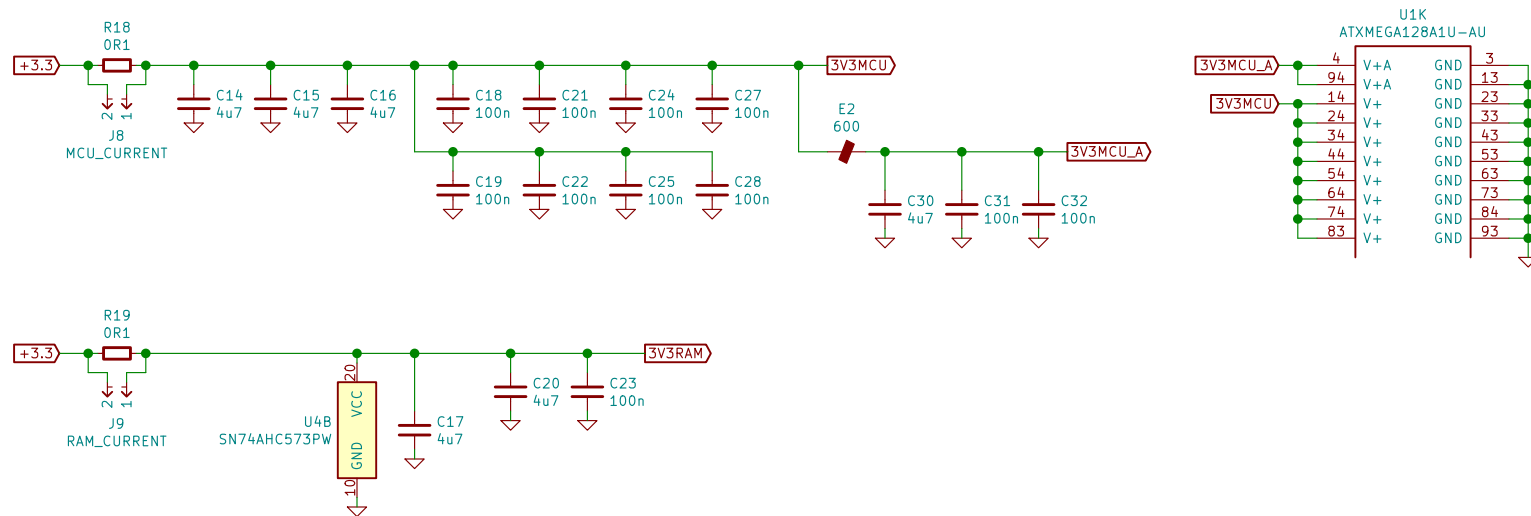
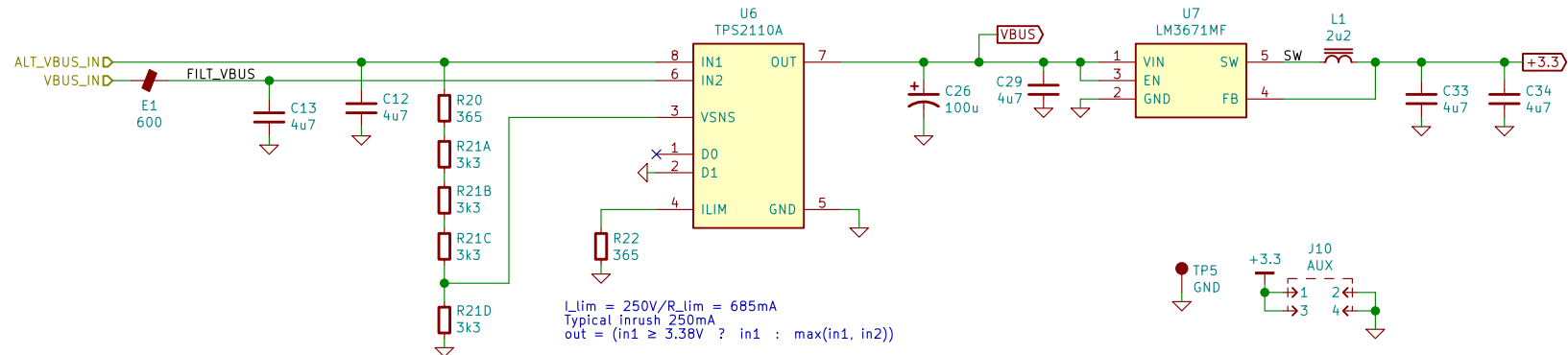
Rev: A

Id: 3/5

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DEV-XMEGA-AU

Title: XMEGA AU development board

Sheet: /Power/

SEMIANALOG



Rev: A

Id: 5/5

Date: 2016-03-22