

Logik

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graph TD; subgraph System; direction TB; L[Logik]; M1[Real Time Clock]; M2[Communi-cation]; M3[Motor]; M4[proximity sensor]; M5[Line sensor]; M6[LED]; M7[Interrupt]; M8[ADC]; M9[I2C]; H[Hardware]; L --- M1; L --- M2; L --- M3; L --- M4; L --- M5; L --- M6; L --- M7; L --- M8; L --- M9; M1 --- H; M2 --- H; M3 --- H; M4 --- H; M5 --- H; M6 --- H; M7 --- H; M8 --- H; M9 --- H; end
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Real
Time
Clock

Communi
-cation

Motor

proximity
sensor

Line
sensor

LED

Interrupt

ADC

I2C

Hardware