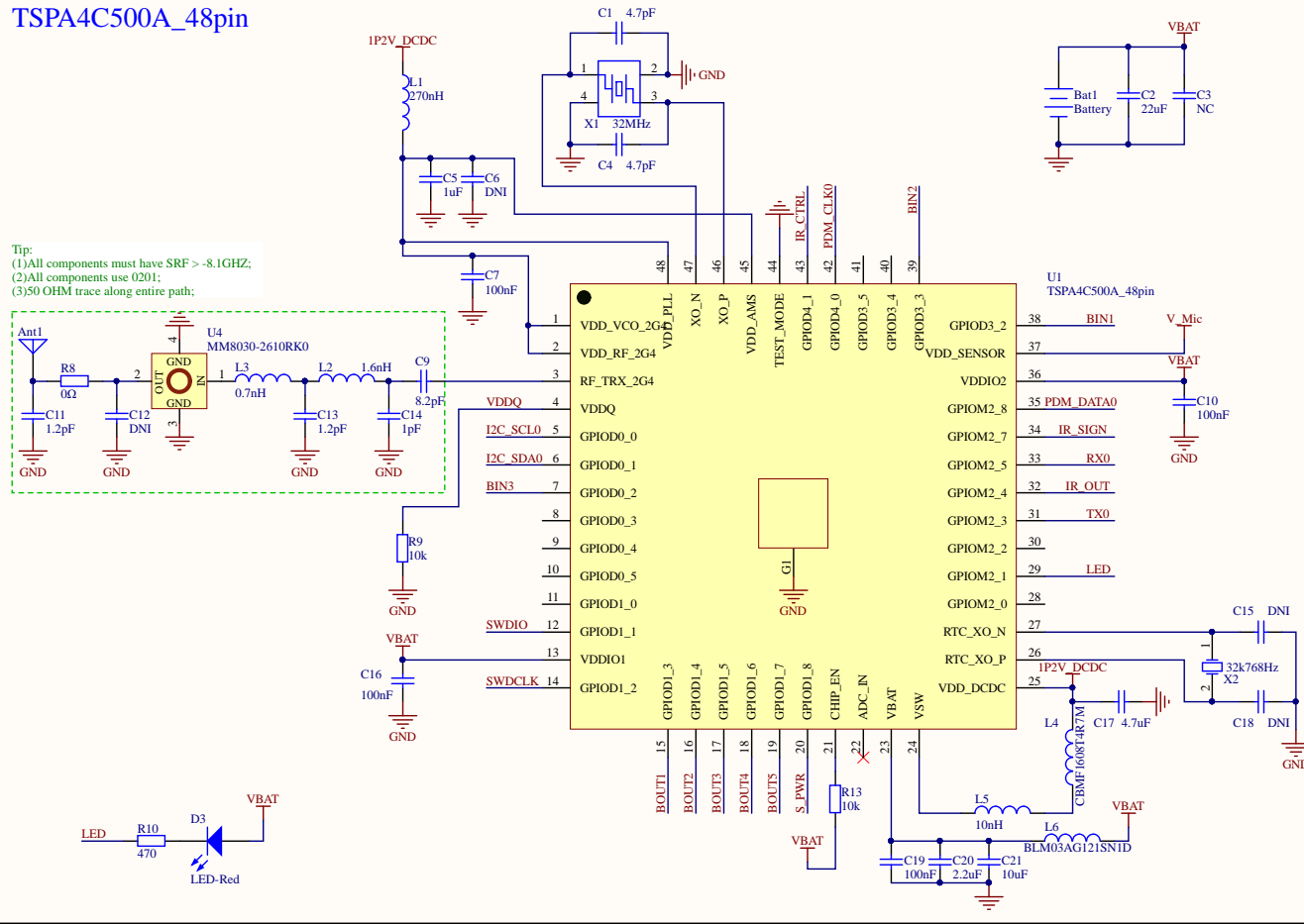


TSPA4C500A_Remoter 电路图

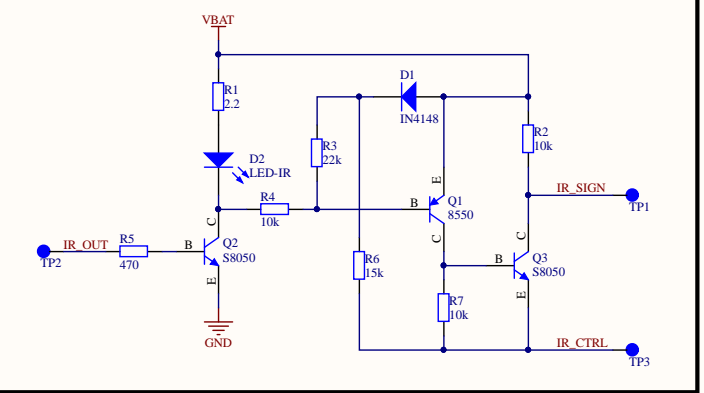
REVISION HISTORY					
REV	DATE	ECN	DESCRIPTION	DRN	CKD
C	2019.05.14	N/A		Liz	Wythe
B	2019.04.03	N/A		Sholin	Wythe

TSPA4C500A_48pin

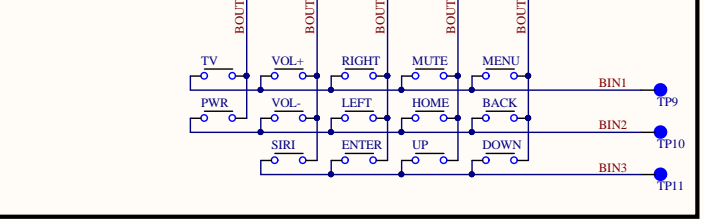
Tip:
(1)All components must have SRF > -8.1GHZ;
(2)All components use 0201;
(3)50 OHM trace along entire path;



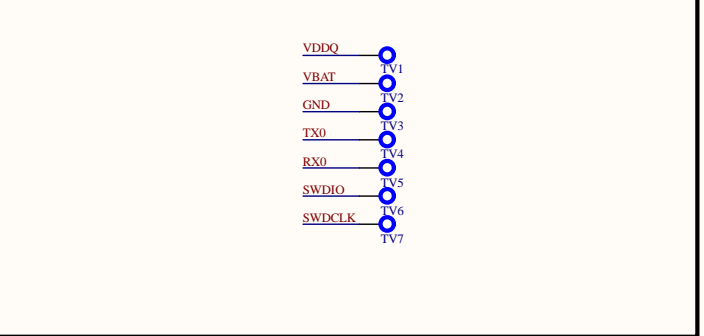
IR_Read&Learn



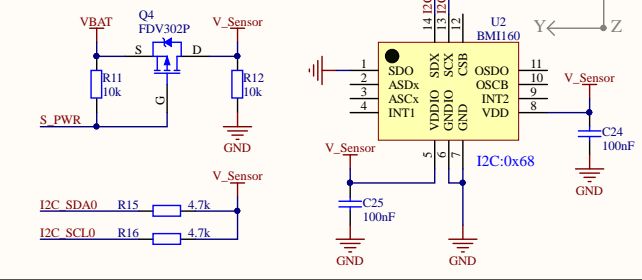
Buttons



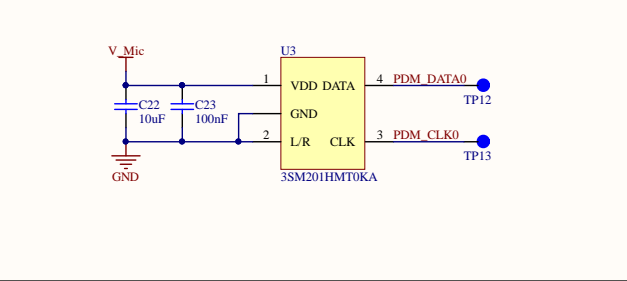
Debug_Port



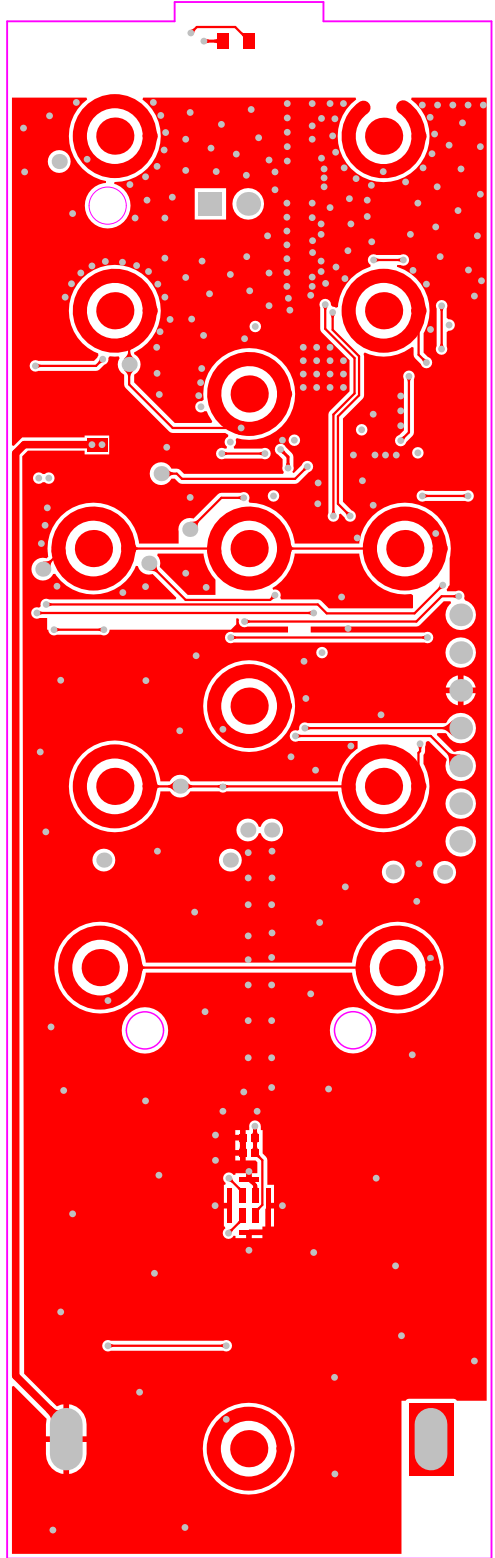
空鼠六轴



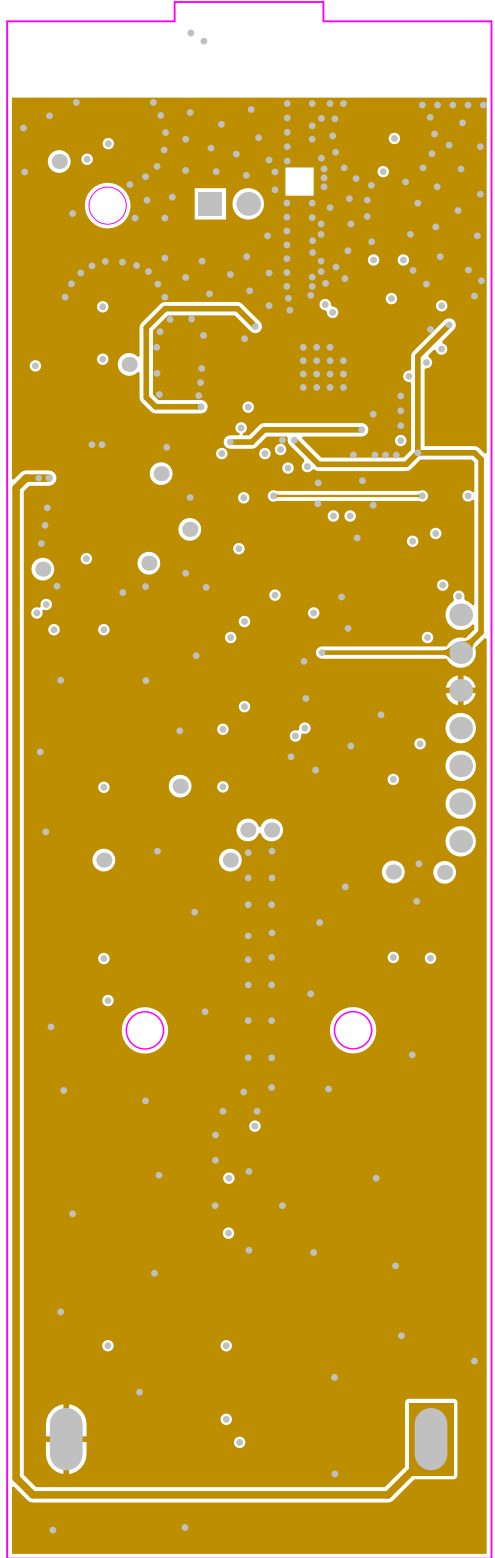
PDM_MIC



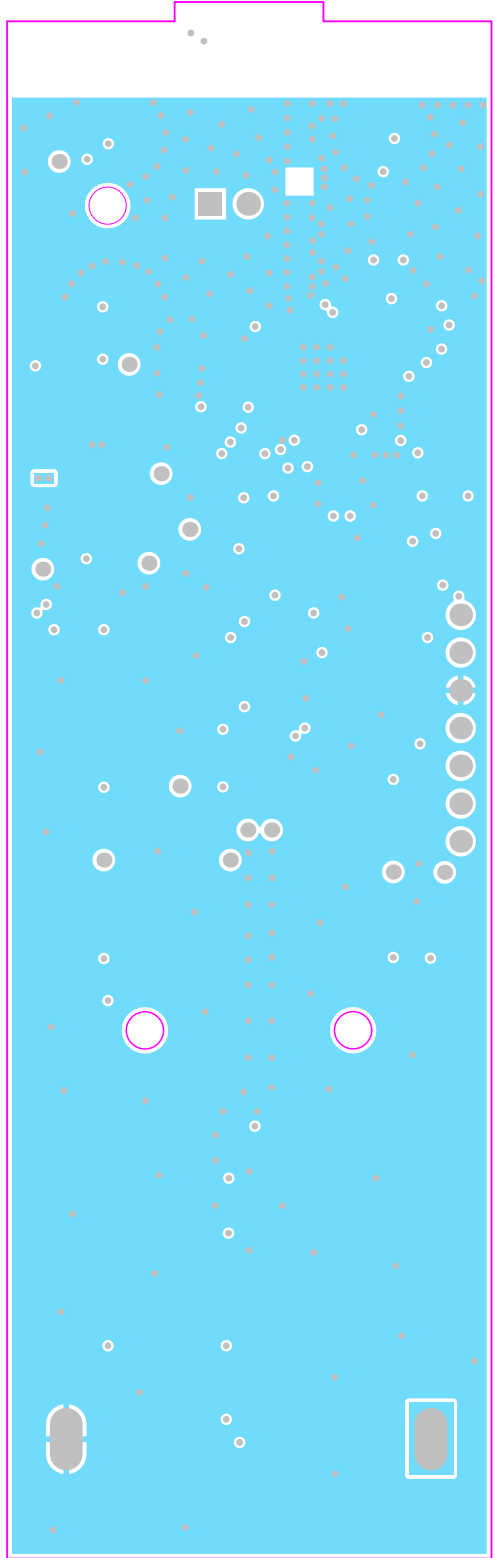
Cannot open file C:\Users\Laura\Desktop\mStink-logo.png		Title TSPA4C500A_Remoter 电路图	
Size A3	Number		Revision B
Date: 2019.05.14		Sheet 1 of 1	
File: TSPA4C500A_Remoter.SchDoc		Drawn By: Liz	
MS-DC20-GEN-01-06A			



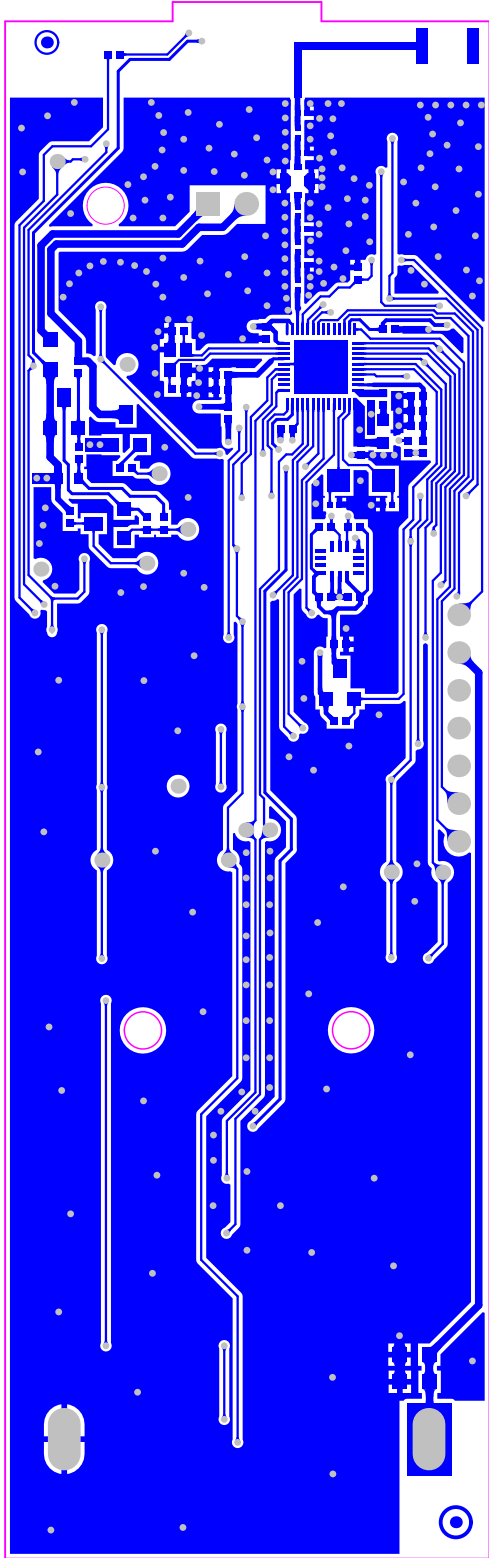
Top



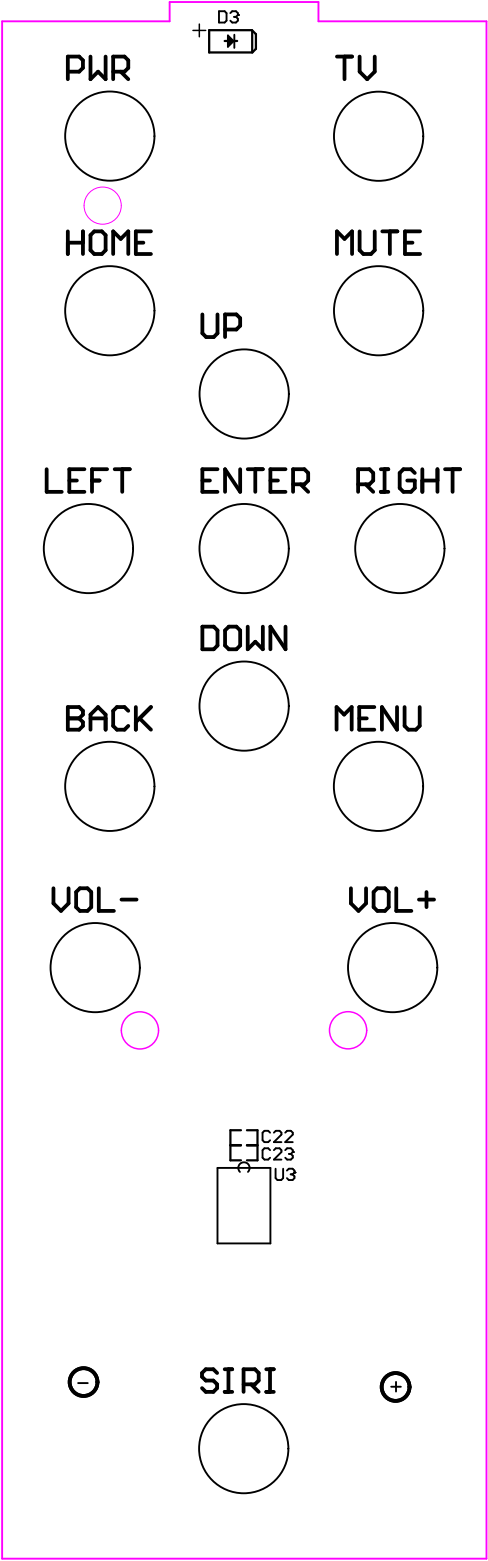
GND



VCC

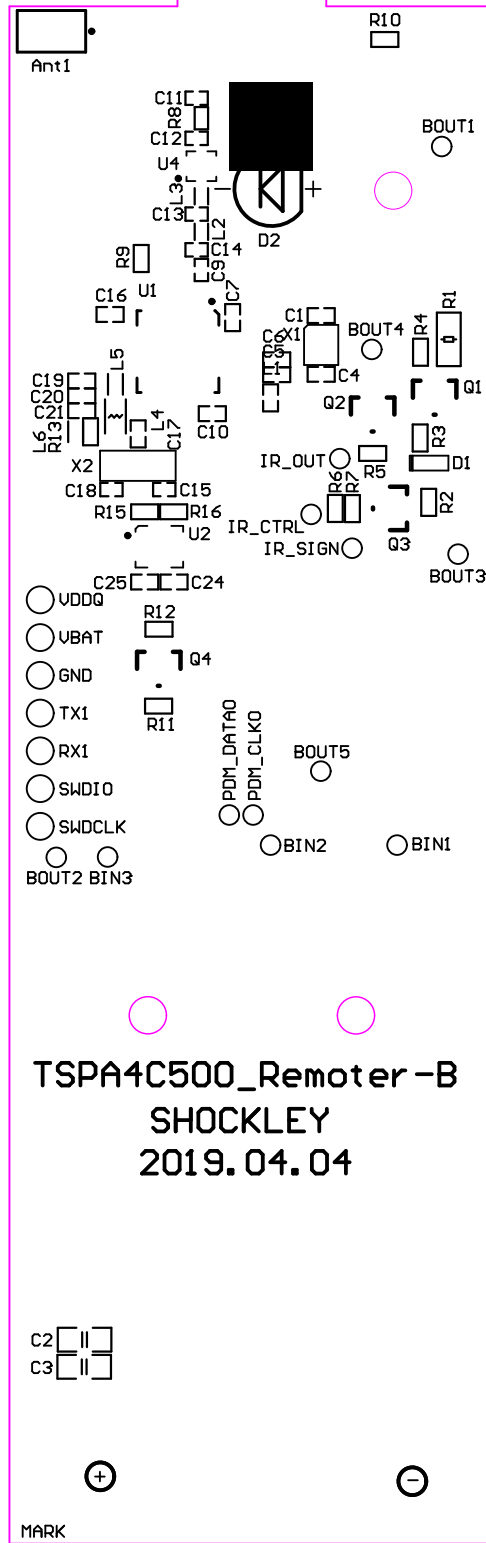


Bottom



TopSilk

BottomSilk



TSPA4C500_Remoter-B
SHOCKLEY
2019.04.04