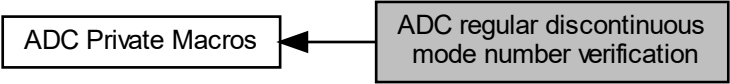


ADC Private Macros

ADC regular discontinuous
mode number verification



```
graph RL; A[ADC regular discontinuous mode number verification] --> B[ADC Private Macros];
```

The diagram consists of two rectangular boxes connected by a horizontal arrow pointing from right to left. The box on the right is shaded gray and contains the text 'ADC regular discontinuous mode number verification'. The box on the left is white with a black border and contains the text 'ADC Private Macros'. The arrow indicates that the verification process depends on or utilizes the private macros.