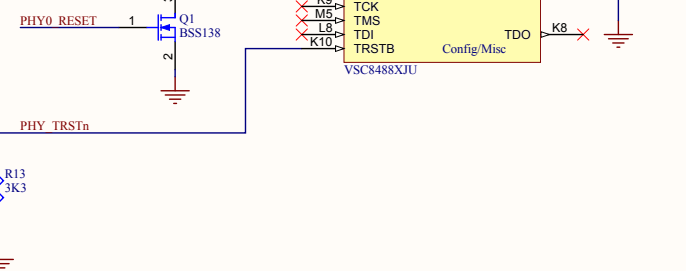
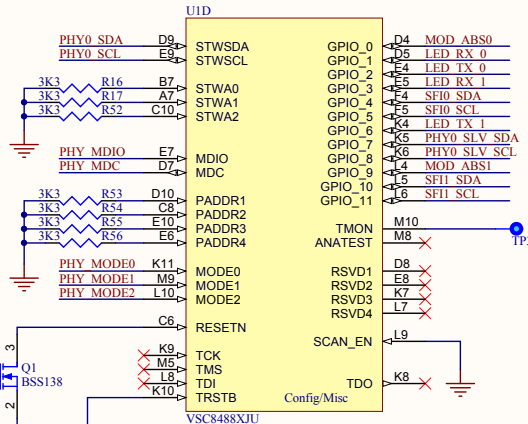
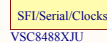
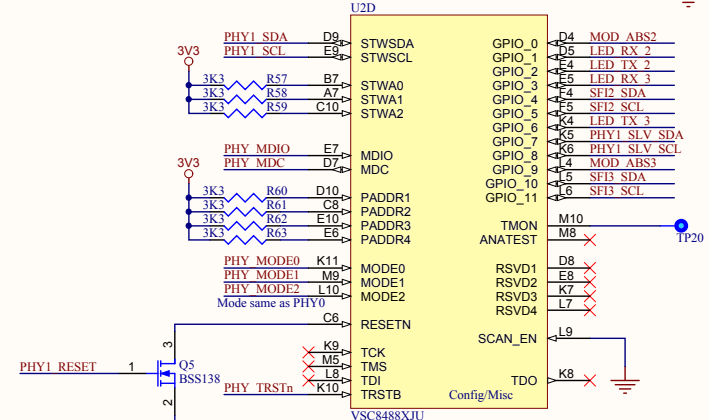
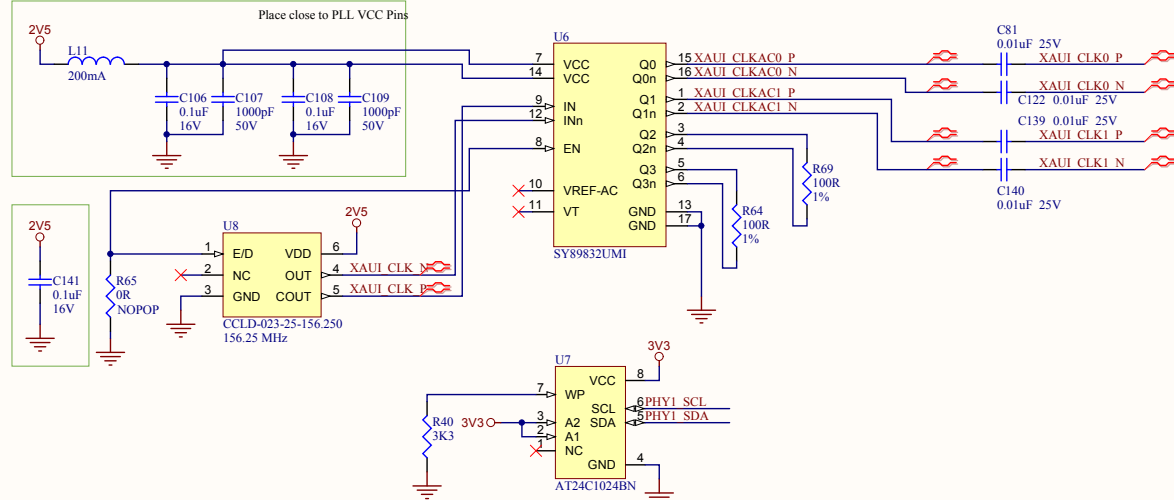
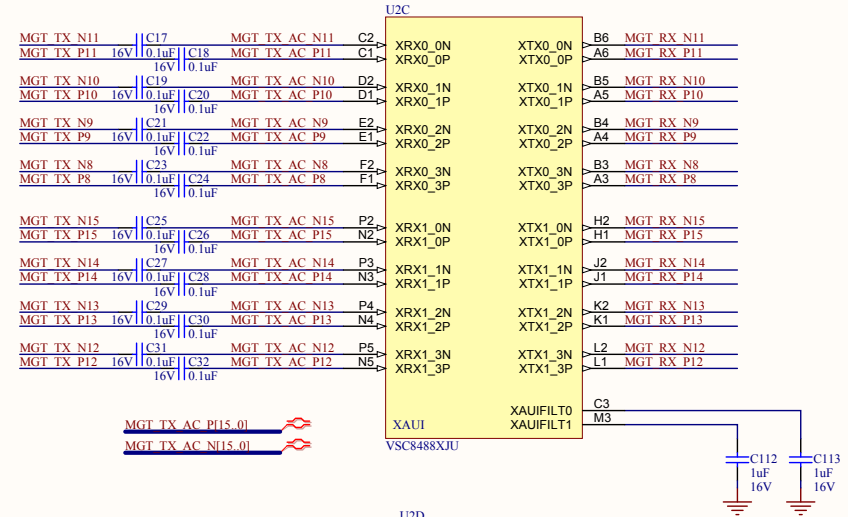
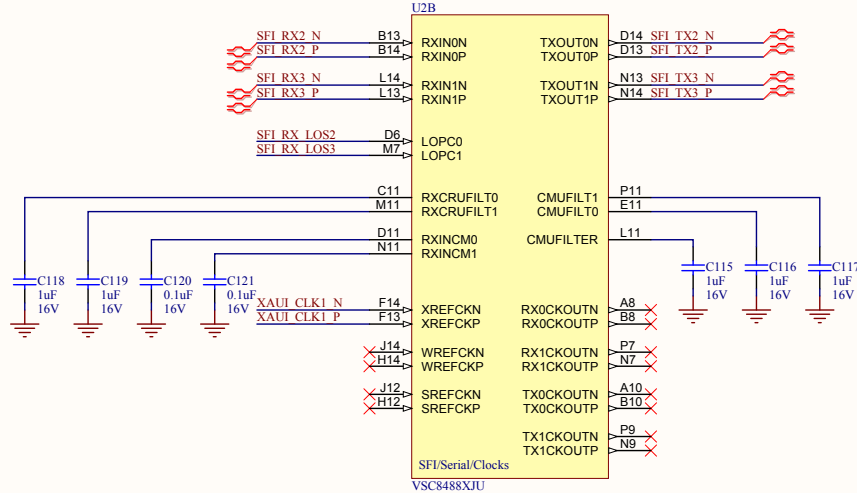
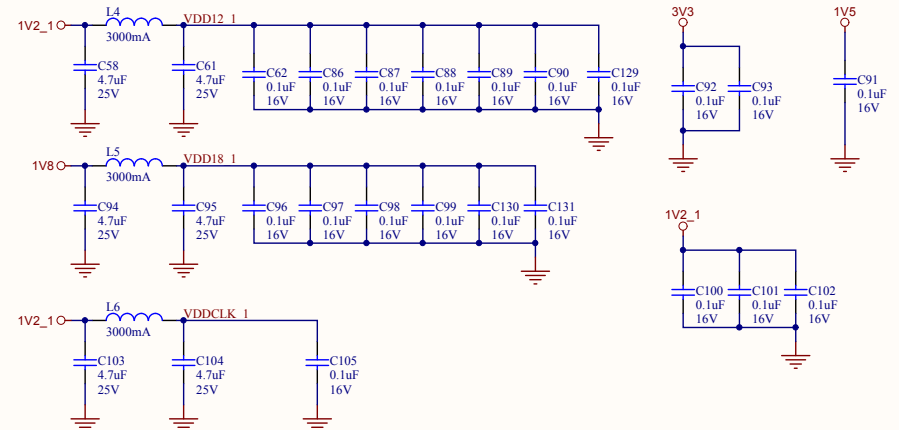
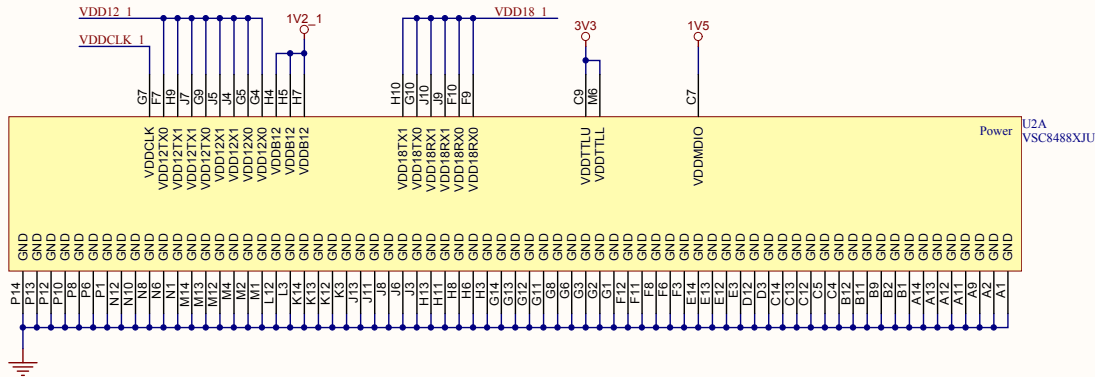


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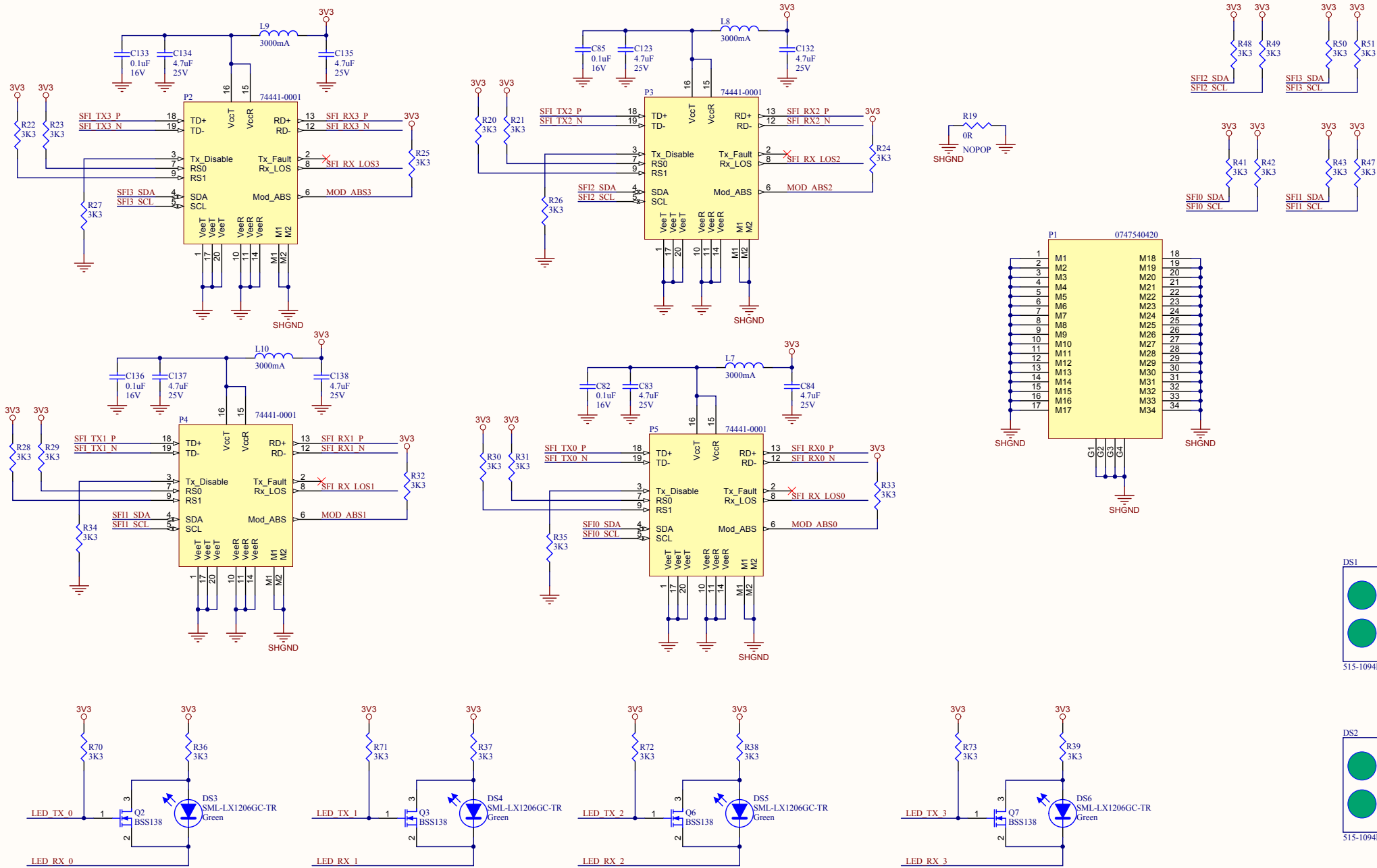
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SCHEMATIC FILENAME: phy1.SchDoc
DRAWN BY: D. George
ENGINEER: D. George
APPROVED:

CIRCUIT DIAGRAM
SFP+ Mezzanine Card
SFP+ PHY 1

DATE: MAY 2012
SHEET: 3 of 4
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