Santiago López Rojas

Electronic Engineer

1 +506 8964 0767 **2** santiago241996@gmail.com

Costa Rica, Cartago, Residencial Hacienda del Rey, casa 26i

i ID 1-16600-593

Electronic Engineer from ITCR, with knowledge in design, simulation and construction of analog and digital circuits. Interest on working with digital signal processing, embedded systems and artificial intelligence. Passionate about research, always looking for new and better ways to perform any process when necessary.

EDUCATION

2014 - 2019 Electronics Engineering, Lic. Degree, Instituto Tecnológico de Costa Rica

2009 - 2013 High school, Colegio Sagrado corazón. San José, Costa Rica

LANGUAGES

Spanish • English



- > Passionate
- > Sociable
- > Patient
- > Hard-Working
- > Discipline
- > Team-Worker
- > Determinate

📑 Hardware and Software Skills

Programming Languages C, C++, Python, Java, Verilog, x86 assembly

Mathematical Tools MatLab, Octave

> Design Tools Simulink, rltool, Pidtool, Filter Designer Matlab

Developer Tools Psoc Creator, Qtcreator, Code Composer, Arduino, Eclipce, ISE Design Suite, Sublime Text, Vi-

sual Studio

Modelsim. Multisim, LTspice Simulation Tools

Operative Systems Windows, Linux

> Others Microsoft office, LTFX, Dropbox, GitHub, VideoPad, QGiS, ILWIS, GoogleColab, Agisoft Photo-

Scan, academic knowledge in database



EXPERIENCE

Currently September 2019

Programmer Analyst I, COVANCE, Costa Rica

- > Python Programming
- > Analysis of pharmaceutical data
- > Set up data entry screens per approved specifications for selected projects
- > Ensure awareness of project specific quality and performance standards and ensure that these are adequately documented, communicated and understood

Python Pandas SAS

Currently January 2019

Student Assistant, UAS PHOTOGRAMMETRYLABORATORY, Instituto Tecnológico de Costa Rica

- > Image processing
- > Application of IA techniques
- > Project development, applying research methods in innovative areas

QGIS ILWIS YOLO GoogleColab

August 2018 February 2018

Student Exchange, UNIVERSIDAD DE MÁLAGA, España

- > Embedded systems
- > Automatic Control
- > Power Electronics Circuits
- > Introduction to Electric Vehicles

Matlab Code Composer Qtcreator Psoc Creator

Currently 2014

Athlete, SWIMMING TEAM, Instituto Tecnológico de Costa Rica

- > Participation in JUNCOS Costa Rica, 2019
- > Participation in JUNCOS Costa Rica, 2017
- > Participation in JUDUCA Honduras, 2016
- > Participation in JUNCOS Costa Rica, 2015
- > National Federated Athlete

Team-Worker | Hard-Worker | Discipline | Passionate

PROJETS

MONITORING OF COFFEE PLANTATIONS THROUGH UAS PHOTOGRAMMETRY

2019

Implement a strategy of monitoring coffee plantations using ANN for detection

QGiS ILWIS GoogleColab Agisoft PhotoScan

TIVA CONTROLLER USING FREERTOS

github.com/slopezRedfox/gise-p1-tiva-Santy241996-master github.com/slopezRedfox/gise-p1-qt-Santy241996-master Implement a FreeRTOS controller to a Tiva™ C Series TM4C123G, in order to verify the board attributes

Code Composer QtCreator Eclipce

RTC CONTROL AND VGA

2016

github.com/MartinBR95/-Controlador-e-interfaz-en-FPGA-para-RTC-

Design and implementation of RTC control system with finite state machines using Digilent Nexys 3 Spartan 6 FPGA board and deployment of its information using VGA.

Modelsim | ISE Design Suite



66 RÉFÉRENCES

MsC. Mario Marín Sánchez

College Professor, ITCR

malbmarin@gmail.com

(+506) 8359 3973

Dr. Renato Rímolo Donadío

College Professor, ITCR rrimolo@tec.ac.cr

25509280

Lic. Lizbeth Sánchez Pérez

swimming Team Coach, ITCR

lisanchez@itcr.ac.cr

(+506) 8710 1835