

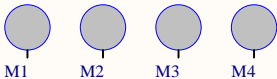
Rev	Description	Date	Author
1.0	Initial Release.	01/07/20	Yan C. de Azeredo

Revision History

Fiducials



Mechanical Holes



PCB Elements

Semi USB Interstage Interface Panels of FloripaSat-2 2U CubeSat

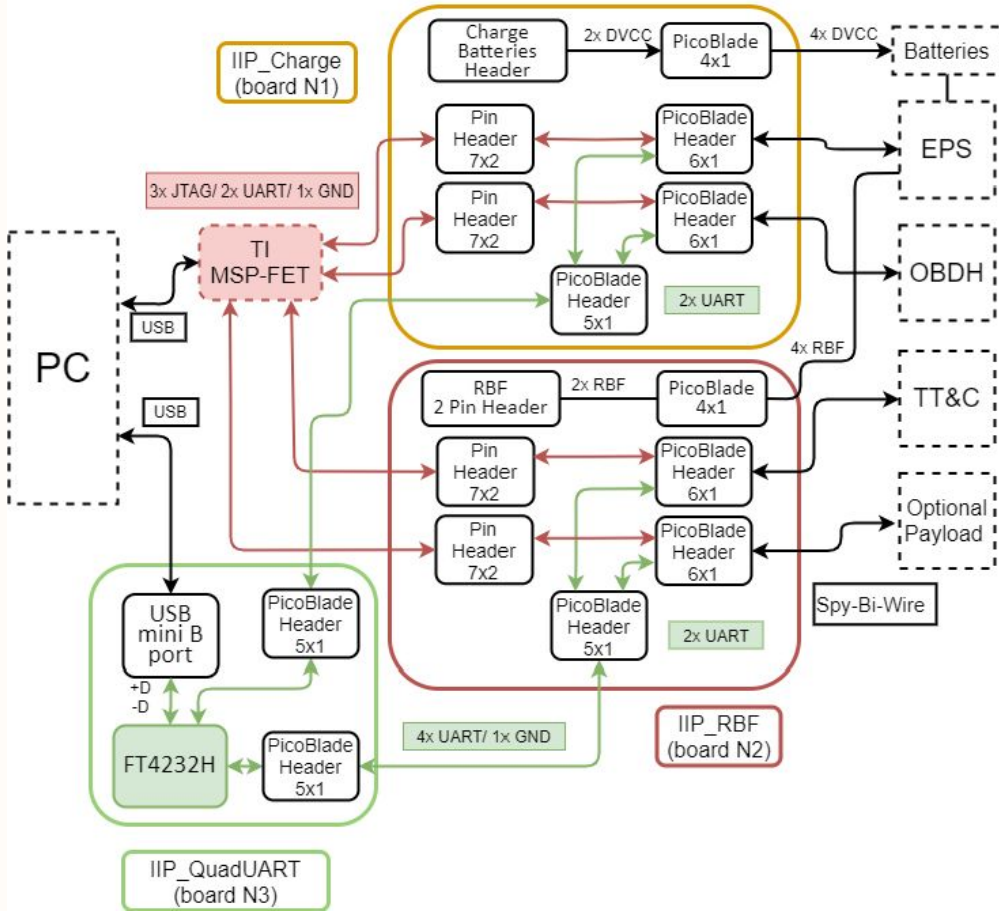
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- Drawn by: Yan Castro de Azeredo
- Reviewers: Gabriel M. Marcelino and André M. P. Mattos
- Support: Gabriel M. Marcelino, André M. P. Mattos and Kleber Gouveia
- Mechanical validation: Edemar Morsch Filho

Project Information

Semi USB Interstage Interface Panels



Full System Block Diagram

SpaceLab - Federal University of Santa Catarina			
Project: <i>1_iip_charge.PrjPCB</i> / [No Variations]			
Title: <i>IIP Hardware Architecture</i>			
Designed by: <i>Yan Castro de Azeredo</i>			Project Code: <i>IIP</i>
Date: <i>11/29/2020</i>	Revision: <i>1.0</i>	Sheet <i>1</i> of <i>2</i>	Size: <i>A4</i>

