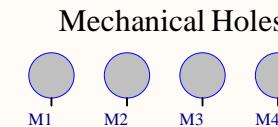
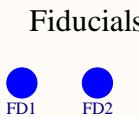


Rev	Description	Date	Author
1.0	Initial Release.	01/07/20	Yan C. de Azeredo

Revision History



PCB Elements

Semi USB Interstage Interface Panels

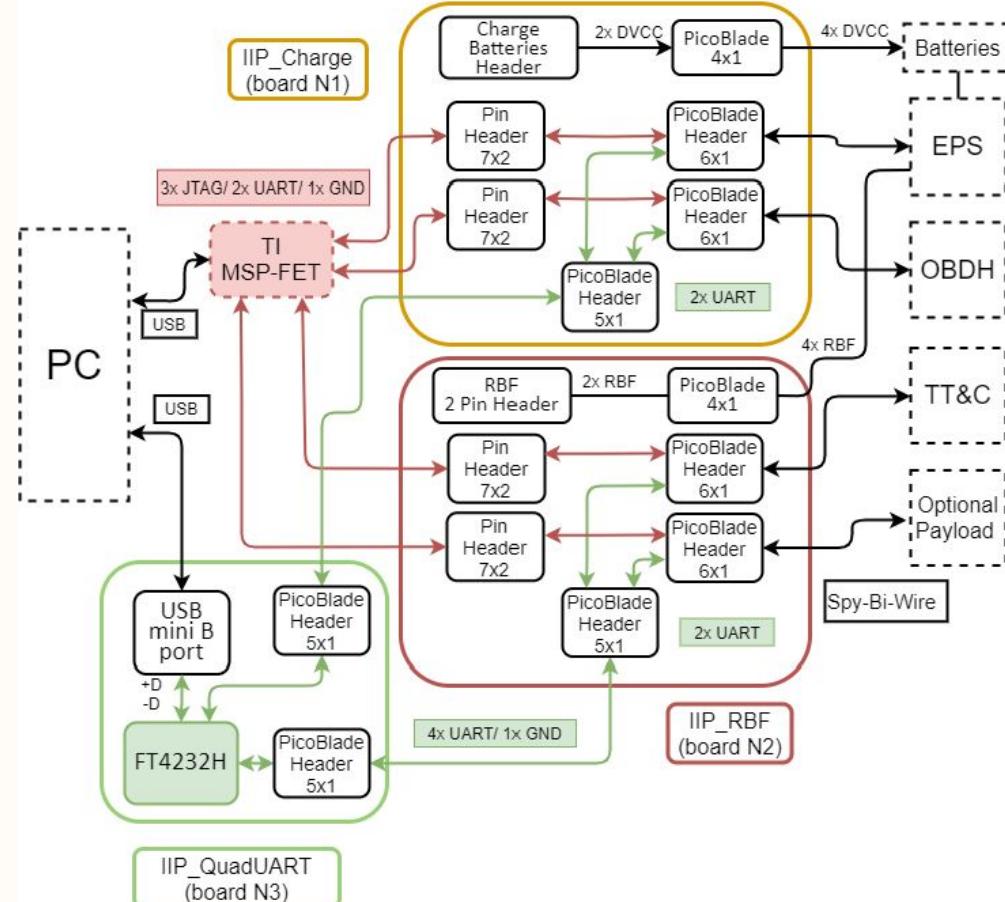
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- Drawn by: Yan Castro de Azeredo
- Reviewers: Gabriel M. Marcelino and André M. P. Mattos
- Support: Gabriel M. Marcelino, André M. P. Mattos and Kleber Gouveia
- Mechanical validation: Edemar Morsch Filho

Project Information

Semi USB Interstage Interface Panels



Full System Block Diagram

Title: 0_Architecture.SchDoc		
Size: A4	Project: 1_IIP_Charge.PrjPCB	Revision: 1.0
Date: 19/07/2020	Time: 00:18:37	Sheet 1 of 2
Drawn By: Yan Castro de Azeredo		Model: Engineering

UFSC - SpaceLab
University Campus - Trindade
Dep. of Electrical Engineering - CTC
Florianópolis, Santa Catarina, Brazil
CEP: 88040 - 900



A

B

C

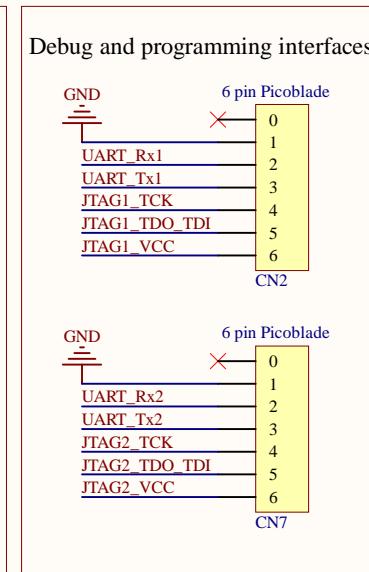
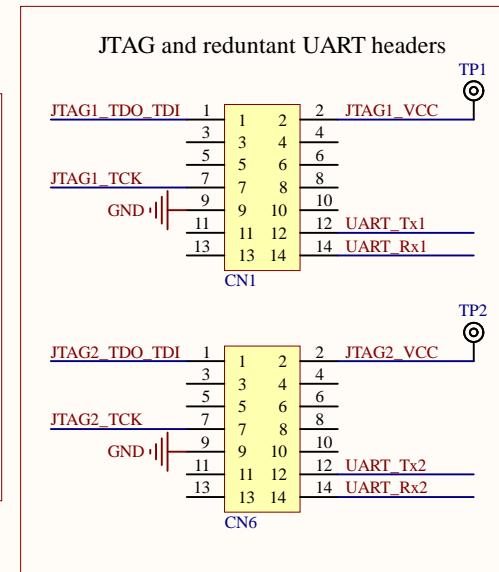
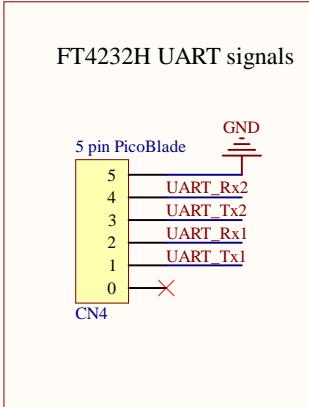
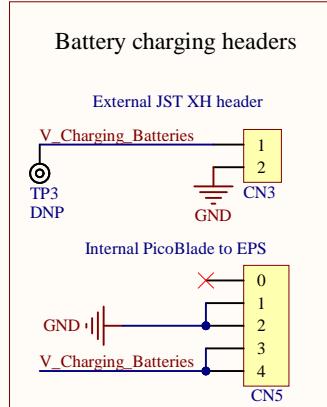
D

A

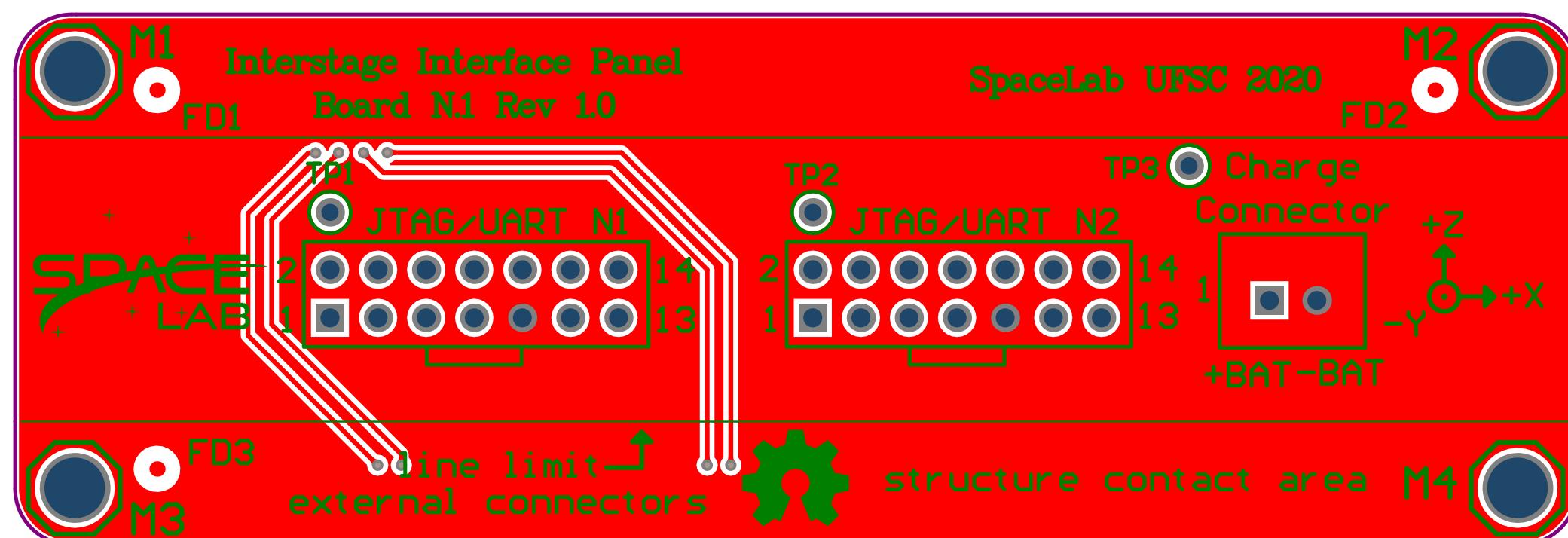
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C

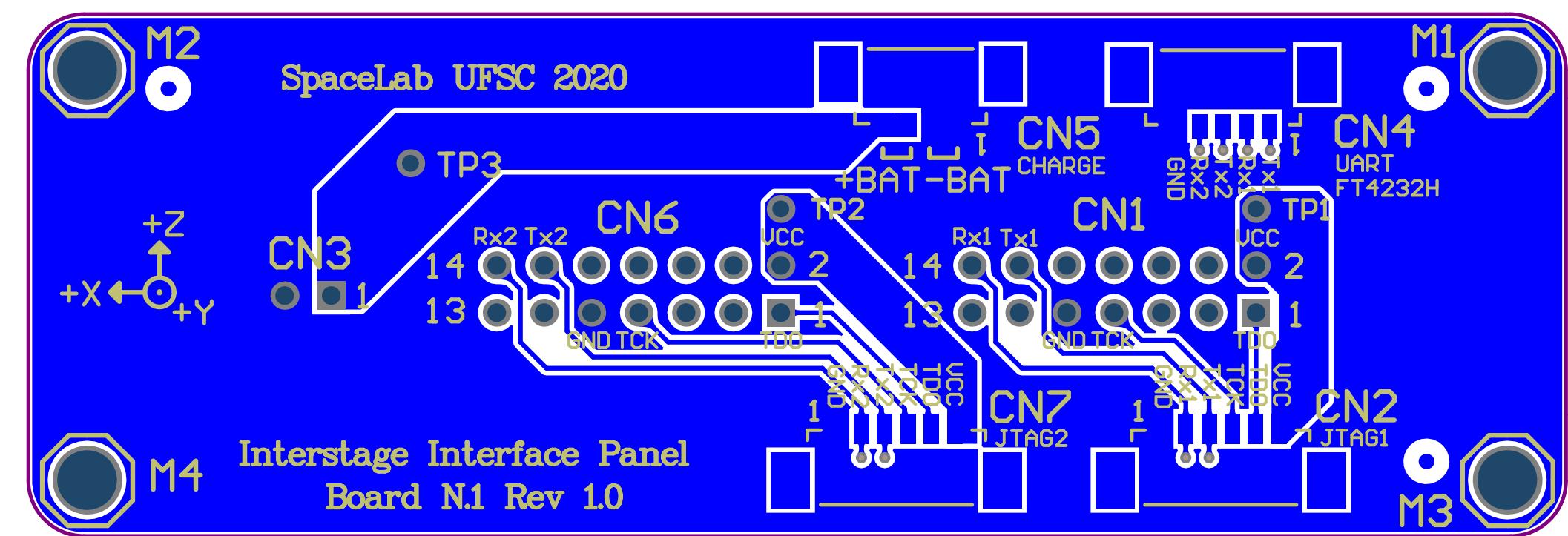
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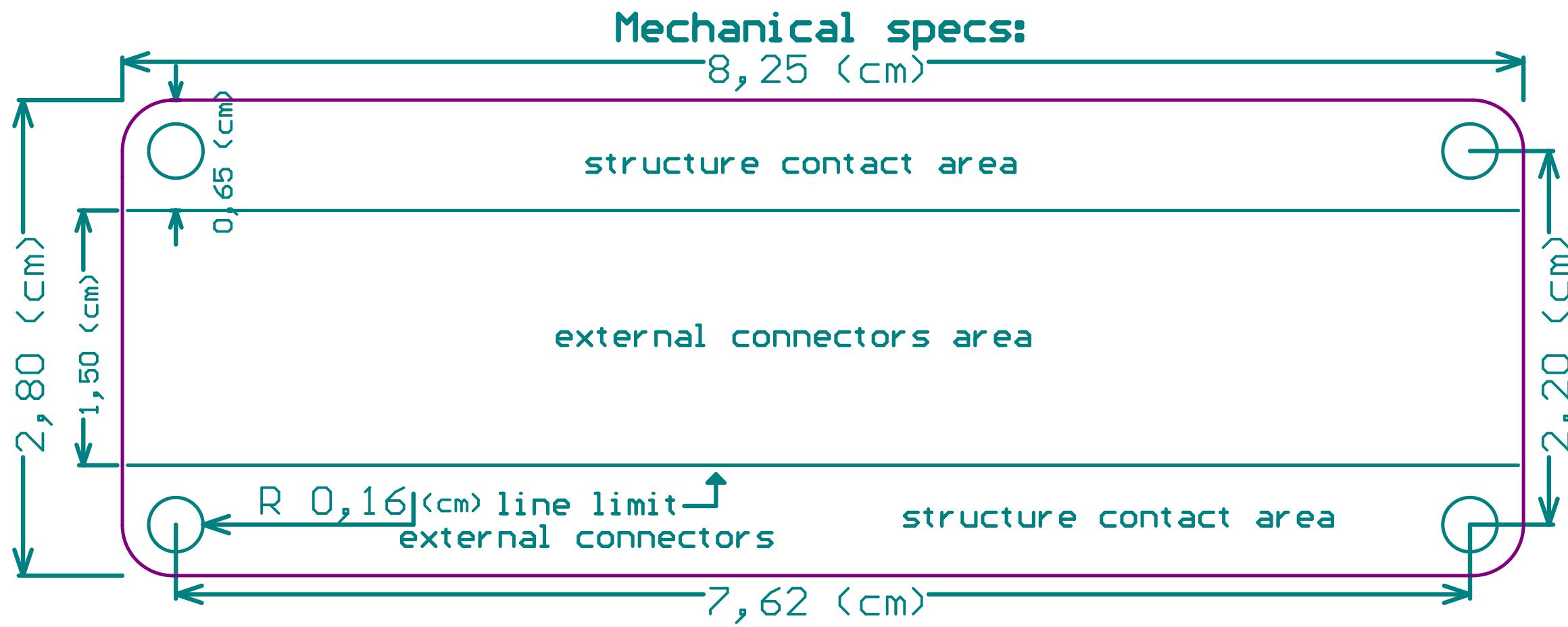


Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0,010mm	3,5	
1	Top Layer	Copper	0,035mm		
	Dielectric 1	FR-4	1,500mm	4,2	
2	Bottom Layer	Copper	0,035mm		
	Bottom Solder	Solder Resist	0,010mm	3,5	
	Bottom Overlay				



TITLE: IIP SEMI USB N.1 BOARD		REV: 1.0	DATE: 01/07/2020
MATERIAL: FR4		Silkscreen color: white	Project: IIP FloripaSat-2
Board Thickness: 1.6mm		Layers: 02	Space Technology Research Laboratory Federal University of Santa Catarina
PCB Surface: HASL		Drawing: Yan C. de Azeredo	SpaceLab UFSC





CN1
+

CN6
+

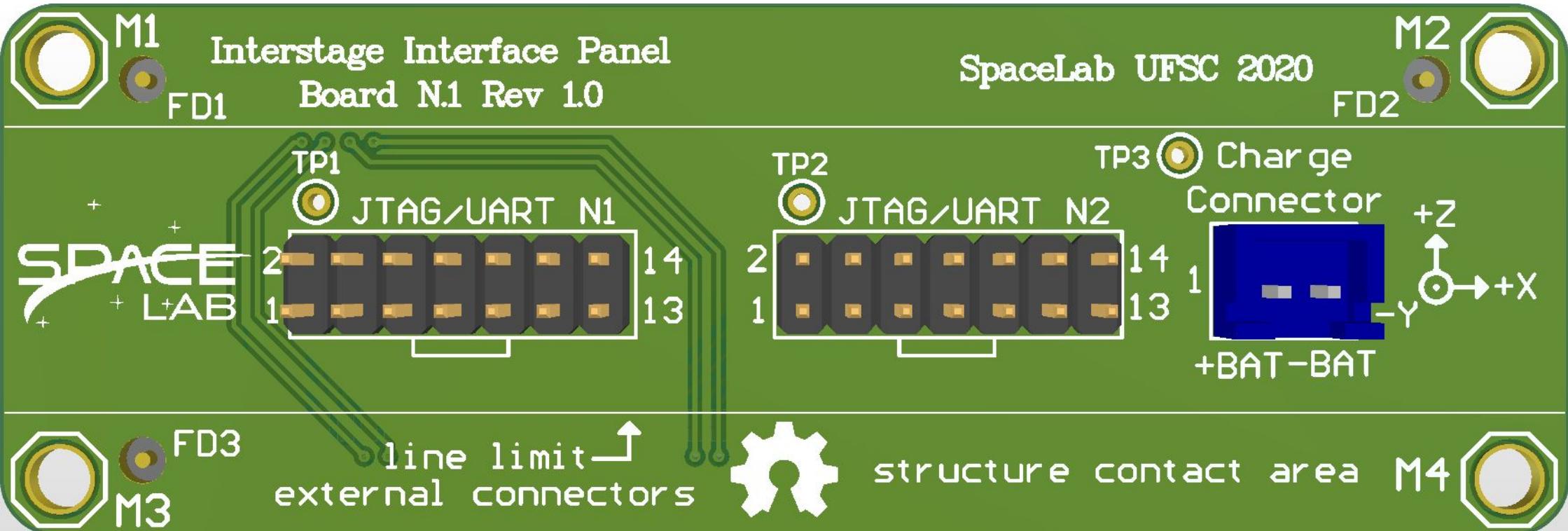
CN3
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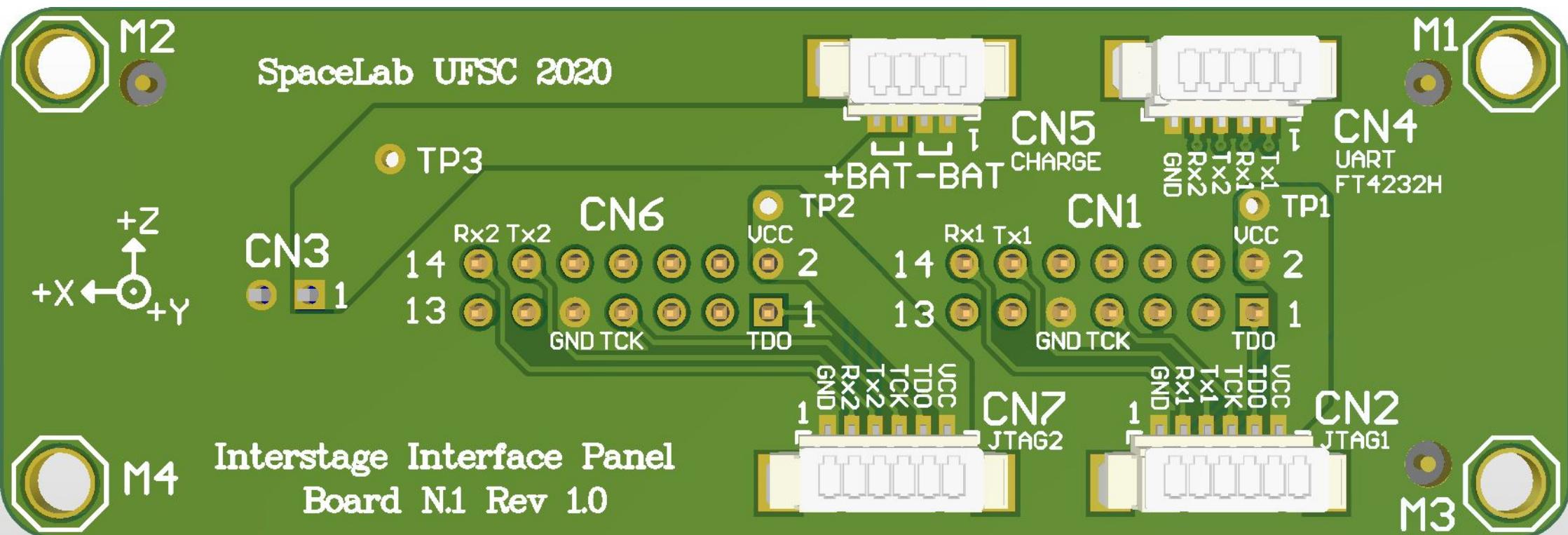
CN5
+

CN4
+

CN7
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CN2
+







Quotation

Source Data From: 1_IIP_Charge.BomDoc

Project: 1_IIP_Charge.Prj

Variant: None

Project Code: <Parameter ProductCode not found>

1

Report Date: 19/07/2020

Date: 05/01/1900 07:45

Print Date: 19/07/2020

Dolar: R\$ 5,3260