

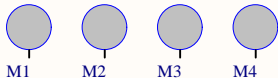
Rev	Description	Date	Author
1.0	Initial Release.		Yan C. de Azeredo

Revision History

PCB

Fidutials

Mechanical Holes



PCB Elements

Semi USB Interstage Interface of FloripaSat-2 2U CubeSat

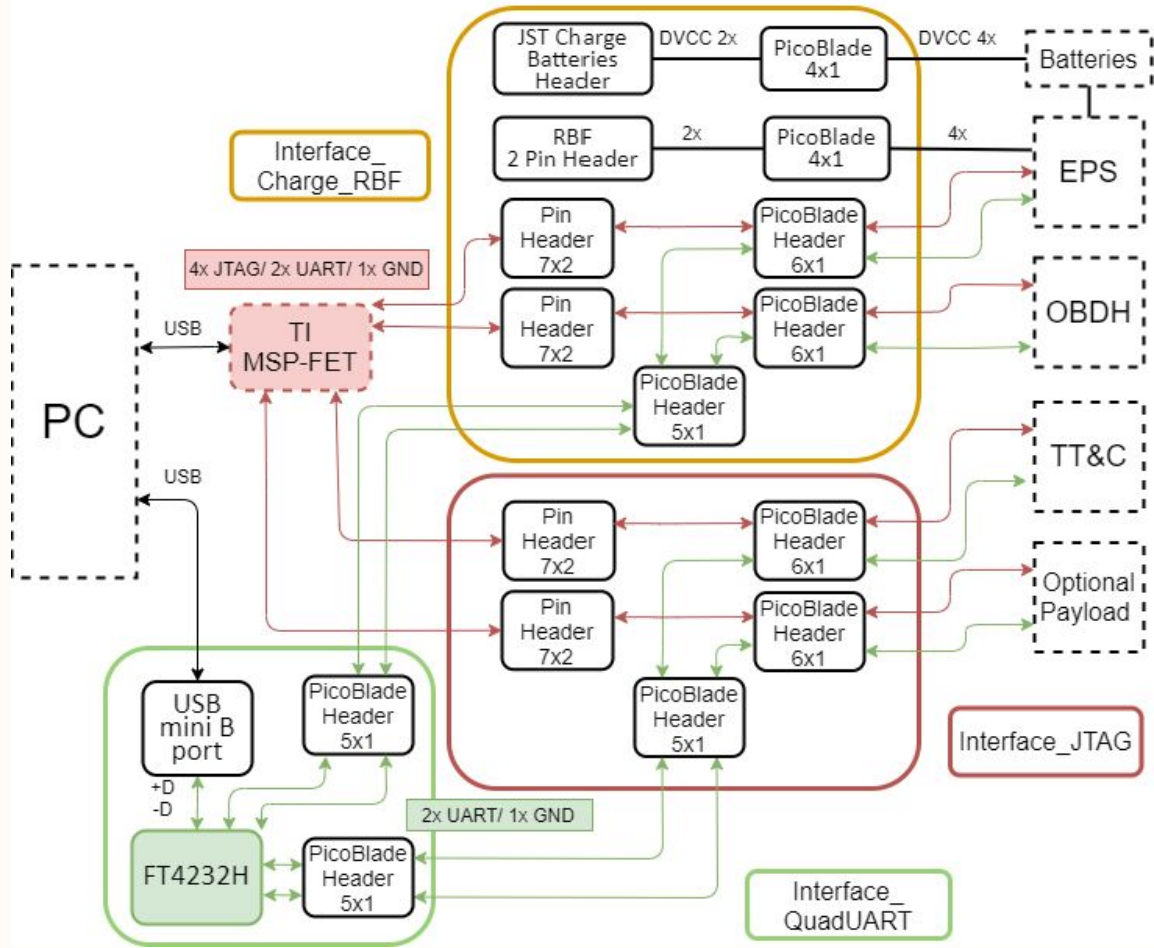
Copyright © 2020
by Universidade Federal de Santa Catarina.

This work is licensed under the Creative Commons Attribution-ShareAlike 4.0 International License. To view a copy of this license, visit <http://creativecommons.org/licenses/by-sa/4.0/>.

- Drawn by: Yan Castro de Azeredo.
- Reviewers:
- Support:

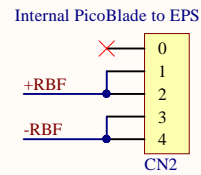
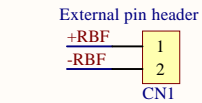
Project Information

Semi USB Interstage Interface Panels

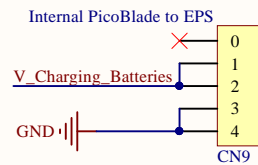
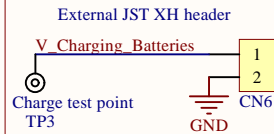


Full system block diagram

Remove Before Flight (RBF)

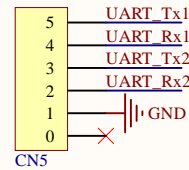


Battery charging headers

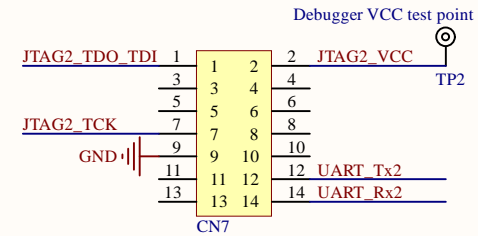
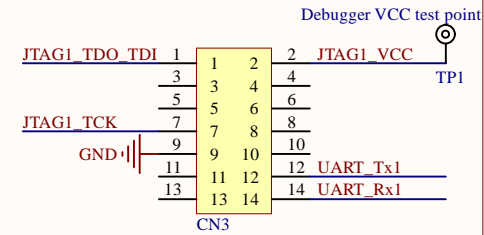


FT4232H UART signals

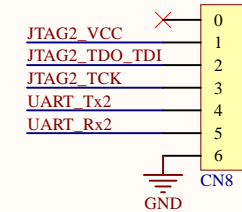
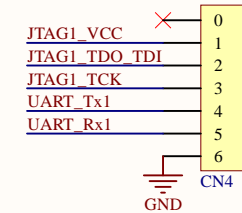
Internal PicoBlade

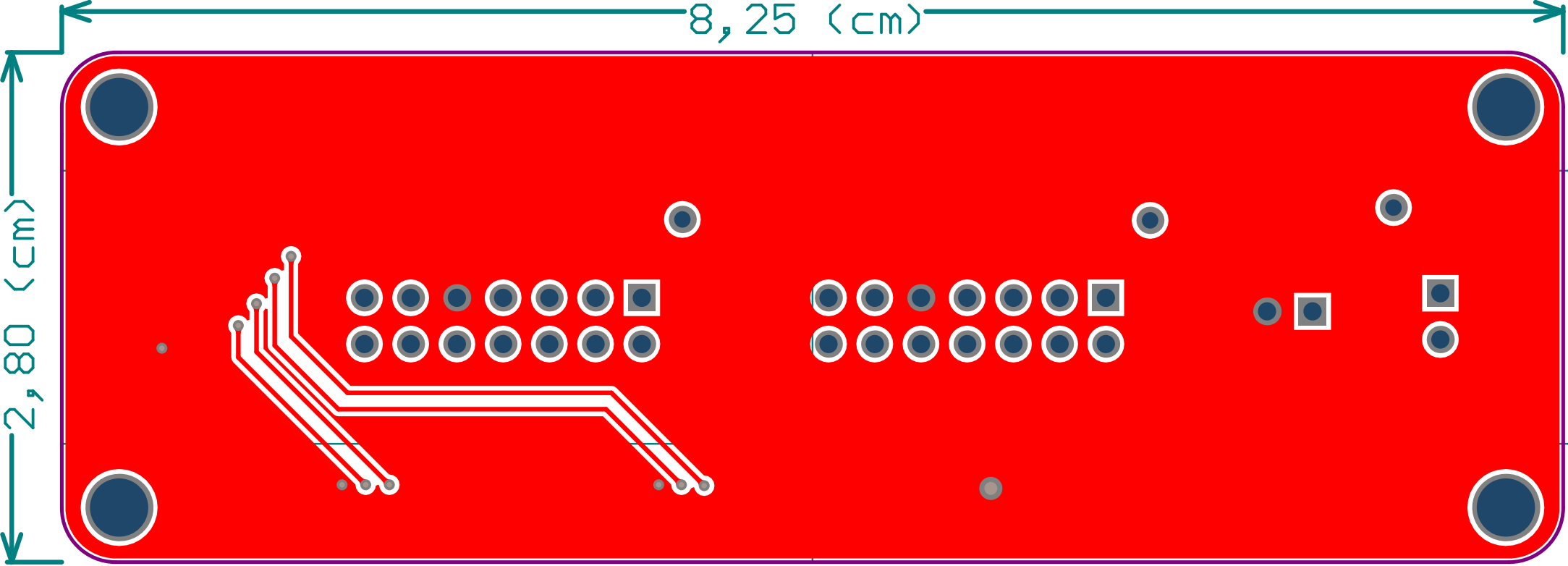


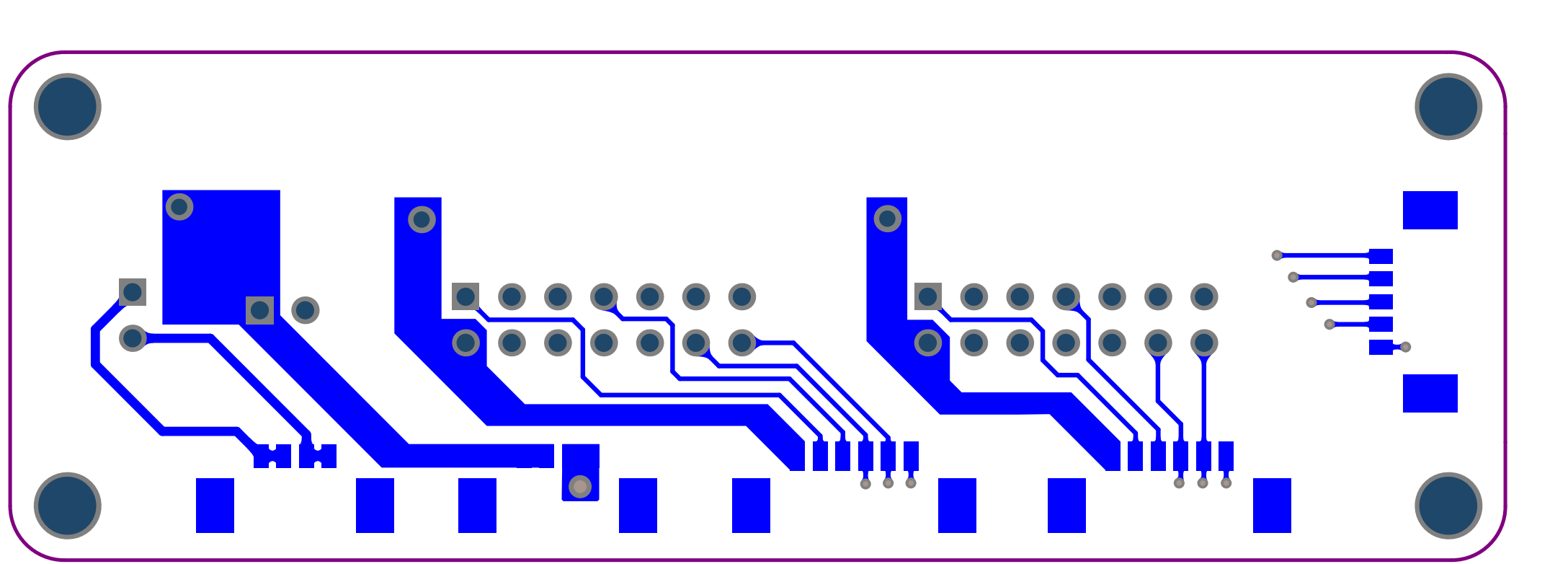
JTAG and redundant UART headers



Debug and programming interfaces







M1 Interstage Interface Panel
Board N.1 Rev 1.0



SpaceLab UFSC 2020

M2



JTAG/UART N1

TP2

JTAG/UART N2

TP1

Charge Connector
TP3 +RBF



-BAT +BAT -RBF

GND +Z
-Y +X

M3

GND

GND



GND

M4

