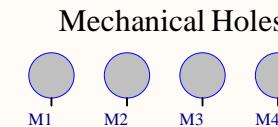
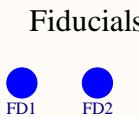


Rev	Description	Date	Author
1.0	Initial Release.	01/07/20	Yan C. de Azeredo

Revision History



PCB Elements

Semi USB Interstage Interface Panels of FloripaSat-2 2U CubeSat

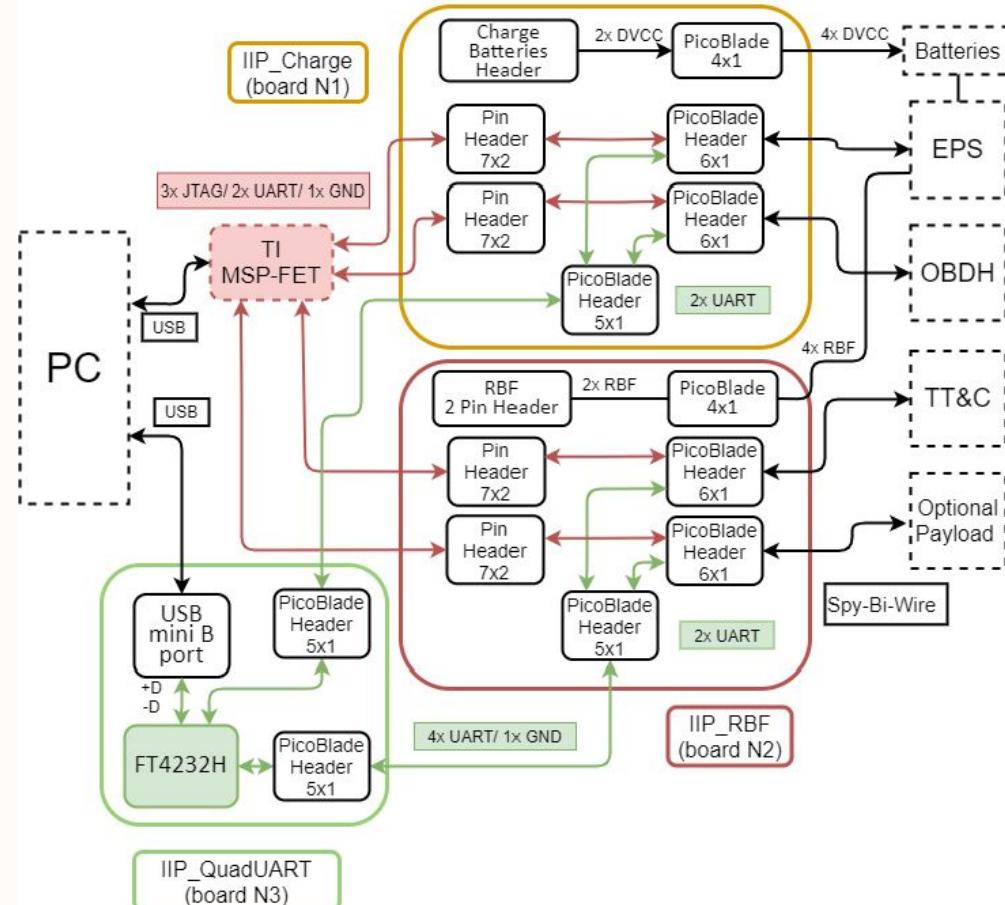
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Project Information

Semi USB Interstage Interface Panels



Full System Block Diagram

SpaceLab - Federal University of Santa Catarina	+	+
Project: I_ip_charge.PnjPCB / [No Variations]		
Title: IIP Hardware Architecture		
Designed by: Yan Castro de Azeredo		Project Code: IIP
Date: 11/29/2020	Revision: 1.0	Sheet 1 of 2
		Size: A4

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