

Interstage Interface Panel N°3 "Quad UART" Hardware:


- Designed by: Yan C. de Azeredo.
- Reviewers: Gabriel M. Marcelino and Andre M. P. Mattos.
- Support: Edemar M. Filho.


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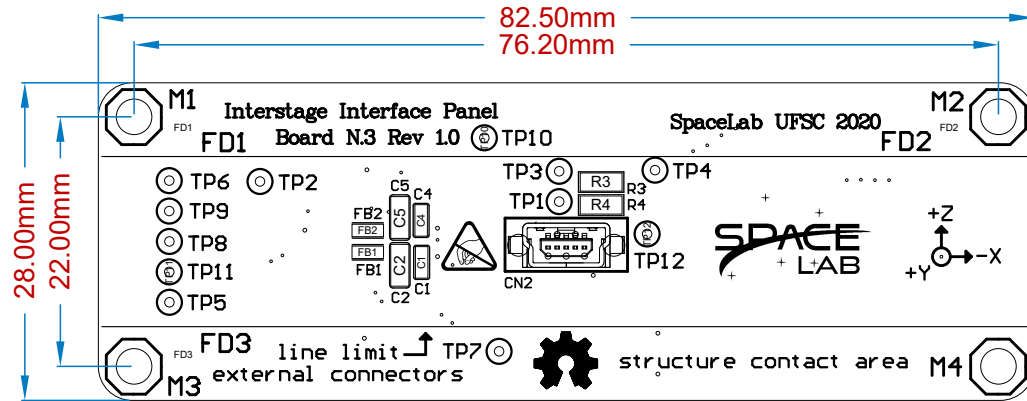
Github repository: <https://github.com/spacelab-ufsc/interface-board>

More info about SpaceLab: <https://spacelab.ufsc.br/>

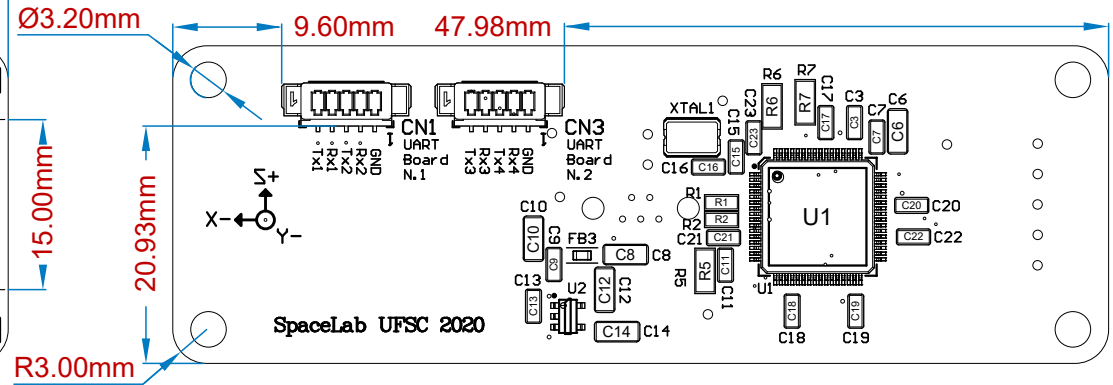
SpaceLab - Federal University of Santa Catarina			
Project: Interstage Interface Panel N°3			
Title: Project info and board isometric views			
Designed by: Yan Castro de Azeredo			Project code: IIPN3
Date: 11/29/2020	Version: 1.0	Sheet 1 of 3	Sheet size: A4

Displayed components within a red mesh box  are not supposed to be soldered in the flight model of the board.

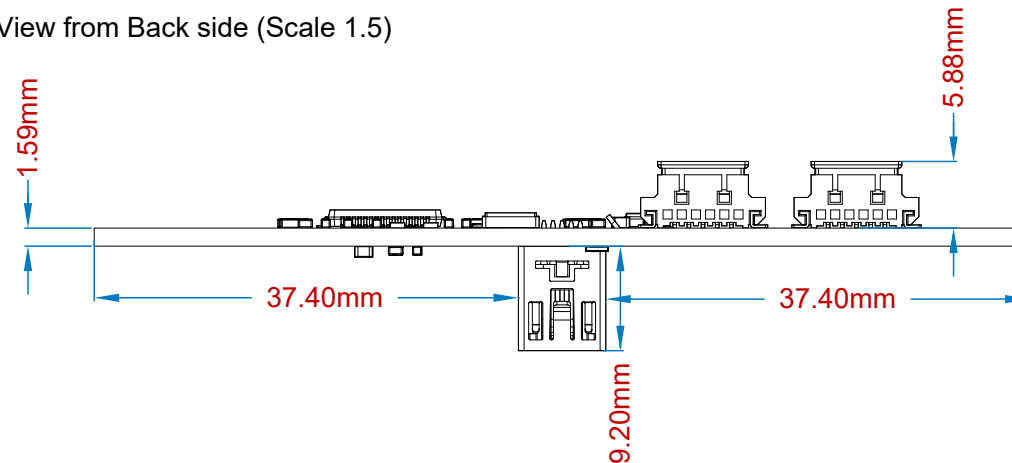
View from Top side (Scale 1.5)




View from Bottom side (Scale 1.5)




View from Back side (Scale 1.5)



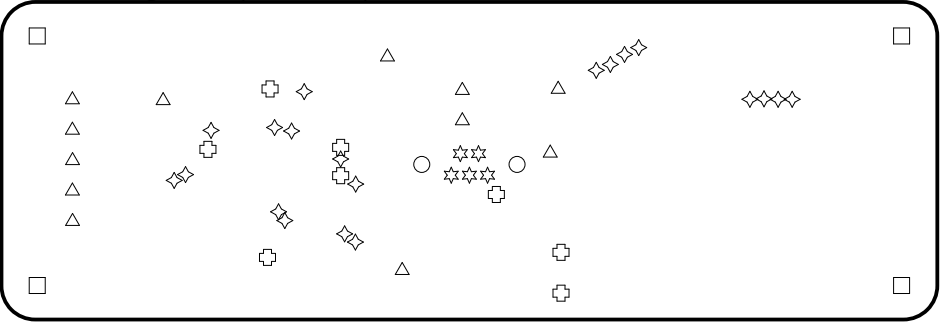
SpaceLab - Federal University of Santa Catarina		
Project: Interstage Interface Panels N°3		
Title: Assembly components and mechanical dimensions		
Designed by: Yan Castro de Azeredo		
Date: 11/29/2020	Version: 1.0	Sheet 2 of 3

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Layer Stack Legend

	Material	Layer	Thickness	Dielectric Material	Type	Gerber
		Top Overlay			Legend	GTO
	Surface Material	Top Solder	0.01mm	Solder Resist	Solder Mask	GTS
	Copper	Top Layer	0.04mm		Signal	GTL
			1.50mm	FR-4	Dielectric	
	Copper	Bottom Layer	0.04mm		Signal	GBL
	Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
		Bottom Overlay			Legend	GBO
Total thickness: 1.59mm						

Drill Drawing View (Scale 1.5)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	20	0.30mm	Plated	
⊕	8	0.40mm	Plated	
☆	5	0.60mm	Plated	
△	12	0.90mm	Plated	
○	2	2.00mm	Plated	
□	4	3.20mm	Plated	
	51 Total			

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Project: Interstage Interface Panel N°3

Title: Layer stack and drill tables

Designed by: Yan Castro de Azeredo

Date: 11/29/2020

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Project code: IIPN3

Sheet size: A4