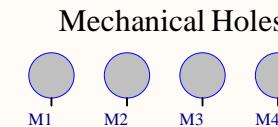
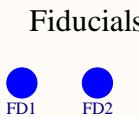


Rev	Description	Date	Author
1.0	Initial Release.	01/07/20	Yan C. de Azeredo

Revision History



PCB Elements

Semi USB Interstage Interface Panels of FloripaSat-2 2U CubeSat

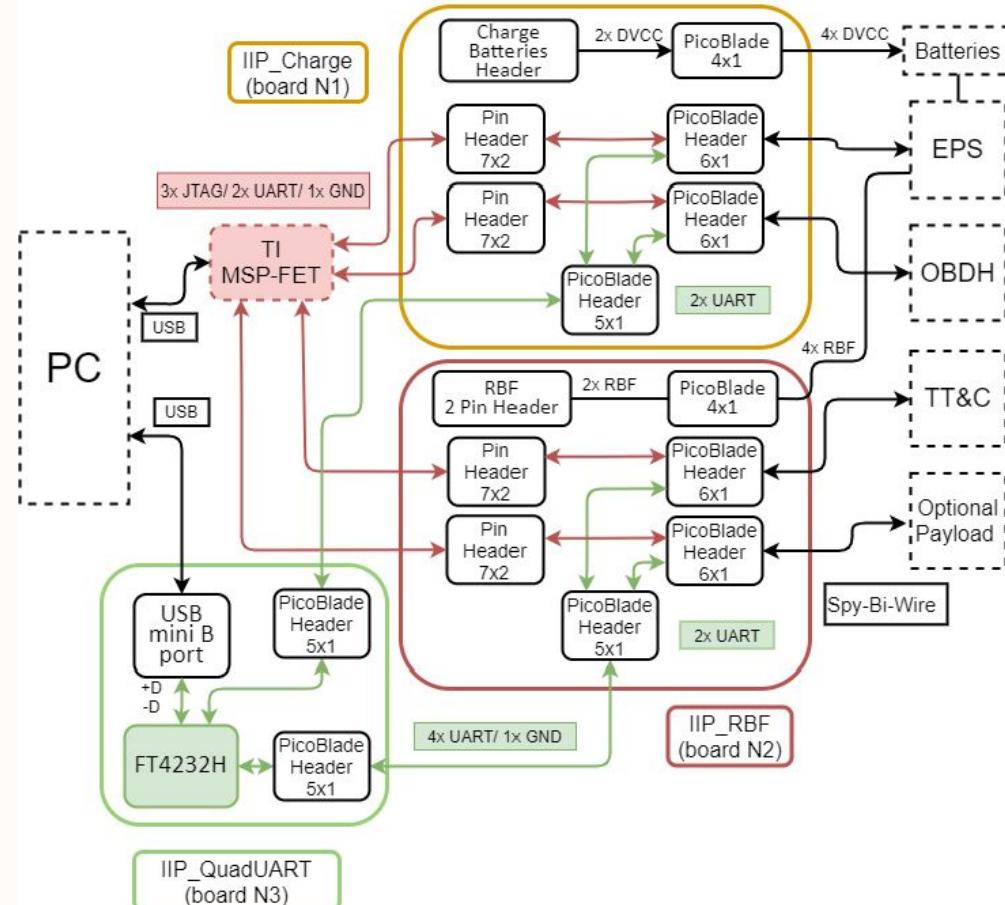
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- Drawn by: Yan Castro de Azeredo
- Reviewers: Gabriel M. Marcelino and André M. P. Mattos
- Support: Gabriel M. Marcelino, André M. P. Mattos and Kleber Gouveia
- Mechanical validation: Edemar Morsch Filho

Project Information

Semi USB Interstage Interface Panels



Full System Block Diagram

SpaceLab - Federal University of Santa Catarina	+	+
Project: I_ip_charge.PkjPCB / [No Variations]		
Title: IIP Hardware Architecture		
Designed by: Yan Castro de Azeredo		Project Code: IIP
Date: 11/29/2020	Revision: 1.0	Sheet 1 of 2
		Size: A4

A

A

B

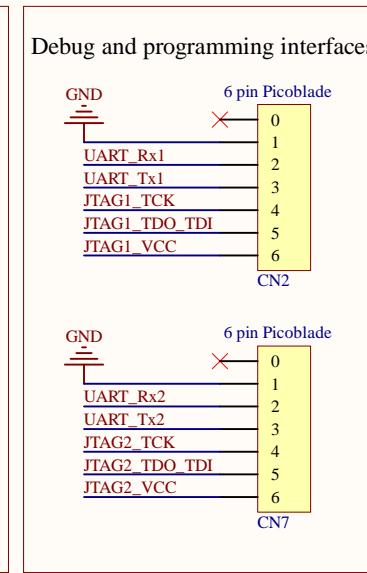
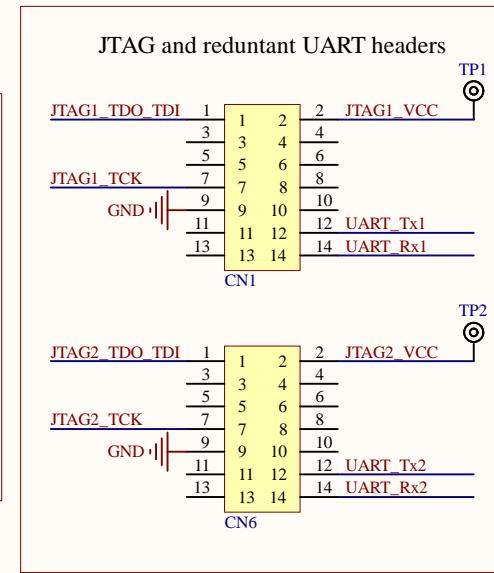
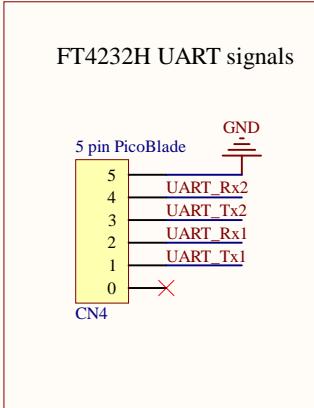
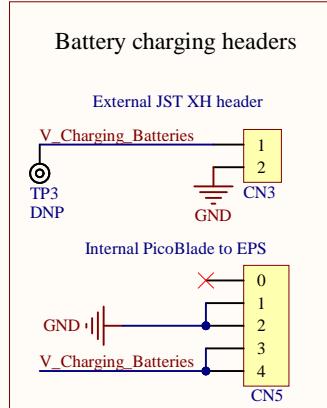
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C

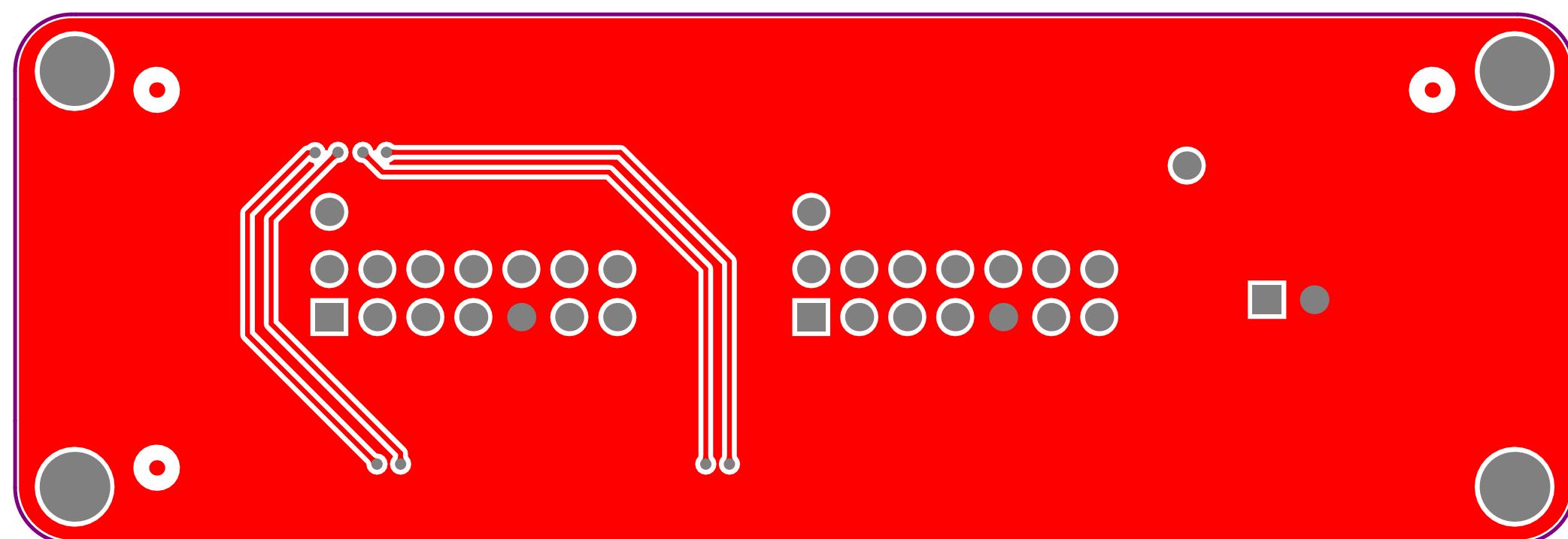
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D

D

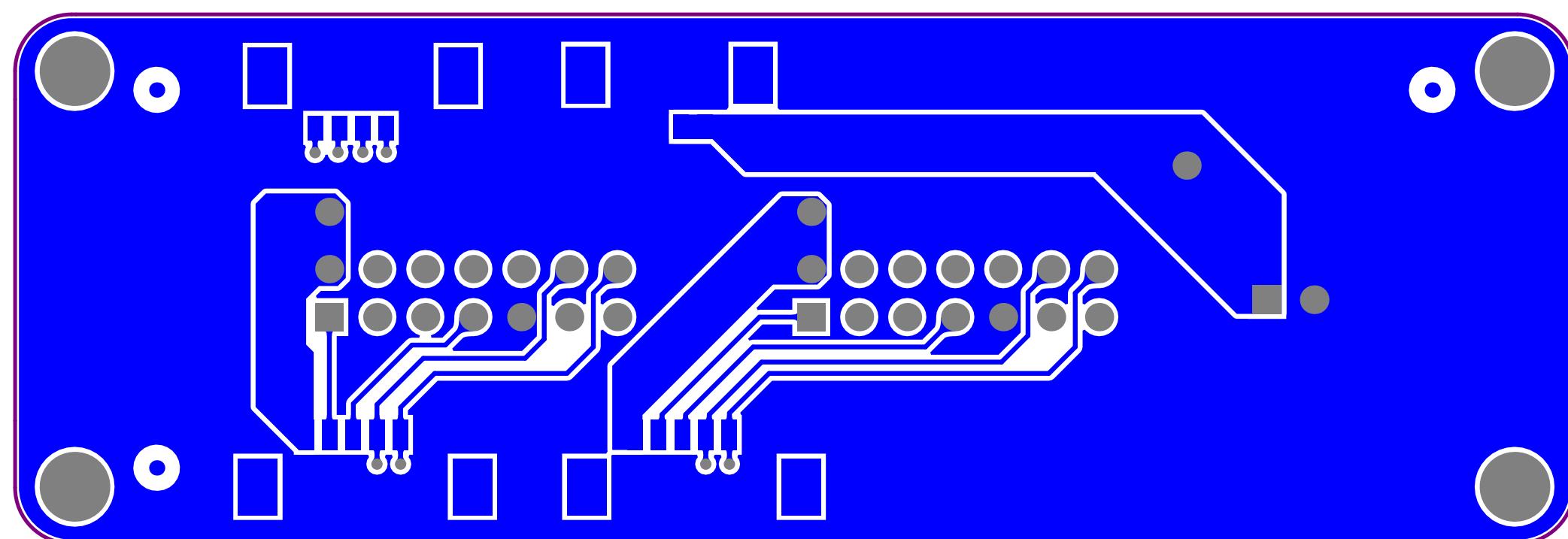


Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.010mm	3.5	
1	Top Layer	Copper	0.035mm		
	Dielectric 1	FR-4	1.500mm	4.2	
2	Bottom Layer	Copper	0.035mm		
	Bottom Solder	Solder Resist	0.010mm	3.5	
	Bottom Overlay				



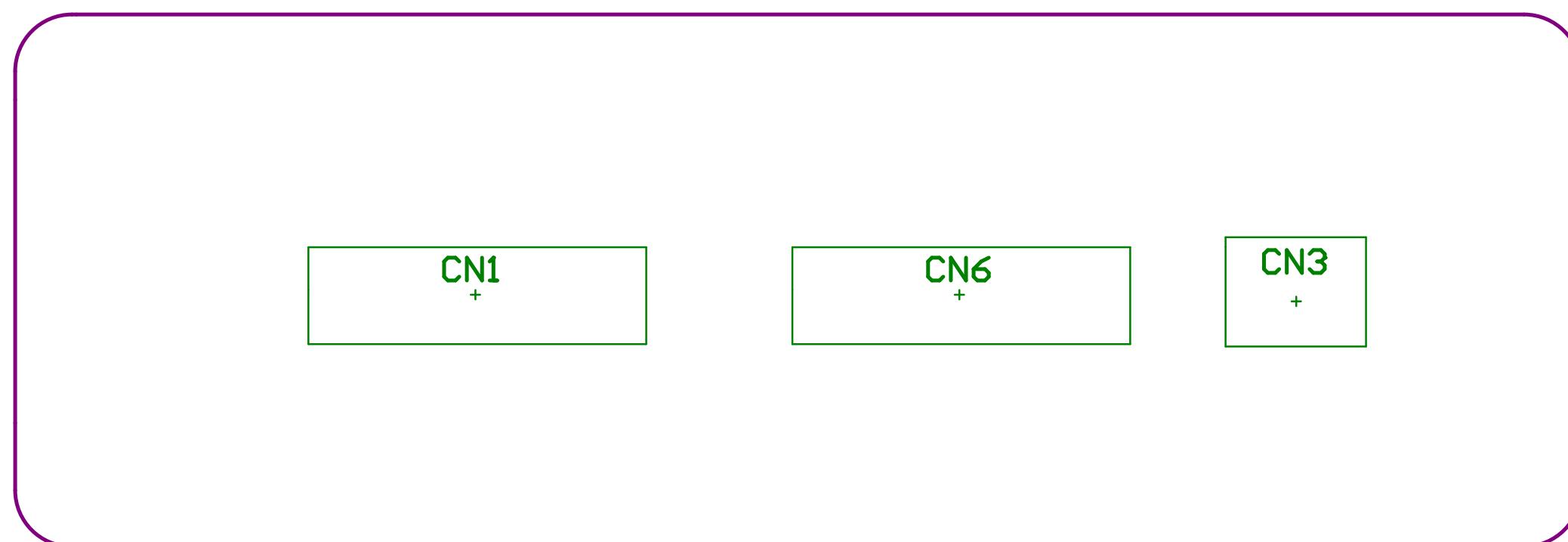
TITLE: IIP N.1 BOARD CHARGE		REV: 1.0	DATE: 01/07/2020
MATERIAL: FR4		Silkscreen color: white	Project: IIP
Board Thickness: 1.6mm		Layers: 02	Space Technology Research Laboratory Federal University of Santa Catarina
PCB Surface: HASL		Drawing: Yan C. de Azeredo	SpaceLab UFSC

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.010mm	3.5	
1	Top Layer	Copper	0.035mm		
	Dielectric 1	FR-4	1.500mm	4.2	
2	Bottom Layer	Copper	0.035mm		
	Bottom Solder	Solder Resist	0.010mm	3.5	
	Bottom Overlay				



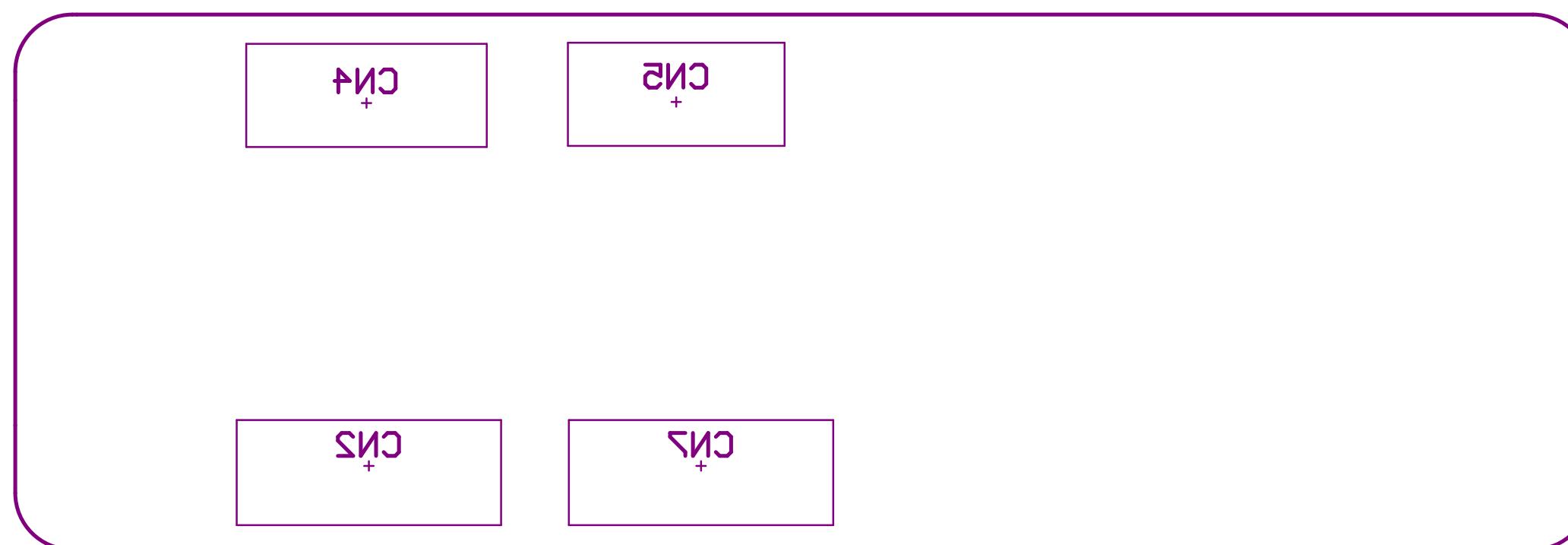
TITLE: IIP N.1 BOARD CHARGE		REV: 1.0	DATE: 01/07/2020
MATERIAL: FR4		Silkscreen color: white	Project: IIP
Board Thickness: 1.6mm		Layers: 02	Space Technology Research Laboratory Federal University of Santa Catarina SpaceLab UFSC
PCB Surface: HASL		Drawing: Yan C. de Azeredo	

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.010mm	3.5	
1	Top Layer	Copper	0.035mm		
	Dielectric 1	FR-4	1.500mm	4.2	
2	Bottom Layer	Copper	0.035mm		
	Bottom Solder	Solder Resist	0.010mm	3.5	
	Bottom Overlay				



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PCB Surface: HASL		Drawing: Yan C. de Azeredo	SpaceLab UFSC

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.010mm	3.5	
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TITLE: IIP N.1 BOARD CHARGE		REV: 1.0	DATE: 01/07/2020
MATERIAL: FR4		Silkscreen color: white	Project: IIP
Board Thickness: 1.6mm		Layers: 02	Space Technology Research Laboratory Federal University of Santa Catarina SpaceLab UFSC
PCB Surface: HASL		Drawing: Yan C. de Azeredo	



Bill of Materials

Source Data From: 1_iip_charge.PrtPCB

Project: 1_iip_charge.PrtPCB

Variant: None

Project Code: IIPN1

Report Date: 11/29/2020

10:58 PM

Print Date: 29/11/2020

22:58:42

#	Designator	Quantity	Manufacturer	Manufacturer Part Number	#Column Name Error:' Partnumber	Description	#Column Name Error:' P	Footprint	Mount	Fitted
1	CN2, CN7	2	Molex	53398-0671		1.25mm Pitch PicoBlade™ Header, Surface Mount, Vertical, 6 Circuits		PICO BLADE 0533980671	Surface Mount	Fitted
2	CN1, CN6	2	Harwin	M20-9980745		Headers & Wire Housings 07+07 DIL VERTICAL PIN HEADER GOLD HT		CONN HEADER VERT 14POS 2.54MM	Through Hole	Fitted
3	CN5	1	Molex	53398-0471		Connector Header Surface Mount 4 position 0.049" (1.25mm)		PICO BLADE 0533980471	Surface Mount	Fitted
4	CN4	1	Molex	53398-0571		Wire-To-Board Connector, Vertical, PicoBlade 53398 Series, Surface Mount, Header, 5, 1.25 mm		PICO BLADE 0533980571	Surface Mount	Fitted
5	CN3	1	JST	B2B-XH-A-M(LF)(SN)		CONN HEADER TOP 2POS 2.5MM GRN		B2B-XH-A-M(LF)(SN)	Through Hole	Fitted