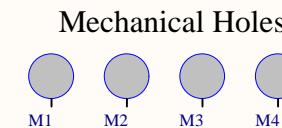
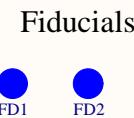


Rev	Description	Date	Author
1.0	Initial Release.	01/07/20	Yan C. de Azeredo

Revision History



PCB Elements

Semi USB Interstage Interface Panels

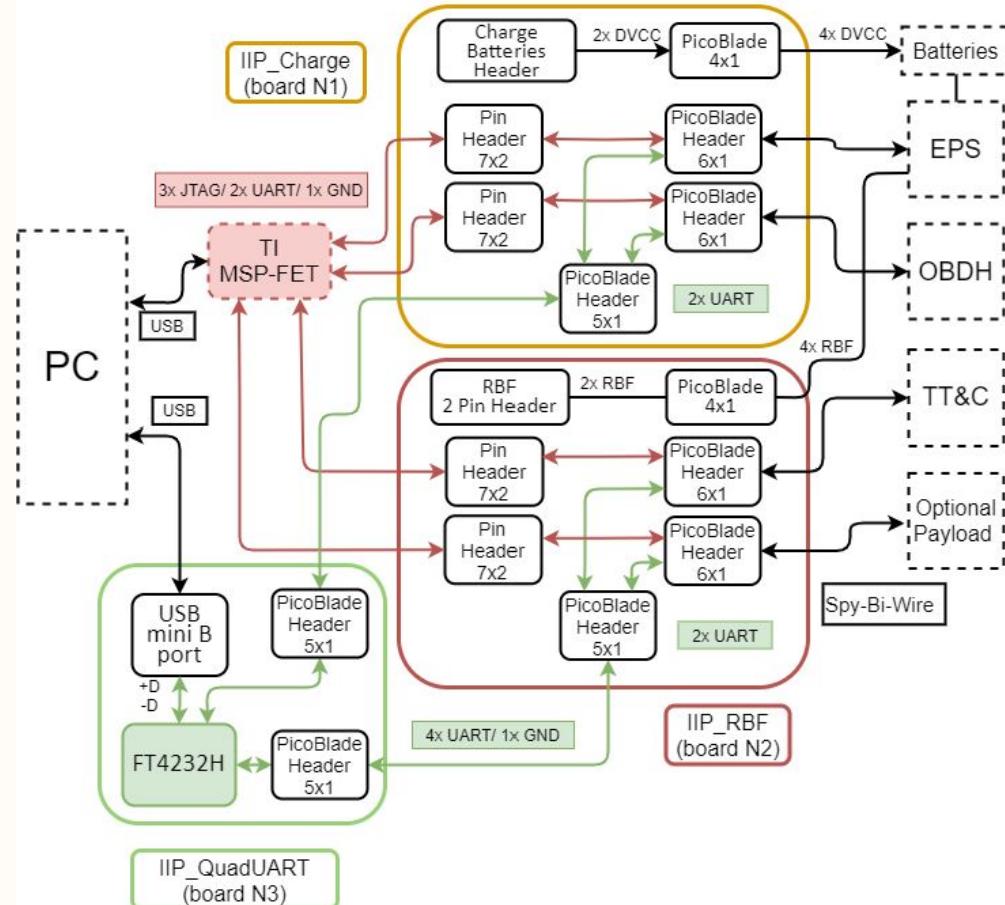
Copyright © 2020
by Universidade Federal de Santa Catarina.

This work is licensed under the Creative Commons Attribution-ShareAlike 4.0 International License. To view a copy of this license, visit <http://creativecommons.org/licenses/by-sa/4.0/>.

- Drawn by: Yan Castro de Azeredo
- Reviewers: Gabriel M. Marcelino and André M. P. Mattos
- Support: Gabriel M. Marcelino and André M. P. Mattos

Project Information

Semi USB Interstage Interface Panels



Full System Block Diagram

Title: 0_Architecture.SchDoc		
Size: A4	Project: 2_IIP_RBF.PnjPCB	Revision: 1.0
Date: 03/07/2020	Time: 11:17:15	Sheet 1 of 2
Drawn By: Yan Castro de Azeredo		Model: Engineering

UFSC - SpaceLab
University Campus - Trindade
Dep. of Electrical Engineering - CTC
Florianópolis, Santa Catarina, Brazil
CEP: 88040 - 900



A

B

C

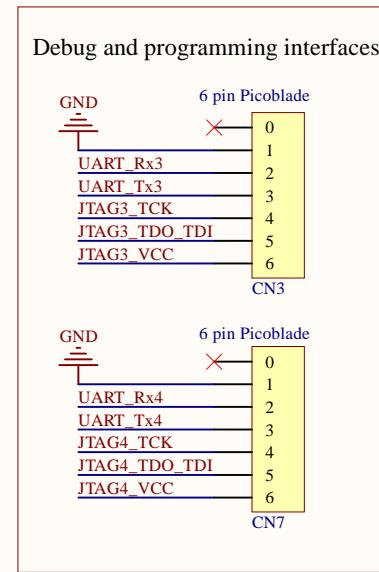
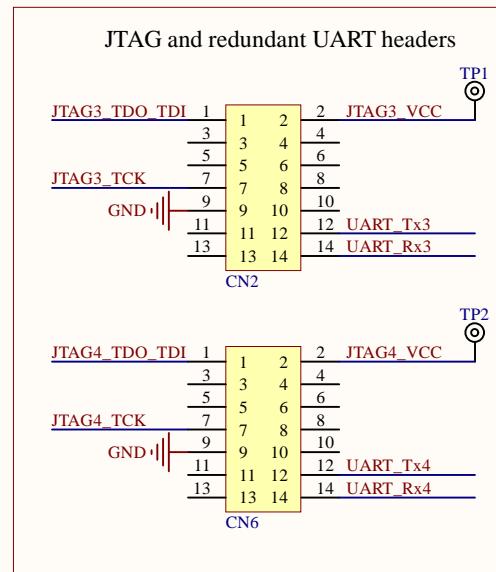
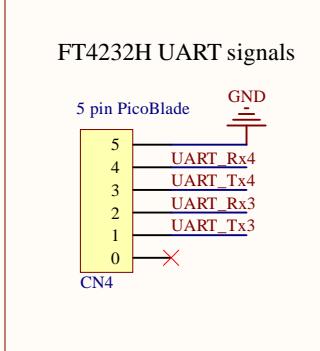
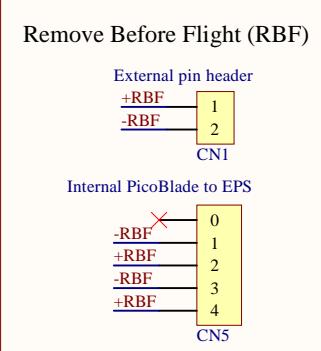
D

A

B

C

D

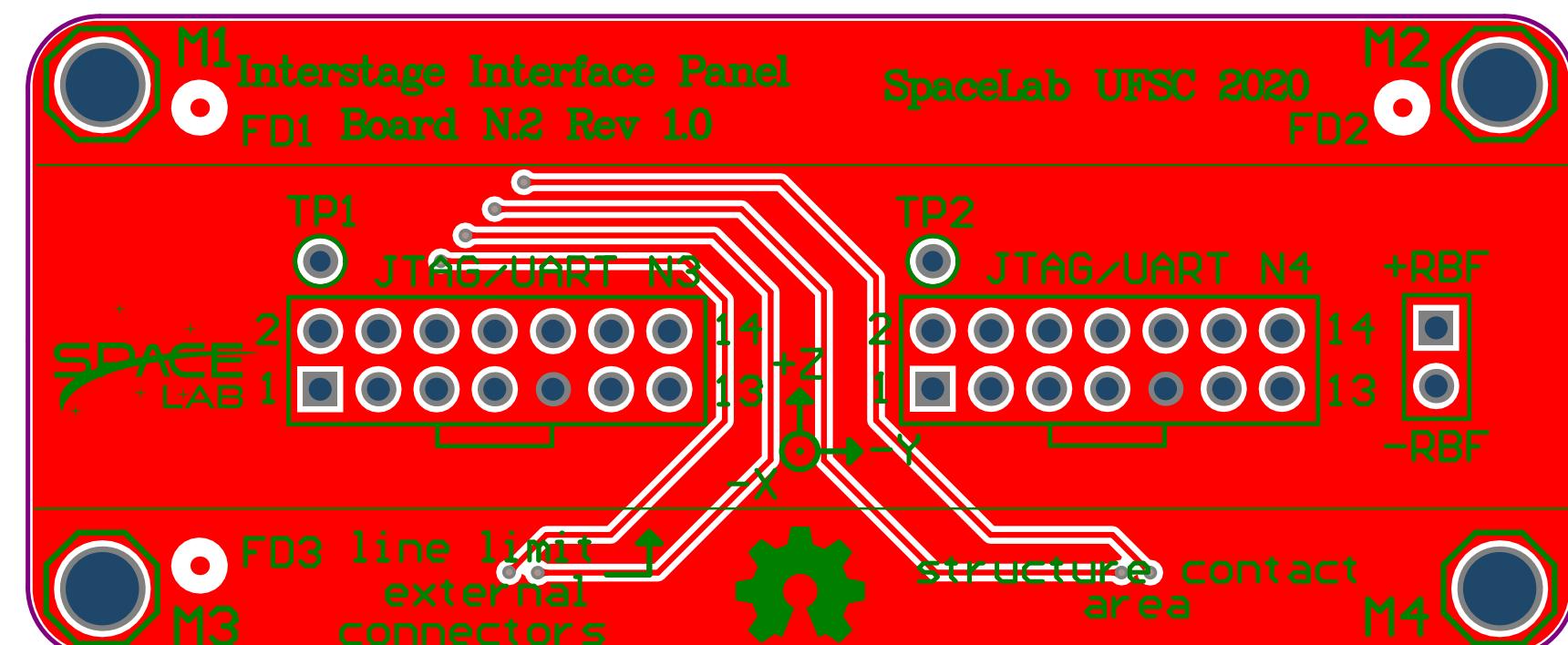


Title: 2_IIP_RBF.SchDoc		
Size: A4	Project: 2_IIP_RBF.PnjPCB	Revision: 1.0
Date: 03/07/2020	Time: 11:17:15	Sheet 2 of 2
Drawn By: Yan Castro de Azeredo		Model: Engineering

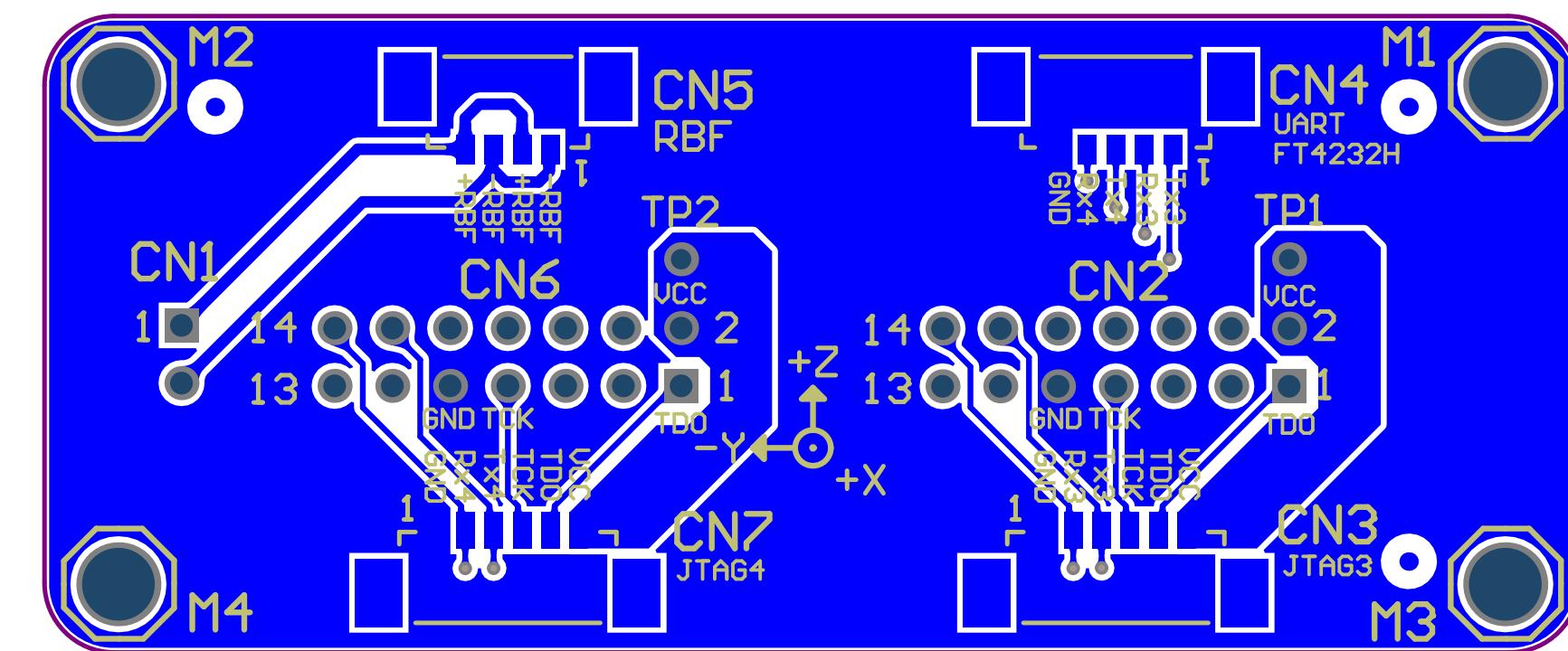
UFSC - SpaceLab
University Campus - Trindade
Dep. of Electrical Engineering - CTC
Florianópolis, Santa Catarina, Brazil
CEP: 88040 - 900

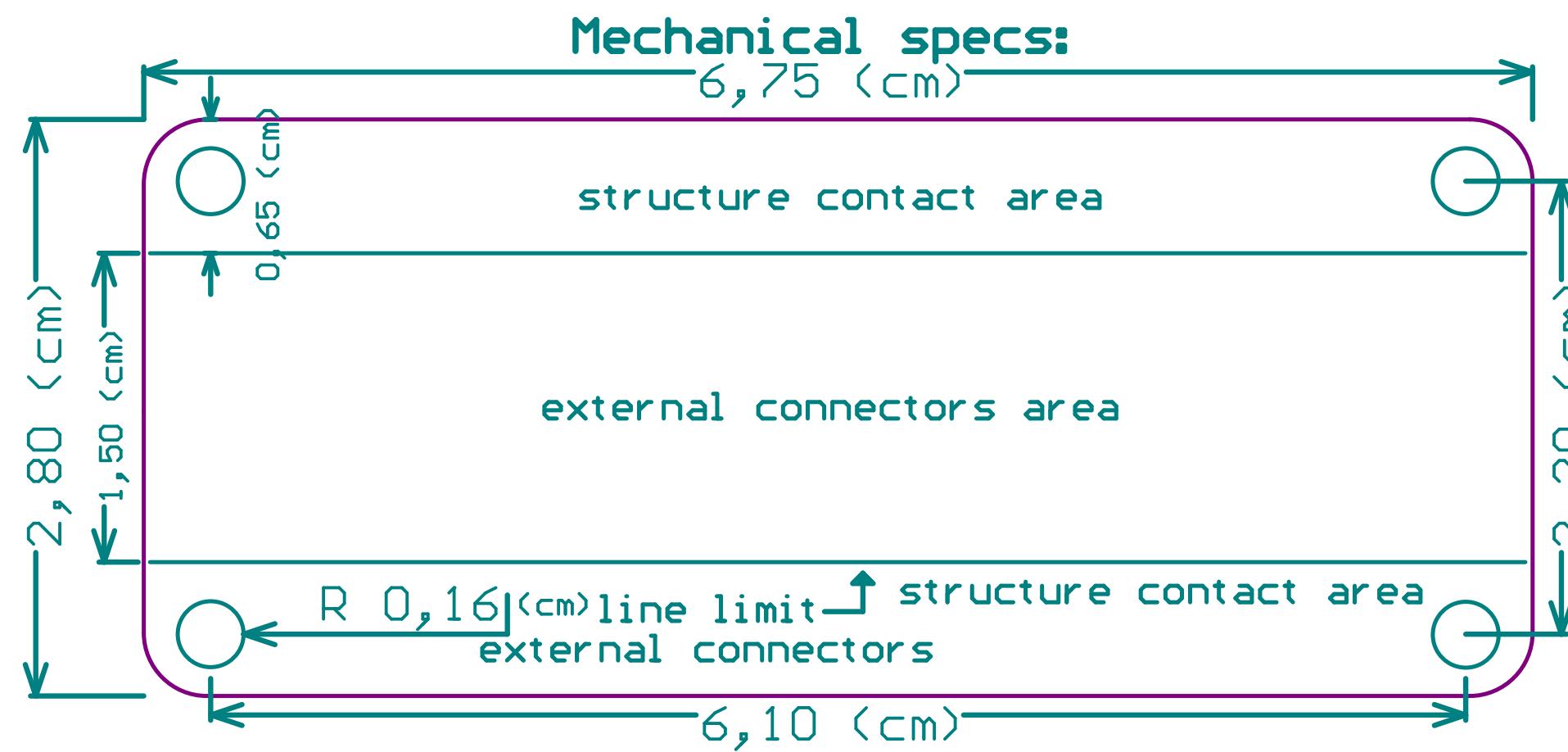


Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Overlay				
2	Top Solder	Solder Resist	0,010mm	3,5	
3	Top Layer	Copper	0,036mm		
4	Dielectric 1	FR-4	1,500mm	4,8	
5	Bottom Layer	Copper	0,036mm		
6	Bottom Solder	Solder Resist	0,010mm	3,5	
7	Bottom Overlay				



TITLE: IIP SEMI USB N.2 BOARD		REV: 1.0	DATE: 02/07/2020
MATERIAL: FR4		Silkscreen color: white	Project: IIP FloripaSat-2
Board Thickness: 1.6mm		Layers: 02	Space Technology Research Laboratory Federal University of Santa Catarina
PCB Surface: HASL		Drawing: Yan C. de Azeredo	SpaceLab UFSC





CN2
+

CN6
+

CN1
+

CN5
+

CN4
+

CN7
+

CN3
+

M1

Interstage Interface Panel
FD1 Board N.2 Rev 1.0

M2

SpaceLab UFSC 2020

FD2



TP2



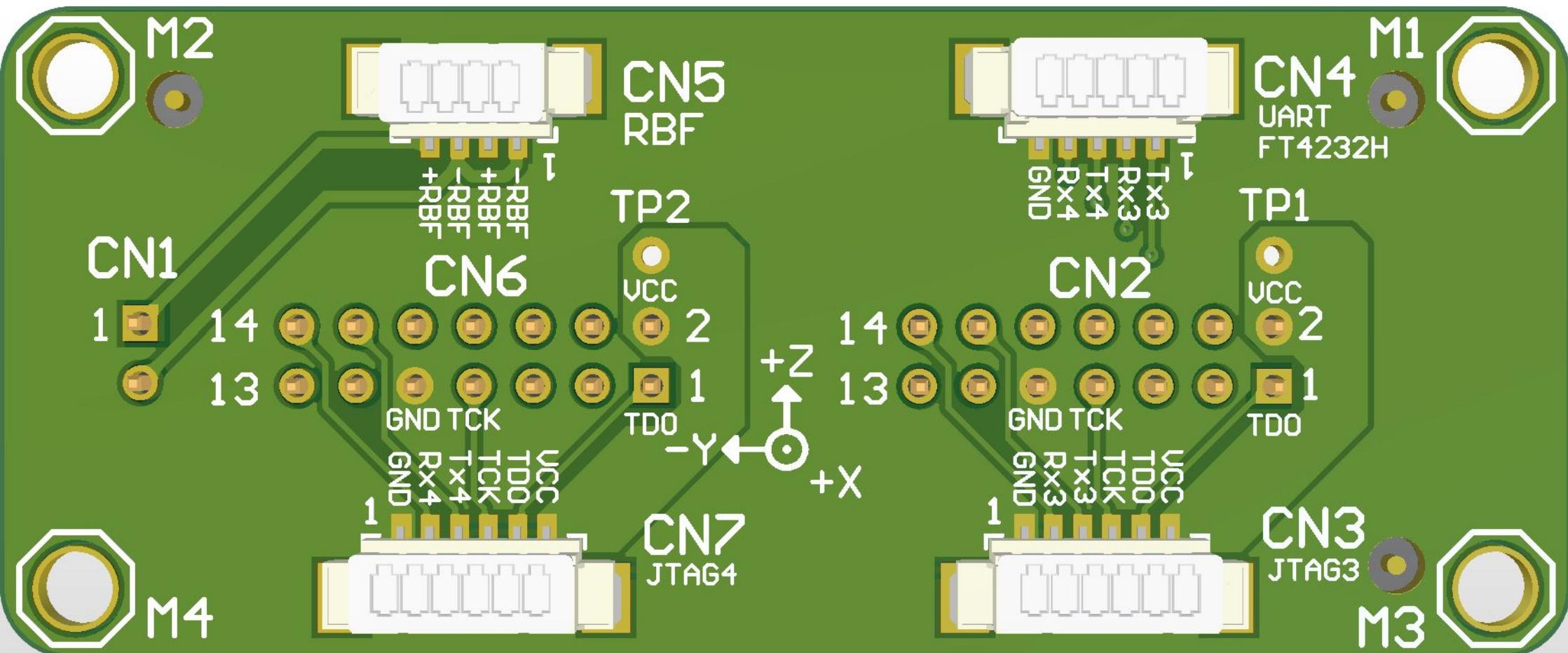
M3

FD3 line limit
external
connectors



structure contact
area

M4





Bill of Materials

Source Data From: 2_IIP_RBF.PrjPCB

Project: 2_IIP_RBF.PrjPCB

Variant: None

Project Code: <Parameter ProductCode not found>

Report Date: 03/07/2020 11:17:25

Print Date: 03/07/2020 11:17:31

#	Supplier 1	Supplier Part Number 1	Supplier 2	Supplier Part Number 2	Manufacturer Part Number	Comment	Description	Quantity	Designator
1	Mouser	855-M20-9990245	Digi-Key	952-2261-ND	M20-9990245	2 POS Pin Header	M20 HDR. PIN, SIL, VERT, 2W	1	CN1
2	Mouser	855-M20-9980745	Digi-Key	952-1932-ND	M20-9980745	7 POS dual line Pin Header 2.54mm	Headers & Wire Housings 07+07 DIL VERTICAL PIN HEADER GOLD HT	2	CN2, CN6
3	Mouser	538-53398-0671	Digi-Key	WM7610DKR-ND	53398-0671	6 pin Picoblade	1.25mm Pitch PicoBlade® Header, Surface Mount, Vertical, 6 Circuits	2	CN3, CN7
4	Mouser	538-53398-0571	Digi-Key	WM7609CT-ND	53398-0571	5 pin PicoBlade	Wire-To-Board Connector, Vertical, PicoBlade 53398 Series, Surface Mount, Header, 5, 1.25 mm	1	CN4
5	Mouser	538-53398-0471	Digi-Key	WM7608CT-ND	53398-0471	4 pin Picoblade	Connector Header Surface Mount 4 position 0.049" (1.25mm)	1	CN5