



**TALLINN UNIVERSITY OF TECHNOLOGY**  
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## Bitcoin mining on FPGA

some description

Master thesis

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## **Author's Declaration**

This work is composed by myself independently. All other authors' works, essential states from literary sources and facts from other origins, which were used during the composition of this work, are referenced.

Signature of candidate:

Date:

## Acronyms

**ASIC** Application-specific integrated circuit. 4

## **Annotation**

Current work introduces conceptual approaches for implementing a user interface for bitcoin mining on FPGA. It is also described about ASIC [here](#).

# Annotatsioon

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See here some ASIC<sup>1</sup>  
Some new text here

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<sup>1</sup>Application-specific integrated circuit

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