

RadixVM

Scalable address spaces for multithreaded applications

Austin T. Clements,
M. Frans Kaashoek,
Nickolai Zeldovich

Presented by Simon Pratt

February 12, 2016

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- 1 Multithreaded address space scaling

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 - ① (And reference counting)

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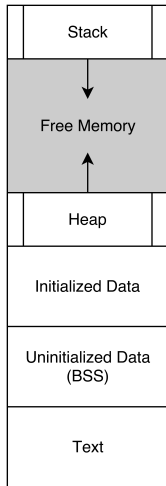
- ① Multithreaded address space scaling
 - ① (And reference counting)
- ② Remote TLB shutdown

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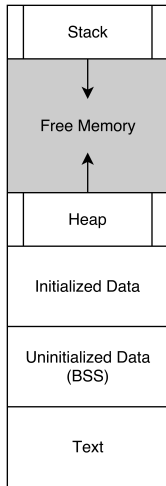
But first, some background...

Background: `malloc` and `mmap`



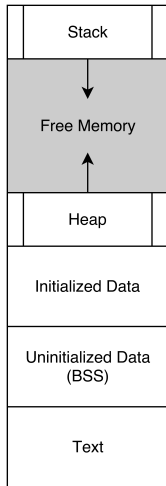
- `malloc` and `free`
 - User-level library function
 - Allocates/frees space in virtual memory
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Background: `malloc` and `mmap`



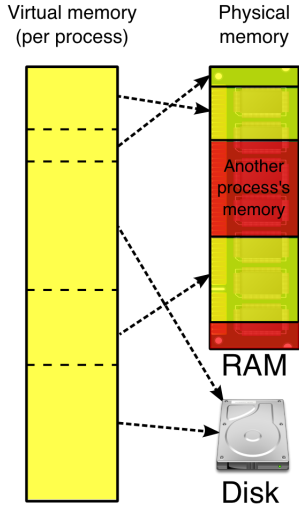
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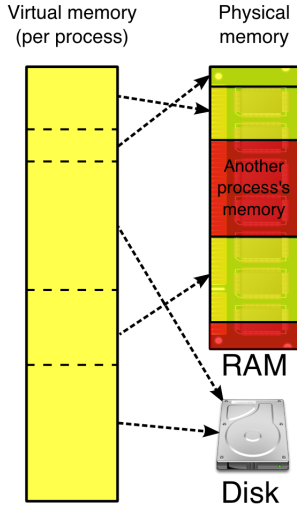
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- So what is virtual memory?

Background: Virtual Memory



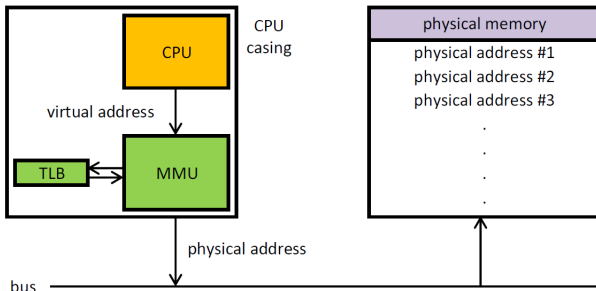
- (on x86) Instructions operate on virtual addresses

Background: Virtual Memory



- (on x86) Instructions operate on virtual addresses
- Data may be stored:
 - In physical memory
 - On disk

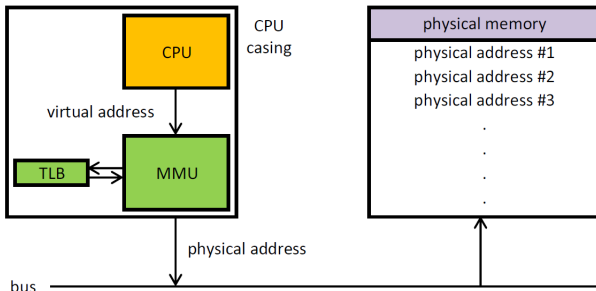
Background: Virtual Memory



CPU: Central Processing Unit
MMU: Memory Management Unit
TLB: Translation lookaside buffer

- Hardware, memory management unit (MMU)
 - Performs the translation
 - Keeps a cache (TLB) of virtual \rightarrow physical mappings
 - No entry in TLB \rightarrow page fault

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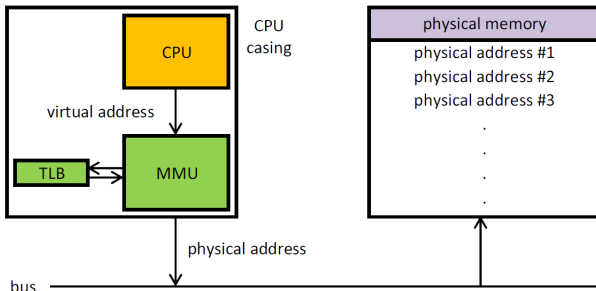
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 - Maintains the mapping (per process)
 - Handles page faults

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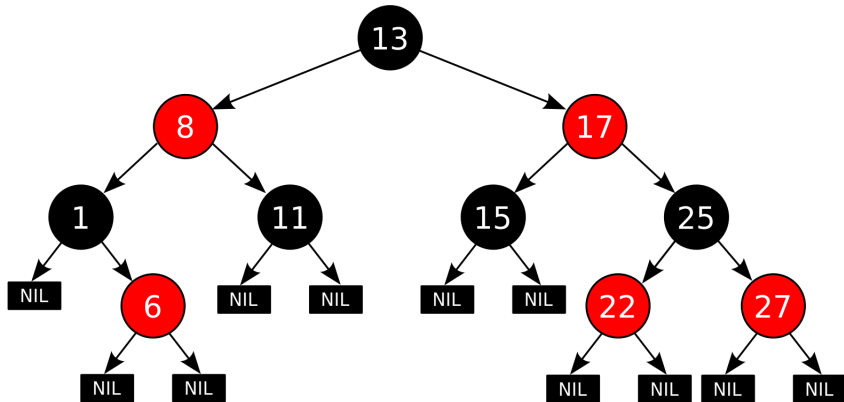
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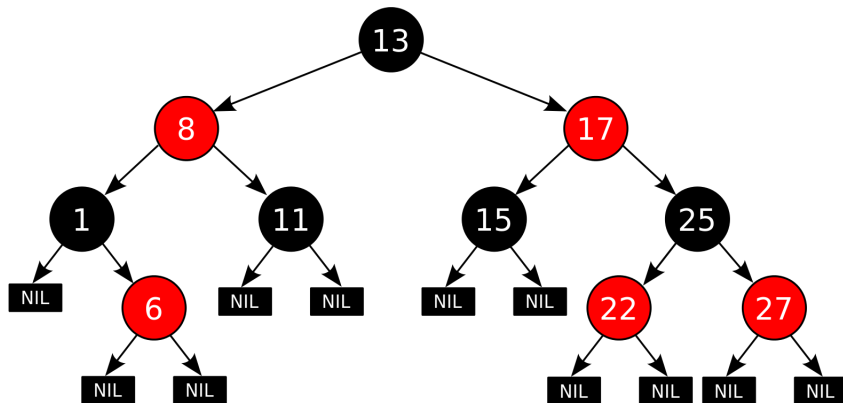
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- So how does the OS store this?

Background: Linux Virtual Memory



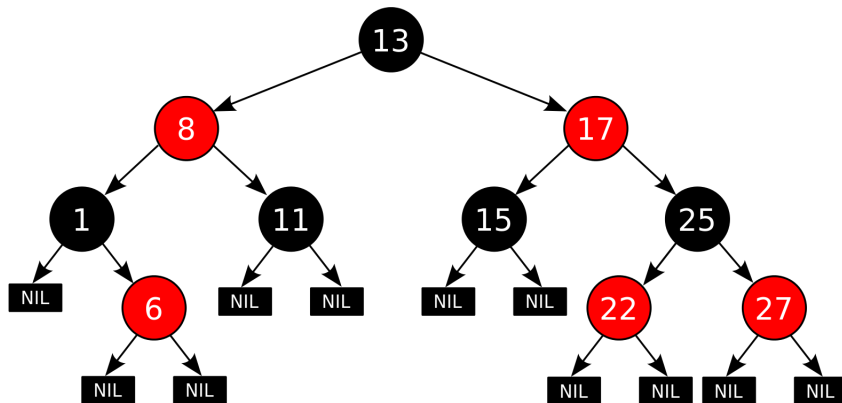
- Red-black tree
- Allows the kernel to search for memory area covering a virtual address

Background: Linux Virtual Memory



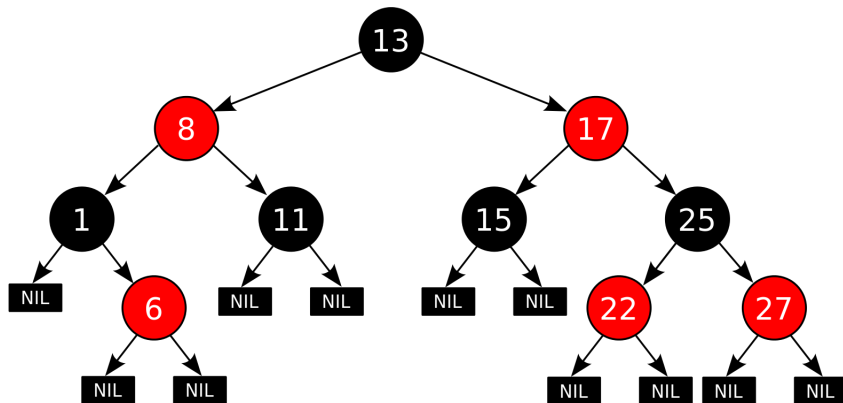
- Red-black tree
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- **Problem: A single lock per address space!**

Problem 1: Multithreaded Address Space Scaling



- A single lock on this structure → mmap within a single process is serialized

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- Aside: prwlock paper notes that Psearchy is mmap-intensive

RadixVM solves 2 problems:

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 - ① (And reference counting)
- ② Remote TLB shutdown

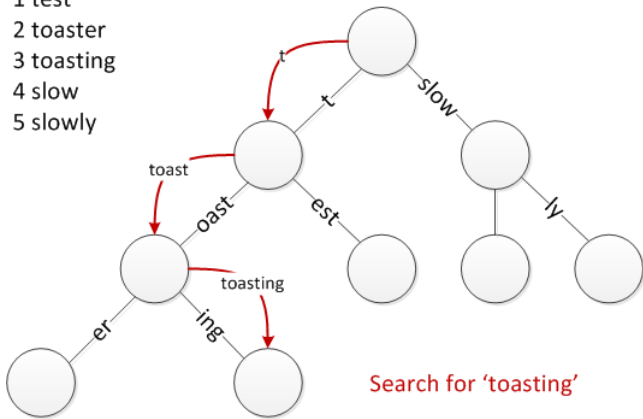
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Background: Radix Tree

- A.K.A. prefix tree

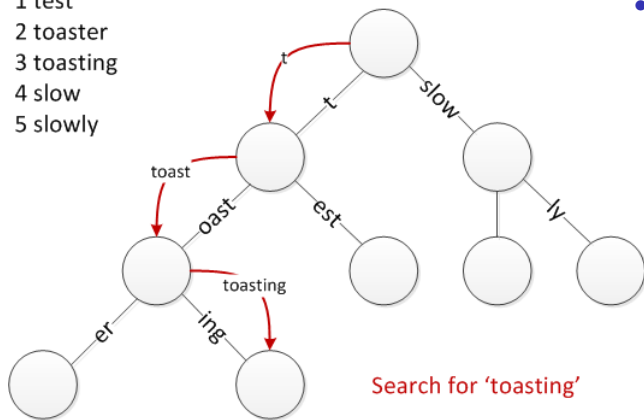
1 test
2 toaster
3 toasting
4 slow
5 slowly



Background: Radix Tree

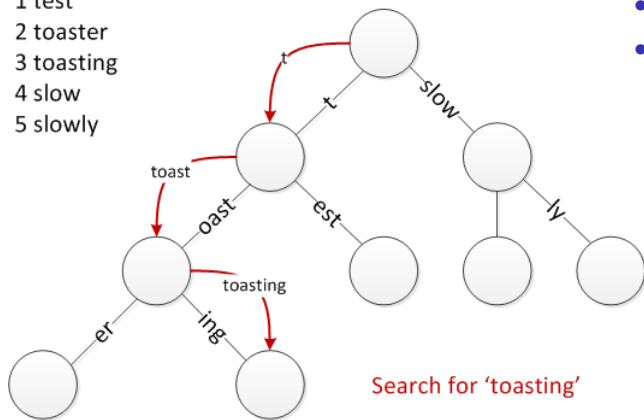
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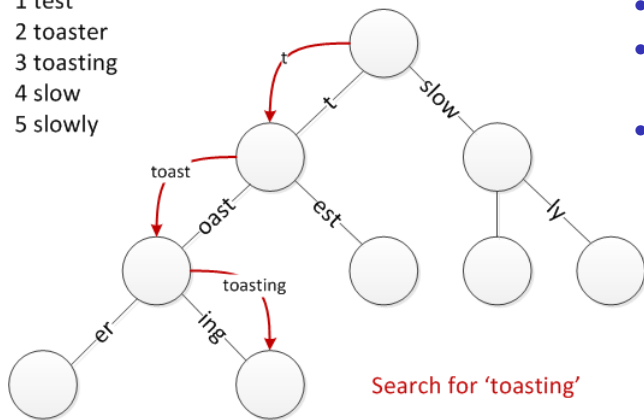


Search for 'toasting'

- A.K.A. prefix tree
- Edges labeled
- Concatenation of edge labels along root→node path gives a string

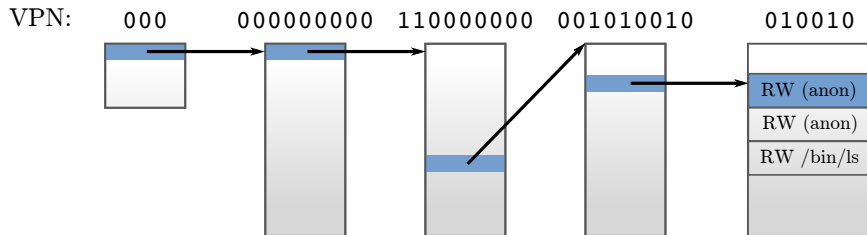
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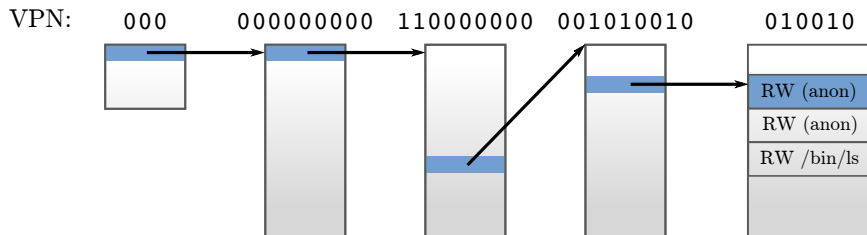
- A.K.A. prefix tree
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- In OSES, usually strings of bits

Design: RadixVM Data Structure



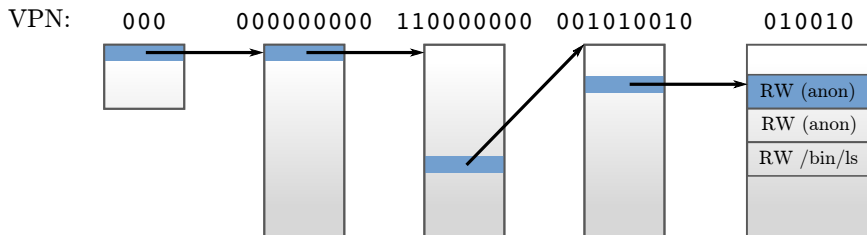
- Pretty much a page table

Design: RadixVM Data Structure



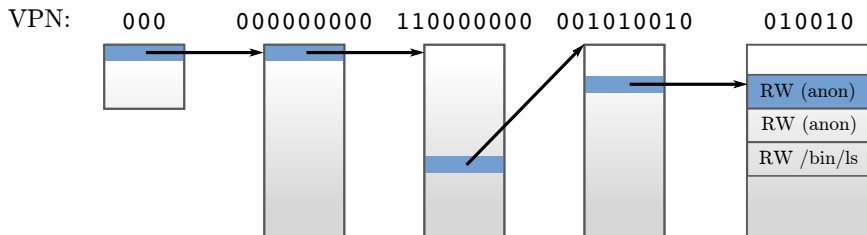
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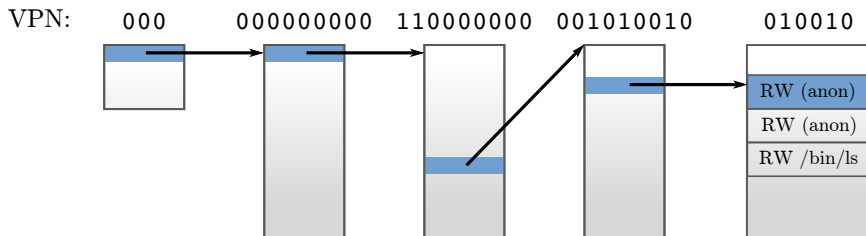
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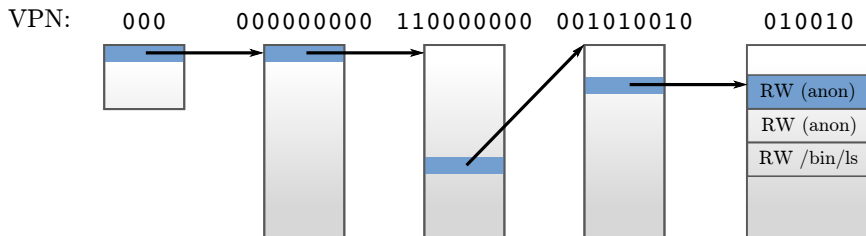
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Design: RadixVM Data Structure



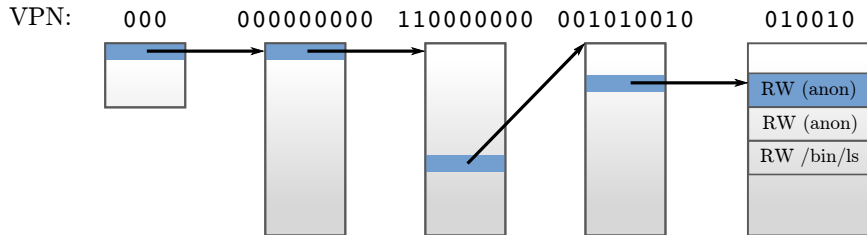
- Minimizes cache-line contention

Design: RadixVM Data Structure



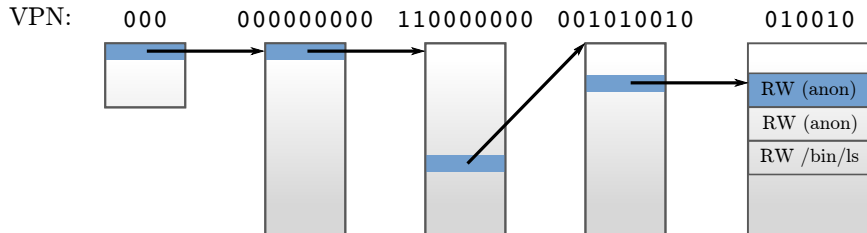
- Minimizes cache-line contention
- Supports precise range locking

Problem 1.1: When to Free



- How do we know when we can free the underlying pages?

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- Reference counting!

RadixVM solves 2 problems:

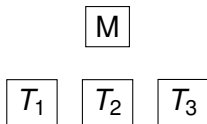
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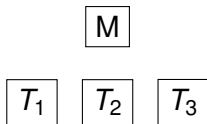
Background: Reference Counting

- 3 operations: inc, dec, zero?



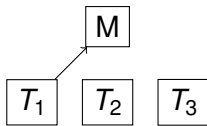
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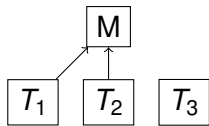


Background: Reference Counting

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 - 1 References: 0
 - 2 inc \rightarrow References: 1

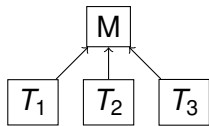


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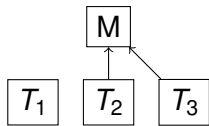
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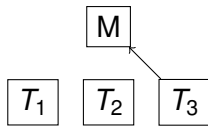
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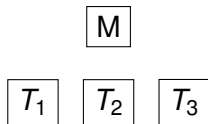
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Background: Reference Counting

T_1 T_2 T_3

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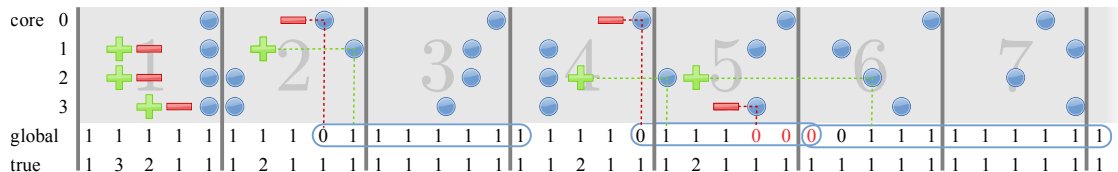
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- But: single counter → contention

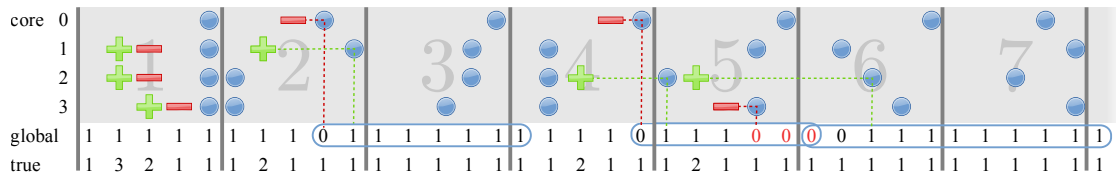
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Design: Refcache



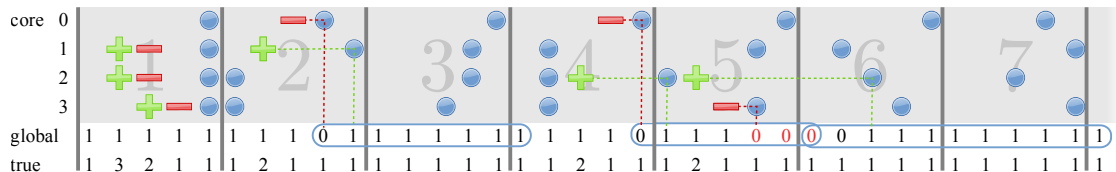
- Per-core lazy counting
 - Each core stores a delta
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Design: Refcache



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Design: Refcache



- Per-core lazy counting
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 - Delta updated lazily (blue circles)
- Divides time into epochs
- Ref. count zero for an entire epoch → free

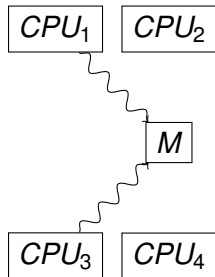
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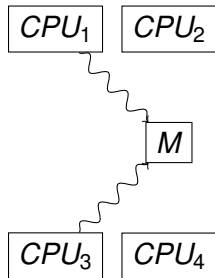
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Problem 2: Remote TLB Shootdowns



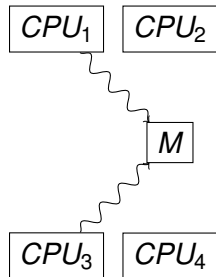
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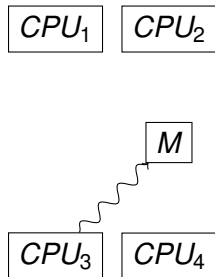
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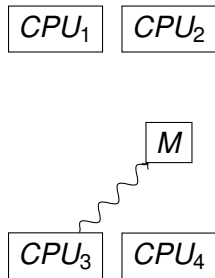
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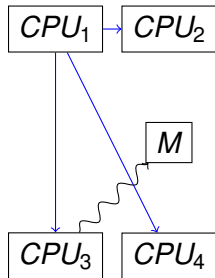
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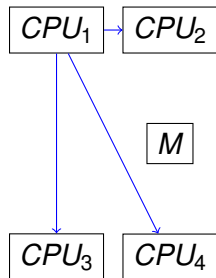
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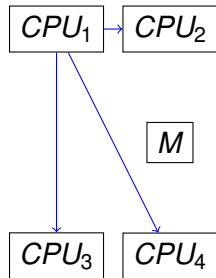
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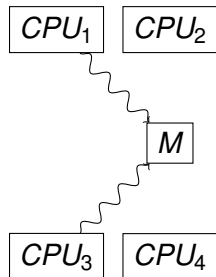
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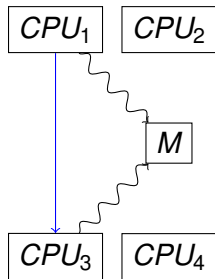
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- **This is expensive!**

Design: Targeted TLB Shootdowns



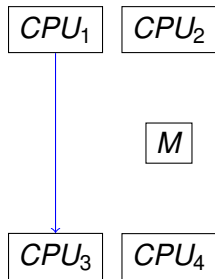
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Design: Targeted TLB Shootdowns



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Design: Targeted TLB Shootdowns



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sv6

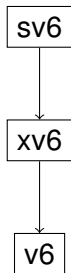
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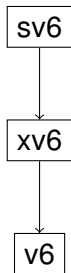


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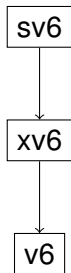


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 - <https://github.com/aclements/sv6>

Comparison

- Compared against:

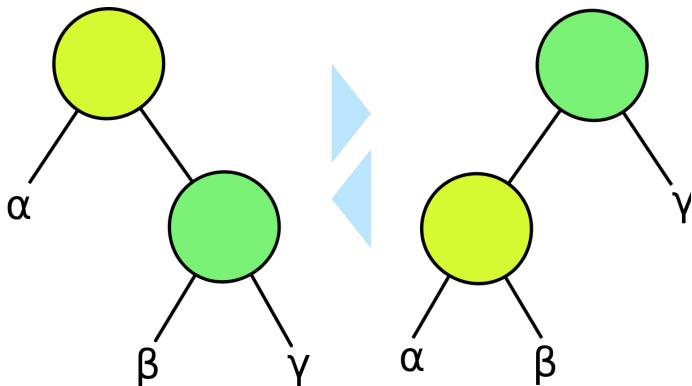
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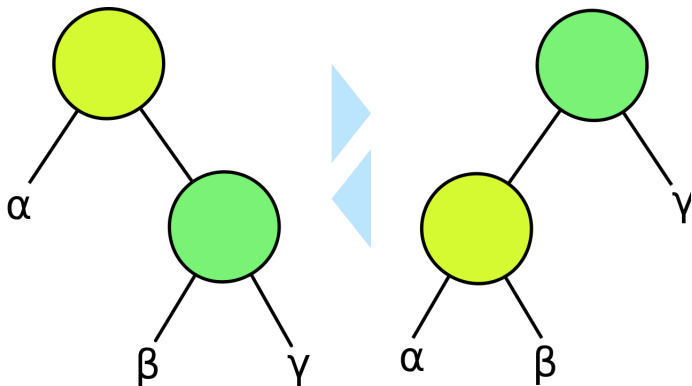
- Compared against:
 - Default Linux
 - “Bonsai”

Background: Bonsai



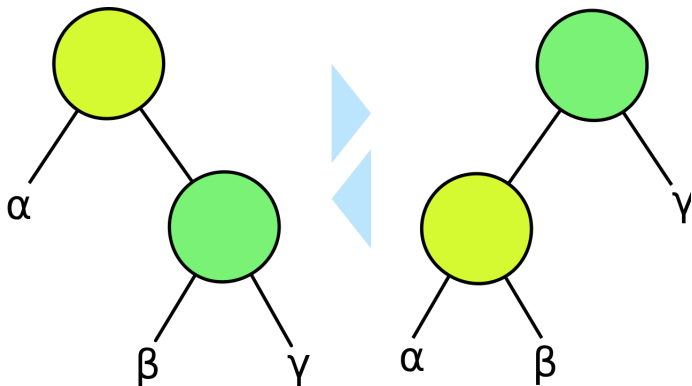
- Designed by the same authors

Background: Bonsai



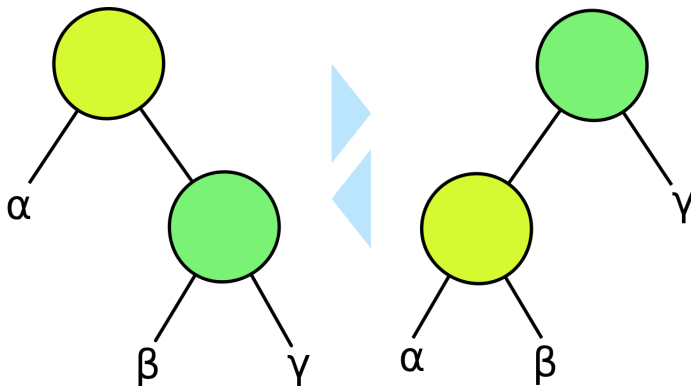
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Background: Bonsai



- Designed by the same authors
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- Maintains bounded balance rather than strict balance (this means fewer rotations)

Background: Bonsai



- Designed by the same authors
- Uses an RCU-based balanced binary tree
- Maintains bounded balance rather than strict balance (this means fewer rotations)
- Rotations construct a new subtree rather than mutate the old one

Application: Metis

- MapReduce Library

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- Single-server

Application: Metis

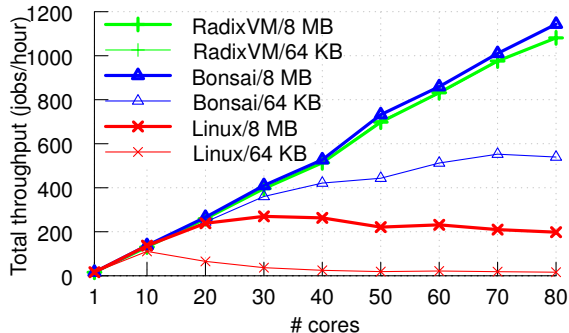
- MapReduce Library
- Single-server
- Multithreaded

Application: Metis

- MapReduce Library
- Single-server
- Multithreaded
- Stresses concurrent `mmaps` and `pagefaults`, but not concurrent `munmaps`

Application: Metis

- MapReduce Library
- Single-server
- Multithreaded
- Stresses concurrent `mmaps` and `pagefaults`, but not concurrent `munmaps`



Microbenchmark: Local

- `mmap` a private 4KB region in shared address space

Microbenchmark: Local

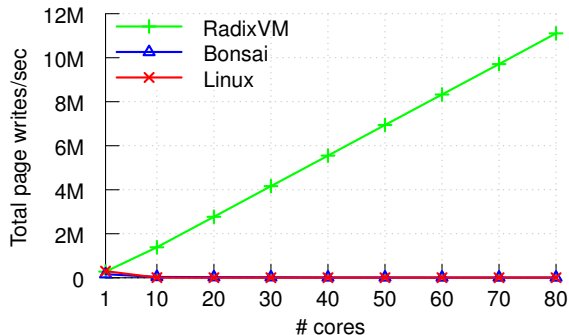
- `mmap` a private 4KB region in shared address space
- Write to every page in region

Microbenchmark: Local

- `mmap` a private 4KB region in shared address space
- Write to every page in region
- `munmap` region

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Microbenchmark: Pipeline

- Each thread `mmap` a region

Microbenchmark: Pipeline

- Each thread `mmap` a region
- Write to every page in region

Microbenchmark: Pipeline

- Each thread `mmap` a region
- Write to every page in region
- Pass region to next thread

Microbenchmark: Pipeline

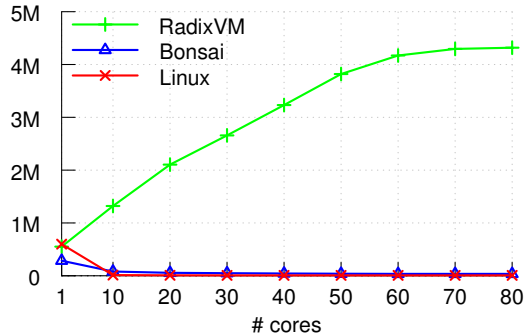
- Each thread `mmap` a region
- Write to every page in region
- Pass region to next thread
- Write to every page in passed region

Microbenchmark: Pipeline

- Each thread `mmap` a region
- Write to every page in region
- Pass region to next thread
- Write to every page in passed region
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Microbenchmark: Pipeline

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- Write to every page in region
- Pass region to next thread
- Write to every page in passed region
- `munmap` region



Microbenchmark: Global

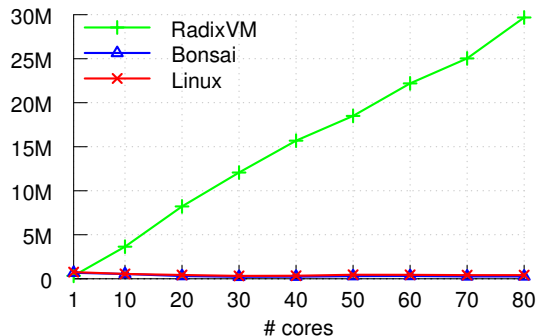
- Each thread `mmap` a 64KB region within a large region of memory

Microbenchmark: Global

- Each thread `mmap` a 64KB region within a large region of memory
- All threads access all pages in random order

Microbenchmark: Global

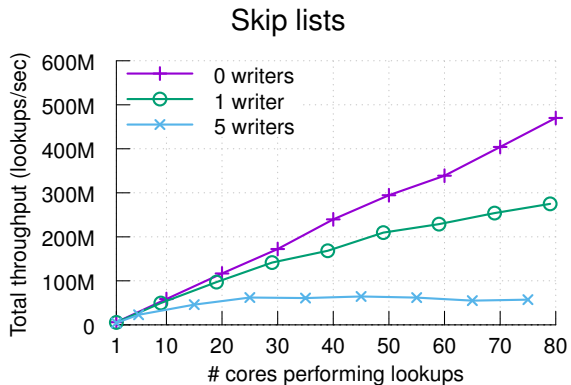
- Each thread `mmap` a 64KB region within a large region of memory
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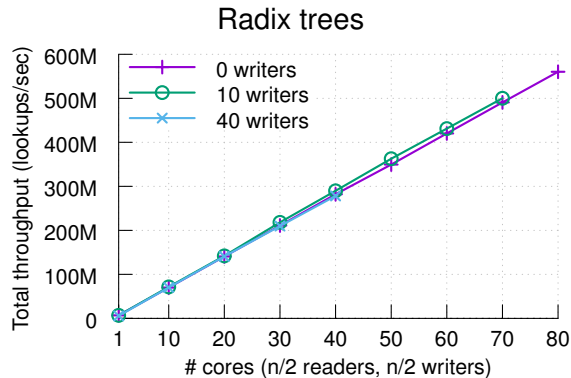
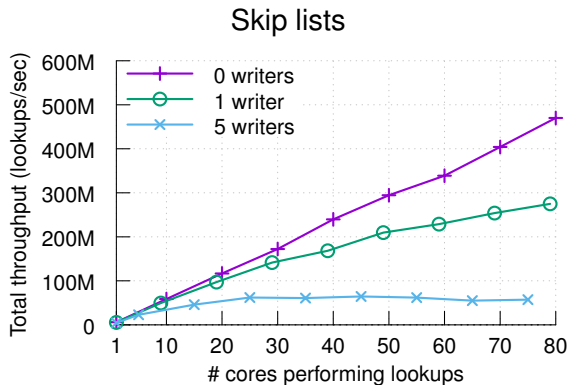
Do we really need all 3 pieces?

- Radix trees
- Refcache
- Targeted TLB shutdown

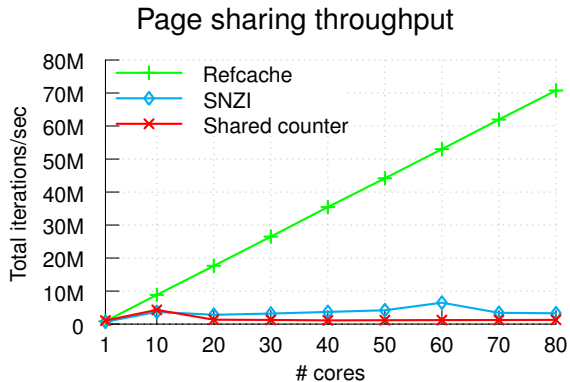
Question: Do we need radix trees?



Question: Do we need radix trees?



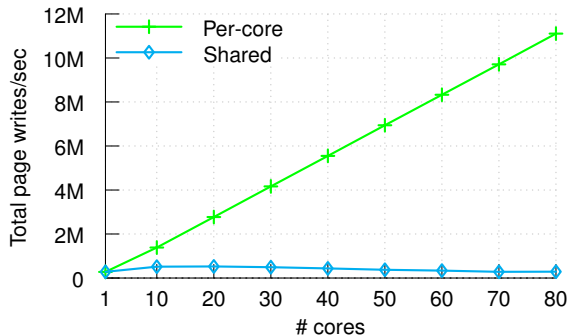
Question: Do we need Refcache?



SNZI: Scalable Non-Zero Indicators

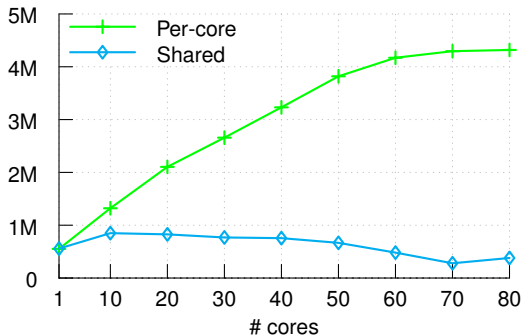
Question: Do we need targeted TLB shutdown?

Local microbenchmark, per-core versus shared



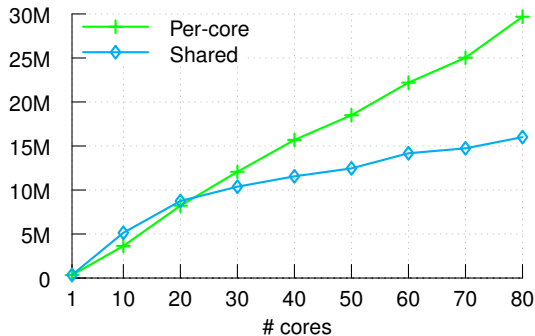
Question: Do we need targeted TLB shutdown?

Pipeline microbenchmark, per-core versus shared



Question: Do we need targeted TLB shutdown?

Global microbenchmark, per-core versus shared



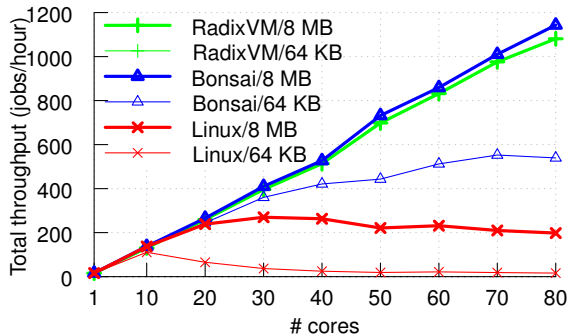
Issue: Memory Overhead

	RSS	Linux		Radix tree
		VMA tree	Page table	(rel. to Linux)
Firefox	352 MB	117 KB	1.5 MB	3.9 MB (2.4×)
Chrome	152 MB	124 KB	1.1 MB	2.4 MB (2.0×)
Apache	16 MB	44 KB	368 KB	616 KB (1.5×)
MySQL	84 MB	18 KB	348 KB	980 KB (2.7×)

- RSS
 - Resident Set Size
 - physical memory used by a process
- VMA
 - Virtual Memory Areas
 - stored in a red-black tree in Linux

Summary

Metis performance



- **Good:** Scales well on
 - Metis, real-world application
 - Microbenchmarks
- **Bad:** Increased memory overhead

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Firefox	352 MB	1.5 MB	3.9 MB (2.4×)
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References

- Clements, Austin T., M. Frans Kaashoek, and Nickolai Zeldovich. "RadixVM: Scalable address spaces for multithreaded applications." In *Proceedings of the 8th ACM European Conference on Computer Systems*, pp. 211-224. ACM, 2013.
 - Revised version: <https://pdos.csail.mit.edu/papers/radixvm:eurosys13-2014-08-05.pdf>
- 6.828 Lecture on RadixVM:
<https://www.youtube.com/watch?v=qlg7jqBtR4c>
- Clements, Austin T., M. Frans Kaashoek, and Nickolai Zeldovich. "Scalable address spaces using RCU balanced trees." *ACM SIGPLAN Notices* 47, no. 4 (2012): 199-210.
 - Available online: <https://pdos.csail.mit.edu/papers/rcuvm:asplos12.pdf>
- Linux VM info from:
<http://duartes.org/gustavo/blog/post/how-the-kernel-manages-your-memory/>

- Refcache diagram, radix data structure diagram, and RadixVM charts used with permission by Austin Clements
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