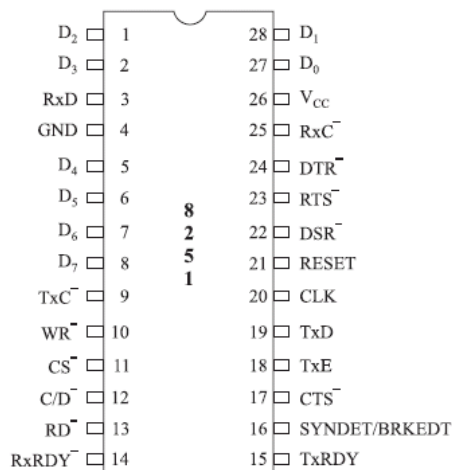


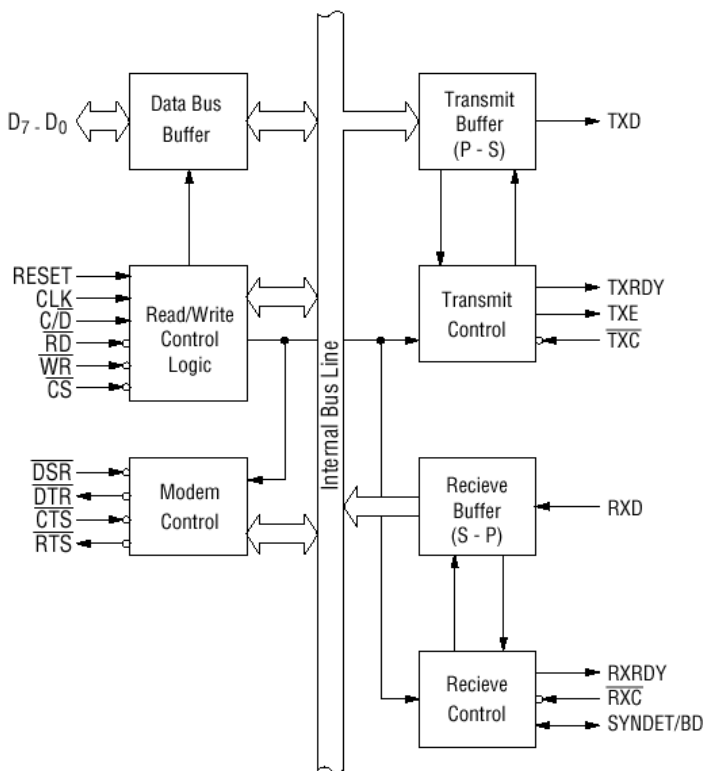
8251A

Programmable communication interface

a. Pin configuration



b. 8251A block diagram



c. 8251A Basic operation

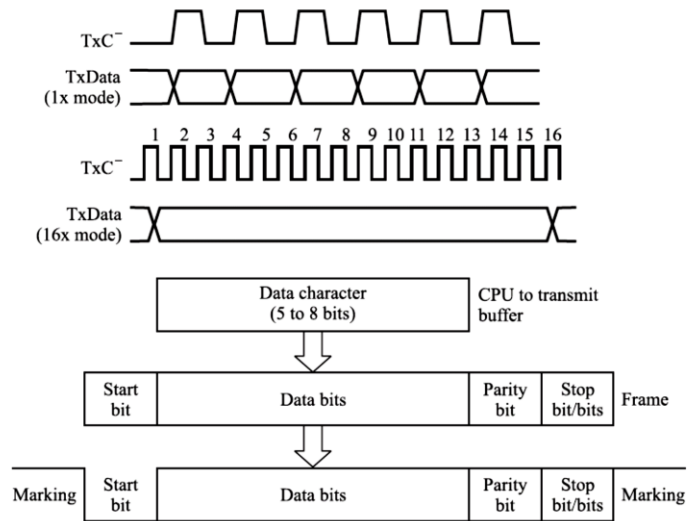
| CS | C/D | RD | WR | |
|----|-----|----|----|--------------------|
| 1 | x | x | x | Data Bus 3-State |
| 0 | x | 1 | 1 | Data Bus 3-State |
| 0 | 1 | 0 | 1 | Status → CPU |
| 0 | 1 | 1 | 0 | Control Word ← CPU |
| 0 | 0 | 0 | 1 | Data → CPU |
| 0 | 0 | 1 | 0 | Data ← CPU |

d. Pin names *

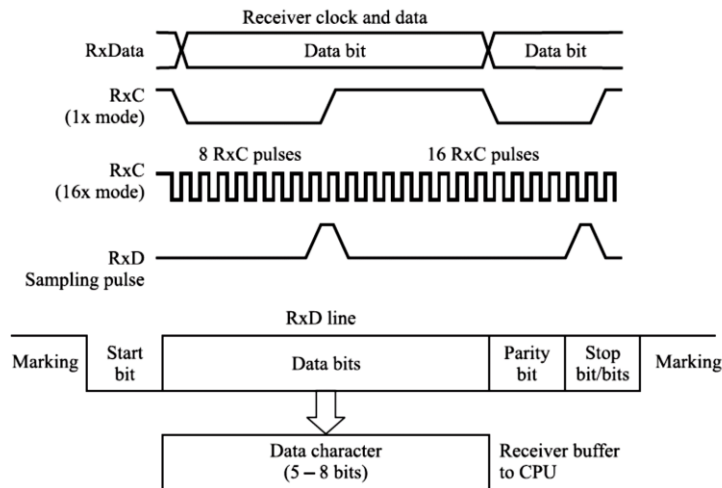
| | | | |
|-------|--|-----------|--|
| D0-D7 | Data Bus (8 bits) | RxRDY | Receiver Ready (has character for 8086) |
| C/D- | Control or Data is to be written or read | TxRDY | Transmitter Ready (ready for char from 8086) |
| RD- | Read Data Command | DSR- | Data Set Ready |
| WR- | Write Data or Control Command | DTR- | Data Terminal Ready |
| CS- | Chip Select | SYNDET/BD | Sync Detect/Break Detect |
| CLK | Clock Pulse (TTL) | RTS- | Request to Send Data |
| RESET | Reset | CTS- | Clear to Send Data |
| TxC- | Transmitter Clock | TxE | Transmitter Empty |
| TxD | Transmitter Data | Vcc | +5 Volt Supply |
| RxC- | Receiver Clock | GND | Ground |
| RxD | Receiver Data | | |

* x- je isto što i \bar{x} , X/Y- je isto što i X/\bar{Y}

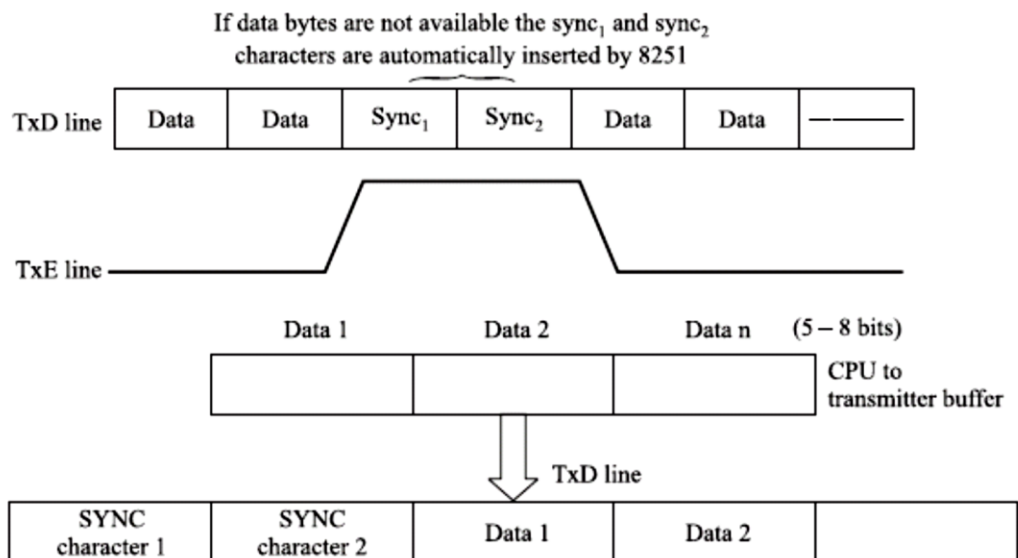
e. Format of the asynchronous mode transmission



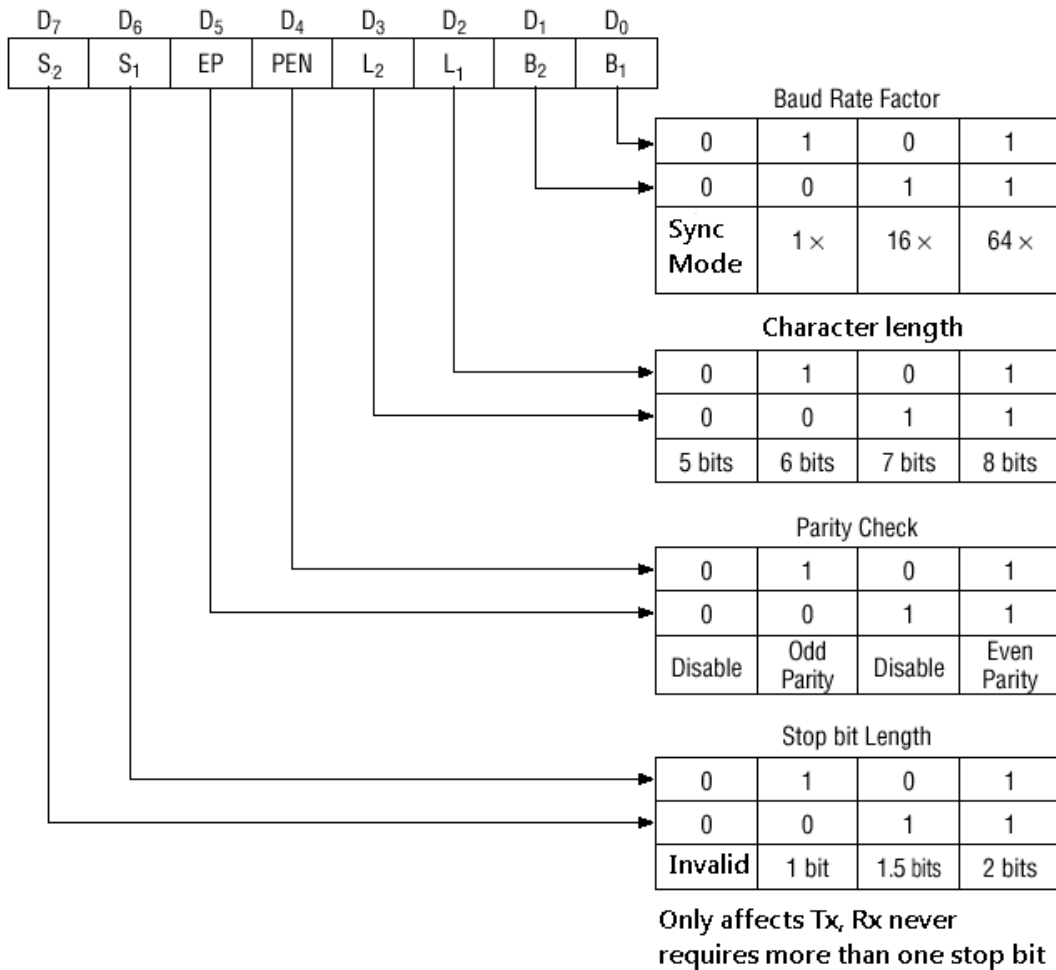
f. Format of the asynchronous mode receive



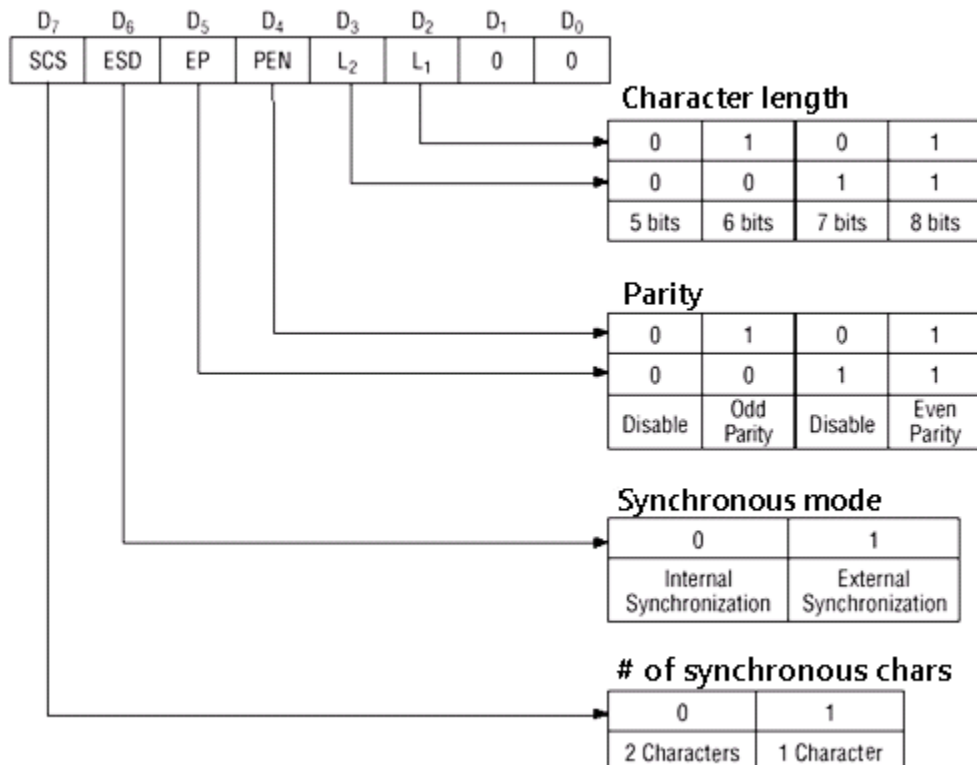
g. Format of the synchronous mode transmission



h. Mode instruction format, Asynchronous mode



i. Mode instruction format, Synchronous mode



j. Command instruction format

