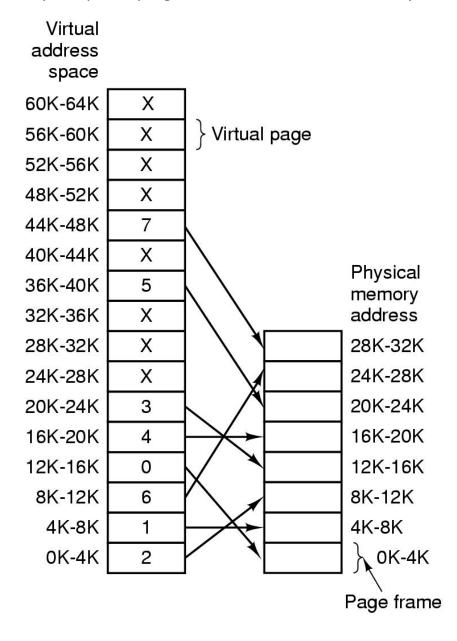
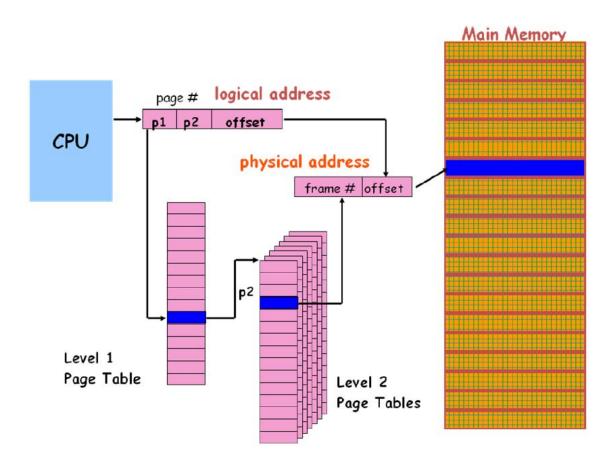
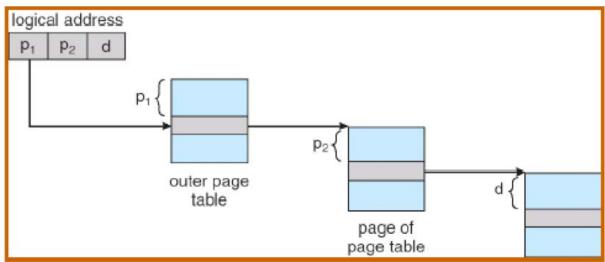
Assignment 6

Introduction

Most virtual memory systems use a technique called **paging**, which we will now describe. On any computer, programs reference a set of memory addresses.







Page number	Page offset
p=m-n bits	d=n bits

4 KB page size

32 bit logical addresses

$$4KB = 2^2 * 2^{10} B = 2^{12} B \rightarrow d=12$$

p=32-12=20 bits	d=12 bits
32 bits logical address	

Guideline

Page number = 2^d

1 One level Paging

A computer with an Intel Pentium processor (i.e. 32bit, 4K page size, 2 layer paging) runs a memory-based operating system based on paging. The dynamic memory space (heap segment) of a process occupies all addresses from 0x08800000 to 0x08dfffff (size 6MBytes). How many memory pages does the page table occupy to represent the above segment? Which Page Directory Entries are used?

2 Two Level Paging

We have an Intel Pentium processor (i.e. 32bit, 4K page size, 2 layer paging) and the operating system uses paging. The process has 16MB. How much space do the page tables of the process take up in memory in the best and worst case?

3 Singly Linked List (Optional)

A computer system has a 36-bit virtual address space with a page size of 8K and the page frame address in each Page Table Entry (PTE) is 32 bits.

- How many pages are in the virtual address space?
- What is the maximum size of addressable physical memory in this system?
- If the average process size is 8GB, would you use a one-level, two-level, or three-level page table? Why?