North South University -Spring 2020
CSE231.11: Digital Logic Design
Final assessment list

			Parts of assignment			
No.	Student ID	Name	Digits	Combinational Circuit	Counter arrangement	Flip-flop for counter design
1	1510831042	Ibrahim Khalil	10831	simplified POS	2-5-7-10-12	jk
2	1511111042	Syed Md. Irfanul Alam Irfan	10864	simplified SOP	1-5-7-9-13	D
3	1511122042	Mushayev Masrur	15384	MUX	3-5-8-11-13	T
4	1511387642	Md. Tousif Rob Chowdhury	27953	Decoder	4-5-8-10-14	SR
5	1620018042	Md. Sifur Rahman	20018	NAND	5-6-8-11-15	D
6	1620181042	Meftaul Hafiz	20181	NOR	3-6-8-11-13	jk
7	1620857042	Md. Abu Kausar	20857	Generalized SOP	1-3-6-9-13	SR
8	1711096642	Saddam Hossain Tonmoy	11096	Generalized POS	1-4-7-10-12	D
9	1712531042	Zakiratul Karim	12531	MUX	9-7-8-11-19	jk
10	1721618642	Md. Ariful Islam	21618	Decoder	3-5-9-10-13	D
11	1721918642	Md Sazzad Hossain	21918	NAND	3-11-8-11-13	T
12	1731145042	Quazi Md. Nabil	31145	NOR	2-7-8-11-14	SR
13	1731194042	Sudipta Bhatta	31194	Generalized SOP	3-4-8-11-13	D
14	1731505642	Md. Omi Hassan	31505	Generalized POS	1-4-8-11-15	jk
15	1731913642	Md. Mashikul Islam	31913	simplified POS	1-5-7-9-13	SR
16	1811229642	Md. Hifjur Rahman	11229	simplified SOP	1-2-7-12-15	D
17	1812098042	Al Sabri Bhuiyan	12098	NAND	1-8-10-12-15	jk
18	1812628042	Ismail Bin Suhrwardy	12628	Generalized POS	2-9-10-12-14	D
19	1813500642	Laisul Morshed	13506	Decoder	1-3-8-10-15	T
20	1821719042	Prity chowdhury	21719	simplified SOP	1-5-7-9-15	SR
21	1821854042	Md.Abdullah Zul	21854	MUX	2-5-8-11-12	D
22	1831185042	Rofiqul Alam Shehab	31185	NAND	2-4-6-8-13	jk
23	1831400642	Md. Zubayer Hossain Chowdhury	31460	NOR	5-9-10-13-14	SR
24	1831513642	S M Gazzali Arafat Nishan	31513	Generalized SOP	3-4-8-11-13	D

Project Description: You are to design a system that will print the digits mentioned in "Digits" column of the above table. For this puspose yoou will design a sequential circuit which will include a combinational part, counter, register and 7 segment display. The System box diagram is shown bellow.
Combinational Part: Use the element mentioned in "Combinational Circuit" column. Counter: Use the element mentioned in "Counter arrangement" & "Flip-flop for counter design" column.
Register: You can make register with which ever flipflop you choose. The output of combinational circuit should first go to register ten to 7 segment

General Instructions: The repot should include all workouts (truth table, k-map, combinational circuit,counter desgin steps, register design steps, final circuit, logisim design

You may consult textbooks and online sources. However, for online sources, make sure they are authentic sources. If you give online references, please mention the sources.

It is not allowed to consult with any person who has knowledge of this subject, including other students of this course. You may not ask question to the instructor if you do not understand the question. All solutions have to be your own work.

You must show all work for each problem to receive full credit.

You must include the front page also attached with this assignment.