### **OPB916 Series**

### **Features:**

- Low power consumption
- Data rates to 250 kBaud
- Choice of two logic states and two electrical outputs
- 24" (610 mm) minimum 26 AWG UL listed wires
- Slot width 0.20" (5.08 mm)
- Slot Depth 0.635" (16.13 mm)





### **Description:**

The OPB916 series of Photologic® photo integrated circuit switches provide optimum flexibility. Each switch consists of an infrared Light Emitting Diode (LED) and a Photologic® photo integrated circuit, mounted in an opaque housing with clear windows for dust protection. The deep slot allows for a longer reach of the optical path from the 0.650" (16.5 mm) mounting plane. Internal apertures are 0.010" x .060" (.25 mm x 1.52 mm) for the Photologic's "S" side and 0.05" x 0.06" (1.27 mm x 1.52 mm) for the LED "E" side.

Devices in this series exhibit stable performance over supply voltages ranging from 4.5 V to 16.0 V, and may be specified as buffered or inverted with an internal 10 k $\Omega$  pull-up resistor or open collector output. Devices are TTL/LSTTL compatible and can drive up to 10 TTL loads.

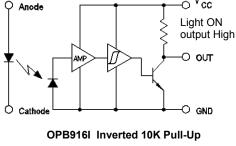
Custom electrical, wire or cabling are available. Contact your local representative or OPTEK for more information.

### **Applications:**

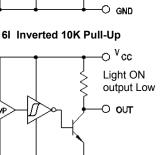
- Mechanical switch replacement
- Speed indication (tachometer)
- Mechanical limit indication
- Edge sensing

Ordering Information							
Part	LED Peak	Sensor	Slot Width /	Aperture Emitter /	Lead Length /		
Number	Wavelength	Photologic <sup>®</sup>	Depth	Sensor	Wire		
OPB916BZ		10K Pull-Up	0.200" /	0.0511./	2411 / 26		
OPB916IZ	880 nm	Inv-10K Pull-Up	0.200" / 0.635"	0.05" / 0.01"	24" / 26 AWG Wire		
OPB916BOCZ		Open-Collector	0.033	0.01	AWG WIIC		

### Color Description Red Anode Black Cathode White Vcc Blue Output Green Ground

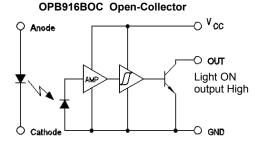


OPB916B 10K Pull-Up

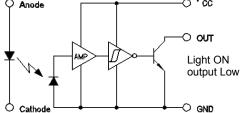


O GND

-O V cc



## **OPB916IOC Inverted Open-Collector** O Anode OUT





General Note

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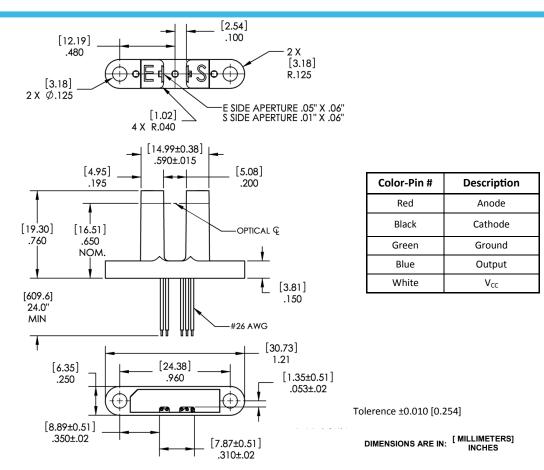
TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

Anode

Cathode

OPTEK Technology, Inc. 1645 Wallace Drive, Carrollton, TX 75006 Ph: +1 972 323 2200 www.optekinc.com | www.ttelectronics.com





Absolute Maximum Ratings (T <sub>A</sub> = 25° C unless otherwise noted)				
Storage & Operating Temperature Range	-40°C to +80°C			
Input Infrared LED				
Diode Reverse DC Voltage	2 V			
Input Diode Power Dissipation <sup>(2)</sup>	75 mW			
Forward DC Current	50 mA			
Output Photologic®				
Supply Voltage, V <sub>CC</sub> (not to exceed 3 seconds)	18 V			
Voltage at Output Lead (Open Collector Output)	30 V			
Output Photologic® Power Dissipation <sup>(3)</sup>	90 mW			

#### Notes:

- (1) RMA flux is recommended. Duration can be extended to 10 seconds maximum when flow soldering.
- (2) Derate linearly 1.67 mW/°C above 25°.
- (3) Derate linearly 2.67 mW/°C above 25°.
- (4) Normal application would be with light source blocked, simulated by  $I_F = 0$  mA.
- (5) All parameters tested using pulse technique.



Flectrical	Electrical Characteristics (T <sub>A</sub> = 25° C unless otherwise noted)						
SYMBOL			TYP	MAX	UNITS	TEST CONDITIONS	
Input Diode							
V <sub>F</sub>	Forward Voltage	-	1.3	1.8	V	I <sub>F</sub> = 20 mA	
I <sub>R</sub>	Reverse Current	-	-	100	μΑ	V <sub>R</sub> = 2 V, T <sub>A</sub> = 25° C	
Output Pho	Output Photologic® Sensor						
V <sub>cc</sub>	Operating DC Supply Voltage	4.5	-	16	V	-	
I <sub>CCL</sub>	Low Level Supply Current:  Buffered with 10k pull-up <sup>(1)</sup> Buffered Open-Collector Output <sup>(1)</sup>	-	-	7	mA	$V_{CC} = 16 \text{ V}, I_F = 0 \text{ mA}, \text{ No Output}$ Load	
	Inverted with 10k pull-up: Inverted Open-Collector Output	-	-	7	mA	$V_{CC}$ = 16 V, $I_F$ = 10 mA, No Output Load	
I <sub>CCH</sub>	High Level Supply Current: Buffered with 10k pull-up Buffered Open-Collector Output	-	-	6	mA	$V_{CC}$ = 16 V, $I_F$ = 10 mA, No Output Load	
	Inverted with 10k pull-up: Inverted Open-Collector Output <sup>(1)</sup>	-	-	6	mA	$V_{CC}$ = 16 V, $I_F$ = 0 mA, No Output Load	
V <sub>OL</sub>	Low Level Output Voltage: Buffered with 10k pull-up Buffered Open-Collector Output	-	-	0.4	V	V <sub>CC</sub> = 4.5 V, I <sub>OL</sub> = 16 mA, I <sub>F</sub> = 0 mA	
	Inverted with 10k pull-up: Inverted Open-Collector Output	-	-	0.4	V	V <sub>CC</sub> = 4.5 V, I <sub>OL</sub> = 16 mA, I <sub>F</sub> = 10 mA	
V <sub>OH</sub>	High Level Output Voltage: Buffered with 10k pull-up	V <sub>cc</sub> - 2.0	-	-	V	. $V_{CC} = 4.5 \text{ V to } 16 \text{ V, I}_F = 10 \text{ mA,} \\ I_{OH} = 100  \mu\text{A}$	
	Inverted with 10k pull-up:	V <sub>cc</sub> -	-	-	V	$V_{CC} = 4.5 \text{ V to } 16 \text{ V, I}_F = 0 \text{ mA,}$	
I <sub>ОН</sub>	High Level Output Current:  Buffered with 10k pull-up Buffered Open-Collector Output	-	1.0	10	μΑ	V <sub>CC</sub> = 4.5 V, I <sub>F</sub> = 10 mA, V <sub>OH</sub> = 30 V	
	Inverted with 10k pull-up: Inverted Open-Collector Output <sup>(1)</sup>	-	1.0	10	μΑ	V <sub>CC</sub> = 4.5 V, I <sub>F</sub> = 0 mA, V <sub>OH</sub> = 30 V	

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Electrical Characteristics (T <sub>A</sub> = 25° C unless otherwise noted)							
SYMBOL	MBOL PARAMETER		TYP	MAX	UNITS	TEST CONDITIONS	
Output Photologic® Sensor							
I <sub>F(+)</sub>	LED Positive-Going Threshold Current Buffered with 10k pull-up Inverted with 10k pull-up	_	5	10	mA	V <sub>CC</sub> = 5 V, No Output Load	
	Buffered Open-Collector Output Inverted Open-Collector Output <sup>(1)</sup>	-	5	10	mA	V <sub>CC</sub> = 4.5 V, I <sub>OL</sub> = 16 mA	
I <sub>F(+)/</sub> I <sub>F(-)</sub>	Hysteresis	-	1.5	-	-	V <sub>CC</sub> = 5 V	
$t_r t_f$	Rise Time, Fall Time	-	50	-	ns	$V_{CC} = 5 \text{ V}, I_F = 0 \text{ or } 10 \text{ mA},$ $R_L = 300 \Omega \text{ to } 5 \text{ V}, C_L = 50 \text{ pF}$	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay	-	3	-	μs		

### Notes:

Normal application would be with light source blocked, simulated by I<sub>F</sub> = 0 mA.
 All parameters tested using pulse technique.

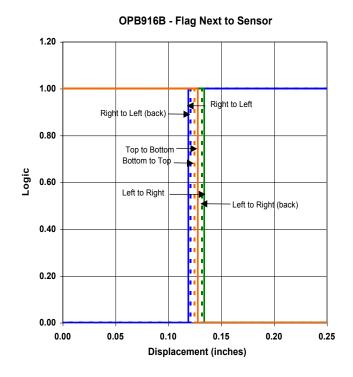


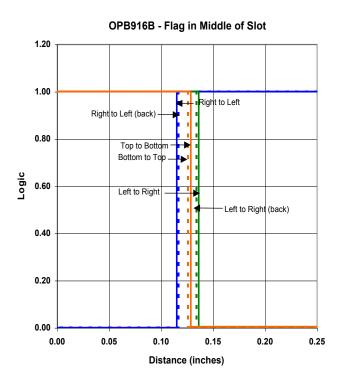
**OPB916B - Flag Next to Emitter** 1.20 1.00 Right to Left (back) 0.80 Top to Bottom Bottom to Top Left to Right Left to Right (back) 0.40 0.20 0.00 0.00 0.05 0.20 0.25

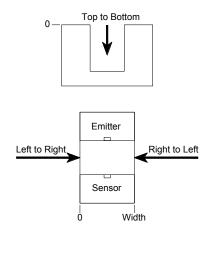
0.10

Displacement (inches)

0.15









Issue	Change Description	Approval	Date
Α	Initial Release, new format	Steve Coble	12/13/06
A.1	Fixed Ordering Table page 1	Bob Procsal	1/25/08
A.2	Fixed nomenclature on schematics page 1	Mark Miller	8/04/08
A.3	Changed mechanical drawing, Absolute Max Ratings and some Electrical Characteristics	Mark Miller	08/07/08
В	Transferred to the new TT Electronics template	L. Timpa	10/6/16

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