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# PowerPC 970 RISC Microprocessor Programming Note

Version 0.1

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**Preliminary**

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## The following steps are strongly recommended when using **larx** and **stcx** instructions:

1. **Larx/Stcx** operations should be done as a pair. Every **larx** executed should have an accompanying **stcx** to clear the reservation.
2. The **stcx** operation should be done in close proximity to the **larx**.
3. In the event of an interrupt occurring before the **stcx**, the operating system (OS) should execute a **stcx** in the interrupt handler to clear the reservation.

**Note:** **larx** - represents **lwarx** and **ldarx**  
**stcx** - represents **stwcx.** and **stdcx.**

Additional information regarding Larx/Stcx instructions and usage can be found in the "PowerPC Microprocessor Family: Programming Environments Manual for 64 and 32-Bit Microprocessors".