

Tejeswini Sundaram

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Education B.Tech. in Computer Science, May 2015
Manipal Institute of Technology, Manipal, India
CGPA **9.43 / 10.00**

Area of Interest Parallel Computing, Computer Architecture, Machine Learning.

Experience **Project Assistant**, HPC Lab, SERC IISc, B'lore. Aug 2015 - Present

- Working in multicore architecture and parallel computing
- Research on accelerated computer vision algorithms on heterogenous compute.

Research Intern, HPC Lab, SERC IISc, B'lore. Jan - Aug 2015.

- Formulated bachelor thesis project titled, "*AlCoViC : Accelerated Computer Vision using Heterogenous Coprocessors*"
- Analyzed performance, execution time, core utilization, and overheads in the choosen OpenCL accelerated OpenCV benchmark algorithms.

Summer Intern, Microsoft, Bangalore. Jun - July 2014.

- Designed and developed the MCS India Delivery Dashboard for customer centric delivery tracking.
- Implemented the web-enable dashboard for information management and actionable decision making using C#, visual studio and metro apps design.

Summer Intern, Tata Consultancy Services, Chennai. Jun - Jul 2013.

- Worked on an internal communication software, using Java Language. Built the front end and linked the database to the backend of the system.

GE Foundation Scholar, GE JFWTC Centre, Bangalore. May - Jun 2013.

- Designed "*Scavenger*", a frugal smart waste management system with an innovative sanitary napkin incinerator and wireless central monitoring system for India buildings.

Winter Schools **IPTSE Winter School**, Carnegie Mellon University. Dec 2014.

- Research on "*Voice Forensics*", a system to identify bodily features and demographic information about a miscreant from the voice evidence collected
- Analyzed audio features such as signal energy, loudness, pitch, MFCC, PLP-CC, voice quality, and formants.
- The Framework developed, using neural networks, consisted of audio feature extraction, machine learning tools, classification and regression algorithms.

I-CARE Winter School, IBM India Research Labs. Oct 2014.

- Studied about the current trends in areas of Deep Learning, Machine Learning and Big Data Analytics.

Publications	<ul style="list-style-type: none"> • <i>Word Existence Algorithm</i>, Tejeswini Sundaram, Vyom Chabbra, International Conference on Computational Methods in Engineering and Health Sciences (ICCMEH), 2014. • <i>Binary Encryption using Based on a Rubiks Cube</i>, Tejeswini Sundaram, Vyom Chabbra, International Conference on Computational Methods in Engineering and Health Sciences (ICCMEH), 2014. 	
Poster Presentation	<ul style="list-style-type: none"> • <i>"Voice Forensics"</i>, Tejeswini Sundaram, Priya Soundarajan, Sakthivel.S, and Utkarsh Pathange at CMU Internship Program in Technology Supported Education (IPTSE) Winter School, held by CMU and NITK Surathkal. 	
Leadership Activities	Program Committee Member , Systems Track, GHCI-2015. Chairperson , IEEE Student Branch Manipal. Judges and Reception Head , Tesseract, IEEE SBM tech-fest. Team Manager , Team Internet, GE Foundation Scholar Leader Program. Technical Committee Member , IEEE SBM. Sports Captain , St Francis Xavier Girls High School. Hostel Representative , SFX Hostels. House Prefect , St Francis Xavier Girls High School.	2015 2014 2014 2013 2012 2009 2009 2008
Honors & Awards	GE Foundation Scholar- Leader Scholarship (GEFSLP) Award 2013-15. AICTE Scholarship, Government of India, 2011-15. Best Project Award at the CMU IPTSE Winter School-2014. Rotary Youth Leadership Awards (RYLA), RI District 3160, 2012.	
Computer Skills	<u>Languages:</u> C, C++, C#, Java, Python, OpenCL, OpenMP, MPI, OpenCV. <u>Software:</u> Git, Vtune Amplifier, Gdb, Visual Studio, Weka, Caffe. <u>Operating Systems:</u> Microsoft Windows, OpenSUSE, Ubuntu, Debian Wheezy.	
References	Will be provided on request.	