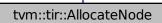


## tvm::tir::StmtNode

- + \_type\_key + \_type\_has\_method\_sequal reduce
- \_reduce + \_type\_has\_method\_shash -reduce

+ TVM DECLARE BASE OBJECT INFO()



- + buffer\_var
- + extents

+ dtype

- + condition
- + body + new expr
- + free\_function
- + \_type\_key
- + VisitAttrs() + SEqualReduce()
- + SHashReduce()
- + constant allocation size()
- + TVM DECLARE FINAL OBJECT INFO()
- + make()
- + constant\_allocation\_size()