MEDIAN of duration							prefetch	matches														
							0						32									
caching	test	scan_type	build	machine	dataset	rows	1	10	100	1000	10000	100000	1	10	100	1000	10000	100000	1	10	100	1000	10000	100000
cached	btree	bitmapscan	master	i5	cycle	1000000	8.0	7.9	8.1	9.8	23.4	99.1	7.8	8.0	8.1	10.0	24.8	105.7	99%	101%	100%	102%	106%	107%
						10000000	7.8	8.1	8.1	9.6	23.6	168.8	8.1	7.9	8.0	9.8	24.7	189.0	104%	98%	99%	102%	105%	112%
						50000000	7.9	7.9	8.3	9.9	23.8	168.0	8.0	7.9	8.1	10.0	25.0	179.8	101%	99%	98%	101%	105%	107%
					random	1000000	7.9	7.9	8.1	9.8	22.5	97.2	7.9	7.9	8.1	10.1	23.4	103.7	100%	101%	101%	103%	104%	107%
						10000000	8.0	7.8	8.2	10.0	23.2	153.7	7.8	7.9	8.1	10.2	24.8	168.8	98%	102%	99%	102%	107%	110%
						50000000	7.9	8.1	8.3	9.9	23.9	166.3	8.0	8.0	8.0	9.9	24.8	176.9	101%	98%	97%	100%	104%	106%
					sequential	1000000	7.8	8.0	7.9	8.5	12.8	54.4	7.9	7.9	7.8	8.7	12.8	55.3	101%	99%	98%	101%	100%	102%
						10000000	7.9	7.9	8.0	8.4	12.9	54.7	8.1	7.9	8.0	8.7	12.9	55.6	102%	100%	100%	103%	100%	102%
						50000000	7.9	8.0	8.0	8.7	12.8	55.9	8.0	7.8	8.0	8.7	13.0	55.8	102%	98%	100%	99%	102%	100%
				xeon	cycle	1000000	9.4	9.5	10.3	12.7	26.4	124.9	9.5	9.8	9.4	11.8	28.0	133.1	101%	103%	92%	93%	106%	107%
						10000000	9.7	9.5	10.2	11.4	28.9	196.5	9.9	9.3	10.3	11.3	27.8	204.5	102%	98%	101%	99%	96%	104%
						100000000	10.6	9.8	10.1	12.0	28.5	206.5	10.0	10.4	10.1	11.9	28.4	215.9	95%	105%	99%	99%	100%	105%
					random	1000000	9.5	9.5	10.4	11.8	25.8	119.8	9.0	9.9	10.3	11.6	26.7	126.8	95%	104%	99%	98%	104%	106%
						10000000	9.8	9.3	9.1	11.3	27.2	170.2	10.4	10.2	10.3	11.3	27.7	187.2	107%	110%	114%	100%	102%	110%
						100000000	10.0	9.4	10.2	12.5	28.9	185.5	9.7	10.3	10.2	12.0	27.8	213.8	98%	110%	100%	96%	96%	115%
					sequential	1000000	10.4	10.2	9.8	10.5	15.3	65.5	9.9	10.2	10.0	10.4	15.6	65.5	96%	100%	103%	99%	102%	100%
						10000000	10.7	9.5	9.3	9.9	16.1	65.6	9.9	9.9	10.0	10.6	15.8	63.3	93%	104%	108%	107%	98%	96%
						100000000	9.8	9.8	9.8	11.2	15.6	65.5	9.5	9.5	9.6	10.8	14.9	65.6	97%	97%	98%	96%	95%	100%
			patched	i5	cycle	1000000	7.9	7.9	8.2	9.8	23.6	99.8	7.9	7.9	8.1	9.9	24.6	105.4	99%	100%	98%	101%	104%	106%
						10000000	7.8	7.9	8.1	9.8	23.5	169.0	7.9	7.9	8.1	9.8	24.7	187.8	101%	101%	101%	101%	105%	111%
						50000000	7.9	8.0	8.2	9.9	24.1	169.0	8.0	8.0	8.2	10.1	24.9	179.3	101%	101%	100%	103%	103%	106%
					random	1000000	8.0	8.0	8.1	9.7	22.2	96.2	8.0	7.9	8.2	10.1	23.7	103.7	100%	99%	101%	104%	106%	108%
						10000000	7.8	7.9	8.2	9.9	23.8	152.4	8.0	7.9	8.1	10.2	24.9	165.8	102%	100%	99%	103%	105%	109%
						50000000	8.0	8.1	8.3	10.1	23.7	166.7	7.9	8.0	8.3	9.9	25.0	176.8	99%	99%	100%	99%	105%	106%
					sequential	1000000	8.0	8.0	7.9	8.6	12.9	53.6	8.0	7.9	8.0	8.7	12.9	54.6	100%	99%	101%	101%	100%	102%
						10000000	7.9	7.9	7.9	8.6	12.9	54.3	7.9	7.9	8.1	8.6	12.9	54.9	100%	101%	102%	101%	100%	101%
						50000000	8.0	8.1	8.0	8.7	13.0	54.5	7.8	8.0	8.1	8.7	13.1	55.2	98%	99%	100%	101%	100%	101%
				xeon	cycle	1000000	10.1	9.5	10.1	12.3	26.7	113.3	9.9	9.3	10.2	11.4	27.7	132.5	98%	98%	102%	92%	104%	117%
						10000000	9.6	9.7	9.9	11.9	29.5	196.6	9.6	9.5	10.7	11.2	27.6	227.9	100%	98%	109%	94%	93%	116%
						100000000	9.6	9.8	10.4	12.8	27.7	209.4	10.3	10.1	10.1	12.8	27.5	216.6	107%	103%	98%	100%	99%	103%
					random	1000000	9.7	9.9	10.6	11.4	26.3	110.1	9.9	10.0	9.8	11.3	26.3	127.2	102%	101%	93%	99%	100%	115%
						10000000	10.4	10.3	9.7	12.8	27.8	187.5	9.8	9.9	10.1	12.4	27.3	201.3	94%	96%	104%	97%	98%	107%
						100000000	10.0	9.7	10.1	11.6	29.0	199.0	10.0	9.4	9.8	10.9	28.0	213.6	100%	97%	97%	94%	97%	107%
					sequential	1000000	10.3	10.3	10.3	10.0	16.0	64.4	10.3	10.2	10.4	10.0	15.5	65.9	101%	99%	100%	100%	97%	102%
						10000000	10.2	9.9	10.1	10.0	15.5	66.3	9.6	10.1	9.7	9.6	15.8	66.5	94%	103%	96%	96%	101%	100%
						100000000	9.6	10.1	10.3	11.0	15.6	67.1	10.2	9.8	10.3	10.9	15.4	67.6	105%	97%	99%	99%	99%	101%
		indexscan	master	i5	cycle	1000000	7.9	7.9	8.2	9.6	20.4	83.6	7.9	7.9	8.1	9.4	20.4	84.6	100%	100%	99%	99%	100%	101%
						10000000	7.9	8.0	8.1	9.4	20.3	123.8	7.9	7.9	8.2	9.3	20.3	124.9	99%	98%	102%	99%	100%	101%
						50000000	8.0	7.9	8.2	9.6	20.5	123.8	7.9	8.0	8.2	9.6	20.5	124.0	99%	101%	100%	100%	100%	100%
					random	1000000	7.9	8.0	8.1	9.5	19.4	80.9	8.0	7.8	8.0	9.4	19.3	81.6	100%	98%	100%	98%	100%	101%
						10000000	7.9	8.0	8.1	9.8	20.6	113.2	7.8	8.0	8.0	9.8	20.3	113.4	99%	101%	99%	100%	98%	100%
						50000000	8.1	7.8	8.2	9.6	20.5	122.0	7.9	8.0	8.0	9.8	20.5	122.0	98%	103%	98%	101%	100%	100%
					sequential	1000000	7.9	7.9	8.0	8.7	13.1	54.9	7.9	7.7	7.9	8.5	13.0	54.9	101%	98%	99%	98%	99%	100%
						10000000	7.9	8.0	7.9	8.4	13.0	55.4	8.0	8.1	8.0	8.5	13.0	55.2	101%	102%	101%	101%	100%	100%
						50000000	8.0	8.0	8.1	8.7	13.3	55.8	8.0	7.9	8.1	8.6	13.1	55.6	100%	99%	100%	98%	99%	100%
				xeon	cycle	1000000	9.3	9.4	9.8	12.1	23.0	106.7	9.4	9.2	9.6	11.3	22.3	106.7	101%	97%	98%	93%	97%	100%
						10000000	9.9	9.7	9.9	11.0	23.9	140.4	9.9	9.1	10.2	10.4	24.2	141.1	100%	94%	103%	94%	101%	100%
						100000000	9.9	9.4	9.6	11.4	24.2	159.0	10.0	10.0	10.4	11.2	25.2	161.0	101%	105%	108%	98%	104%	101%
					random	1000000	9.4	9.4	10.3	10.8	22.3	101.9	9.4	9.7	10.2	11.1	22.0	100.8	100%	104%	99%	103%	99%	99%

					10000000	9.2	9.7	9.2	11.0	24.0	130.5	10.0	10.1	9.9	11.3	23.4	145.8	109%	104%	108%	102%	97%	112%
					100000000	10.2	8.9	9.1	12.3	25.4	147.4	10.0	9.7	9.8	11.9	24.0	154.2	98%	109%	107%	97%	94%	105%
				sequential	1000000	10.1	10.2	9.7	10.5	15.4	65.8	10.3	10.0	9.8	10.4	15.6	63.8	102%	98%	101%	99%	101%	97%
					10000000	10.4	9.2	9.1	9.8	15.2	67.0	9.9	9.6	10.0	10.3	15.4	66.7	95%	105%	109%	105%	102%	100%
					100000000	8.9	10.2	9.6	10.4	15.1	67.5	9.2	9.4	9.8	10.6	15.1	65.7	103%	92%	102%	102%	100%	97%
		patched	i5	cycle	1000000	7.8	7.8	8.1	9.4	20.2	82.6	8.1	8.0	8.1	9.5	20.8	86.8	104%	101%	100%	101%	103%	105%
					10000000	8.0	8.0	8.0	9.3	20.1	122.9	7.8	7.9	8.1	9.5	21.1	128.3	98%	99%	100%	102%	105%	104%
					50000000	7.9	8.0	8.2	9.7	20.3	122.4	8.0	8.1	8.1	9.6	21.0	129.4	100%	102%	99%	98%	104%	106%
				random	1000000	8.0	7.9	8.1	9.4	19.0	80.9	8.0	7.8	8.1	9.6	20.2	86.9	101%	99%	100%	102%	106%	107%
					10000000	7.9	8.0	8.2	9.6	20.2	111.7	7.8	7.8	8.1	9.9	21.2	120.6	100%	98%	99%	103%	105%	108%
					50000000	8.2	8.0	8.2	9.7	20.1	121.4	7.9	8.0	8.0	9.7	21.4	127.6	96%	100%	99%	100%	106%	105%
				sequential	1000000	7.8	7.8	8.0	8.7	12.9	55.9	8.0	7.9	8.0	8.6	13.2	56.5	102%	102%	100%	99%	102%	101%
					10000000	7.8	8.1	8.1	8.5	12.9	54.9	7.8	7.8	8.1	8.5	13.3	57.0	100%	97%	100%	101%	103%	104%
					50000000	8.1	7.9	8.0	8.8	13.3	55.5	7.9	7.9	8.1	8.9	13.5	58.6	98%	100%	101%	100%	101%	106%
			xeon	cycle	1000000	9.8	9.3	10.6	11.6	22.6	96.2	9.8	9.1	10.1	10.7	23.4	105.4	100%	98%	95%	93%	104%	110%
					10000000	9.9	9.7	10.1	11.0	26.0	141.3	10.1	9.3	10.4	10.5	23.4	166.9	102%	96%	103%	95%	90%	118%
					100000000	10.2	9.7	10.3	12.3	24.5	161.0	9.8	10.1	10.0	12.4	23.9	168.5	96%	105%	97%	101%	98%	105%
				random	1000000	9.6	9.7	10.2	10.6	22.4	98.2	9.8	9.5	9.6	10.5	22.8	109.5	102%	98%	95%	99%	102%	112%
					10000000	10.0	10.0	9.6	12.4	24.1	146.6	10.1	9.7	10.0	12.0	23.0	153.5	101%	96%	104%	97%	95%	105%
					100000000	9.7	9.5	9.8	11.2	24.9	149.7	10.1	9.3	9.6	10.3	23.5	166.0	105%	97%	97%	92%	94%	111%
				sequential	1000000	10.1	10.1	10.3	10.0	15.7	66.3	10.1	10.3	10.7	10.2	15.5	69.6	100%	102%	104%	102%	99%	105%
					10000000	9.8	9.8	9.7	10.4	15.3	67.2	9.4	10.1	9.4	9.3	16.0	69.6	95%	104%	96%	90%	104%	104%
					100000000	9.3	9.3	9.9	10.3	15.8	67.3	10.1	9.6	10.7	11.4	15.9	70.1	108%	103%	109%	111%	101%	104%
	seqscan	master	i5	cycle	1000000	139.7	139.9	139.4	141.7	146.2	139.8	139.3	138.4	140.5	140.7	146.0	183.0	100%	99%	101%	99%	100%	131%
					10000000	1303.3	1305.6	1299.0	1305.5	1312.7	1371.3	1305.2	1317.8	1296.7	1312.1	1325.2	1371.6	100%	101%	100%	101%	101%	100%
					50000000	15397.2	15287.0	15330.3	16027.6	16078.0	16146.6	15354.8	15340.9	15330.3	16467.1	15302.4	15406.6	100%	100%	100%	103%	95%	95%
				random	1000000	139.4	141.0	139.5	142.0	147.0	179.7	139.4	139.3	140.2	141.6	146.6	161.2	100%	99%	101%	100%	100%	90%
					10000000	1298.9	1300.2	1299.4	1311.1	1320.3	1365.6	1322.8	1306.1	1310.6	1299.5	1307.6	1358.4	102%	100%	101%	99%	99%	99%
					50000000	15298.1	15339.0	15304.9	16172.8	16168.6	15500.8	16044.3	15915.6	15331.7	15368.2	15670.0	17120.2	105%	104%	100%	95%	97%	110%
				sequential	1000000	140.1	141.7	139.7	140.5	145.1	175.9	140.1	138.8	140.4	139.6	143.2	174.7	100%	98%	101%	99%	99%	99%
					10000000	1299.0	1314.1	1321.5	1332.2	1309.1	1339.9	1304.8	1313.3	1303.0	1318.1	1304.3	1382.0	100%	100%	99%	99%	100%	103%
					50000000	15344.7	15300.3	15301.2	15366.8	15816.0	16536.7	15332.3	15330.3	15870.2	15291.0	15323.6	15406.9	100%	100%	104%	100%	97%	93%
			xeon	cycle	1000000	159.4	160.5	161.8	164.9	167.6	213.4	160.1	160.5	163.4	163.7	162.3	164.5	100%	100%	101%	99%	97%	77%
					10000000	1476.8	1479.6	1468.5	1474.4	1483.0	1531.5	1497.3	1464.4	1513.0	1516.6	1521.3	1544.4	101%	99%	103%	103%	103%	101%
					100000000	16207.8	14988.3	14675.8	16065.6	14690.0	15249.8	14942.4	15006.4	15025.0	14681.0	14733.6	15142.6	92%	100%	102%	91%	100%	99%
				random	1000000	161.2	159.3	162.0	161.2	165.6	213.4	158.5	162.0	165.4	159.4	165.3	215.4	98%	102%	102%	99%	100%	101%
					10000000	1487.6	1472.4	1471.7	1467.6	1512.7	1551.1	1485.2	1494.7	1487.9	1468.6	1482.0	1548.4	100%	102%	101%	100%	98%	100%
					100000000	14880.6	14863.2	16151.8	16173.6	16013.5	15049.5	14887.7	14783.5	14861.8	14817.7	14712.1	14979.7	100%	99%	92%	92%	92%	100%
				sequential	1000000	163.2	161.5	159.3	162.2	163.1	159.9	159.6	163.0	160.4	160.6	165.8	158.9	98%	101%	101%	99%	102%	99%
					10000000	1471.3	1501.4	1468.6	1486.1	1534.0	1509.1	1529.1	1503.0	1481.0	1468.0	1521.5	1516.1	104%	100%	101%	99%	99%	100%
					100000000	16292.0	15192.2	16140.6	15025.3	14928.3	14804.7	14710.1	16206.2	14700.1	15171.3	14865.7	14787.9	90%	107%	91%	101%	100%	100%
		patched	i5	cycle	1000000	140.7	140.2	139.1	141.5	146.6	181.2	139.1	139.8	140.1	141.6	146.7	183.7	99%	100%	101%	100%	100%	101%
					10000000	1313.0	1309.8	1312.1	1299.6	1331.8	1373.8	1305.0	1301.4	1332.6	1300.7	1334.2	1373.0	99%	99%	102%	100%	100%	100%
					50000000	15295.1	15804.4	15352.1	15304.2	15797.8	15366.2	15339.8	15308.5	15425.4	16370.6	15343.5	15383.9	100%	97%	100%	107%	97%	100%
				random	1000000	139.5	141.4	142.5	141.5	149.8	182.6	139.6	139.9	139.1	140.5	146.8	162.4	100%	99%	98%	99%	98%	89%
					10000000	1304.4	1298.8	1302.3	1305.6	1314.4	1360.3	1306.1	1304.4	1296.1	1299.1	1330.4	1367.2	100%	100%	100%	100%	101%	101%
					50000000	15364.0	15303.8	15306.5	15324.0	15258.7	15378.5	15356.9	15321.4	15649.6	15252.0	15281.2	15302.6	100%	100%	102%	100%	100%	100%
				sequential	1000000	141.1	140.6	140.6	140.7	143.8	174.2	139.7	139.4	140.0	140.8	142.9	173.9	99%	99%	100%	100%	99%	100%
					10000000	1316.0	1302.3	1296.7	1306.7	1302.7	1339.7	1301.2	1303.6	1296.9	1304.7	1302.9	1333.1	99%	100%	100%	100%	100%	100%
					50000000	15322.6	15325.3	16317.9	16518.7	15293.5	15405.9	16119.4	15233.3	15313.1	15354.4	15323.4	15410.4	105%	99%	94%	93%	100%	100%
			xeon	cycle	1000000	163.3	160.9	161.8	166.7	173.0	167.4	162.6	161.2	162.7	164.3	173.4	163.5	100%	100%	101%	99%	100%	98%
					10000000	1486.7	1487.3	1523.0	1497.2	1516.9	1584.5	1486.0	1496.5	1518.1	1513.7	1517.1	1574.7	100%	101%	100%	101%	100%	99%

						100000000	15287.3	15131.8	15130.6	15193.3	14965.5	15279.0	15280.0	14972.4	15108.4	15295.6	14927.6	15355.3	100%	99%	100%	101%	100%	100%
					random	1000000	162.9	162.5	164.8	167.3	167.6	216.4	164.9	163.1	161.3	165.8	171.0	226.3	101%	100%	98%	99%	102%	105%
						10000000	1512.8	1506.9	1505.9	1497.4	1527.8	1562.9	1514.5	1509.0	1511.4	1505.1	1508.4	1577.9	100%	100%	100%	101%	99%	101%
						100000000	15082.9	15101.3	15092.8	15018.6	14936.5	15258.5	14916.3	14920.7	15216.5	14929.1	15235.4	15248.4	99%	99%	101%	99%	102%	100%
					sequential	1000000	162.3	161.7	163.1	161.2	167.1	201.3	163.1	165.6	163.8	164.2	165.7	201.7	101%	102%	100%	102%	99%	100%
						10000000	1504.2	1494.4	1486.9	1520.9	1503.3	1551.8	1486.3	1515.1	1514.8	1495.6	1506.7	1528.7	99%	101%	102%	98%	100%	99%
						100000000	14918.2	14920.5	14954.8	15155.4	14983.6	15043.6	14868.4	14965.8	14920.9	15382.6	15099.7	14969.0	100%	100%	100%	101%	101%	100%
	btree-sort	bitmapscan	master	i5	cycle	1000000	85.1	74.7	82.5	84.1	126.5	234.6	80.2	80.1	100.4	89.4	109.5	186.2	94%	107%	122%	106%	86%	79%
						10000000	1613.3	1568.8	1339.0	1362.9	1297.7	2215.9	1673.0	1663.8	1540.4	1587.1	1861.9	2348.1	104%	106%	115%	116%	143%	106%
						50000000	14500.5	11312.6	11579.4	16505.5	21357.6	24353.7	15849.6	20547.2	19727.1	16114.5	18107.3	18112.0	109%	182%	170%	98%	85%	74%
					random	1000000	142.0	120.1	120.9	122.7	130.1	229.9	142.3	144.6	145.2	137.4	149.0	242.0	100%	120%	120%	112%	115%	105%
						10000000	2477.9	2366.5	2268.5	2351.8	2340.5	2445.5	2805.9	2513.5	2596.9	2622.1	2529.6	2821.8	113%	106%	114%	111%	108%	115%
						50000000	19254.3	18533.4	18106.2	18105.4	17535.9	17966.7	34575.2	34470.3	35090.0	34458.7	34821.6	34471.6	180%	186%	194%	190%	199%	192%
					sequential	1000000	102.3	82.8	82.0	99.6	93.9	119.4	112.4	101.6	78.7	80.4	102.4	137.3	110%	123%	96%	81%	109%	115%
						10000000	1580.0	1395.6	1328.8	1576.8	1440.3	1521.2	1394.3	1438.4	1584.0	1533.1	1432.2	1674.7	88%	103%	119%	97%	99%	110%
						50000000	13606.2	14208.0	15383.9	14469.4	11071.4	15078.8	16932.4	17919.1	19103.2	19687.4	13877.9	15994.4	124%	126%	124%	136%	125%	106%
				xeon	cycle	1000000	100.4	99.7	99.4	99.2	142.7	266.1	99.1	105.7	87.1	122.1	158.9	218.0	99%	106%	88%	123%	111%	82%
						10000000	1957.4	1801.6	1762.1	1909.1	2216.7	2393.0	1724.4	1619.7	1893.1	2202.4	1997.3	2556.8	88%	90%	107%	115%	90%	107%
						100000000	14879.1	17214.9	12864.0	16707.8	15610.9	18125.4	17408.0	15255.0	19779.7	17298.1	20169.0	18311.8	117%	89%	154%	104%	129%	101%
					random	1000000	166.0	146.7	151.4	172.9	168.7	272.0	154.2	152.4	163.1	177.9	132.0	208.7	93%	104%	108%	103%	78%	77%
						10000000	3116.5	2825.2	2853.8	2906.1	2914.5	3042.6	3144.1	3104.5	3161.6	3089.6	3211.6	3299.7	101%	110%	111%	106%	110%	108%
						100000000	32058.5	30213.3	30769.5	29813.6	29620.2	28977.0	32857.9	32587.8	32218.5	31850.2	32404.4	32824.1	102%	108%	105%	107%	109%	113%
					sequential	1000000	96.4	110.5	112.4	92.7	108.2	209.5	103.2	116.8	88.1	86.5	126.5	206.0	107%	106%	78%	93%	117%	98%
						10000000	2020.7	1744.0	1908.5	1736.1	1426.0	1883.6	1959.6	1823.7	1842.0	2126.6	1749.9	1743.6	97%	105%	97%	122%	123%	93%
						100000000	14650.2	13141.7	18000.0	18580.0	14064.6	12577.7	16800.3	15426.0	14609.6	19205.1	16172.4	14545.7	115%	117%	81%	103%	115%	116%
			patched	i5	cycle	1000000	87.1	101.6	100.1	109.8	131.2	197.4	91.1	92.9	81.9	112.9	117.8	224.5	105%	91%	82%	103%	90%	114%
						10000000	1599.5	1176.9	1265.5	1702.6	1372.4	1969.5	1788.6	1601.1	1462.0	1617.6	1796.6	2590.0	112%	136%	116%	95%	131%	132%
						50000000	15158.6	15252.7	14197.2	16198.3	21560.8	24148.7	17102.3	13985.1	17947.9	16829.9	19400.4	20408.7	113%	92%	126%	104%	90%	85%
					random	1000000	139.8	137.9	121.6	137.0	139.0	242.9	138.7	138.3	129.1	140.8	159.5	238.4	99%	100%	106%	103%	115%	98%
						10000000	2550.6	2370.9	2341.7	2440.3	2271.8	2671.5	2611.9	2702.2	2593.4	2649.2	2526.3	2783.4	102%	114%	111%	109%	111%	104%
						50000000	18325.9	18412.6	18182.3	17771.1	17743.5	18110.0	35613.7	35071.4	34601.1	34134.9	34848.4	35235.6	194%	190%	190%	192%	196%	195%
					sequential	1000000	100.9	74.9	82.3	93.0	104.0	133.2	83.5	91.2	79.8	106.1	87.0	122.6	83%	122%	97%	114%	84%	92%
						10000000	1558.9	1285.5	1572.9	1529.5	1459.2	1477.0	1872.9	1278.5	1636.9	1419.6	1640.2	1450.0	120%	99%	104%	93%	112%	98%
						50000000	13254.6	13554.7	11106.4	10991.9	15206.7	11519.0	19630.8	16129.2	16671.3	15527.4	17747.2	13723.7	148%	119%	150%	141%	117%	119%
				xeon	cycle	1000000	107.7	118.1	108.9	94.1	168.4	280.9	132.8	115.7	104.1	105.6	163.3	266.1	123%	98%	96%	112%	97%	95%
						10000000	1851.0	1627.5	1928.1	1978.5	2111.2	2798.2	2253.0	1632.4	2254.0	2176.7	1937.3	2817.5	122%	100%	117%	110%	92%	101%
						100000000	13674.8	13900.1	17138.6	20953.2	15847.8	19925.8	18635.6	14897.1	19456.2	15726.1	21406.8	17420.0	136%	107%	114%	75%	135%	87%
					random	1000000	155.5	155.6	166.9	156.9	156.7	292.4	180.8	166.3	180.7	171.4	158.6	262.0	116%	107%	108%	109%	101%	90%
						10000000	2927.0	2756.3	2724.4	2898.2	2988.5	2993.2	3400.7	3141.8	3184.6	3299.8	3189.7	3365.8	116%	114%	117%	114%	107%	112%
						100000000	30255.6	30514.1	29736.6	30497.6	30229.1	29737.8	33647.1	32699.8	32344.9	32340.2	31945.2	32774.2	111%	107%	109%	106%	106%	110%
					sequential	1000000	112.5	96.2	85.7	94.7	106.5	181.5	137.4	86.2	112.3	112.0	100.5	171.9	122%	90%	131%	118%	94%	95%
						10000000	1487.8	1622.2	1897.5	1570.4	1876.4	1962.9	2198.4	1646.0	1729.6	1851.9	1545.5	2191.3	148%	101%	91%	118%	82%	112%
						100000000	14338.7	16655.7	18299.3	16470.1	16740.4	14893.1	18621.2	19279.0	17093.5	19196.3	13560.6	17302.9	130%	116%	93%	117%	81%	116%
		indexscan	master	i5	cycle	1000000	7.9	8.0	8.2	10.0	20.5	86.0	8.0	8.0	8.2	9.9	20.6	84.7	101%	101%	101%	100%	100%	99%
						10000000	8.0	8.0	8.2	10.3	20.4	125.5	8.0	7.9	8.3	10.2	20.5	124.9	100%	98%	101%	99%	100%	100%
						50000000	8.1	8.2	8.3	9.6	20.7	128.9	8.3	8.3	8.3	9.5	20.8	125.8	102%	100%	100%	100%	100%	98%
					random	1000000	8.0	8.0	8.2	9.6	19.8	82.4	8.0	8.1	8.2	9.6	19.3	82.4	100%	101%	100%	100%	98%	100%
						10000000	8.0	8.0	8.2	9.4	20.6	115.7	8.0	8.0	8.2	9.4	20.4	114.6	100%	101%	100%	100%	99%	99%
						50000000	8.4	8.2	8.4	10.1	21.0	123.7	8.2	8.1	8.4	10.0	20.9	123.5	97%	100%	101%	98%	100%	100%
					sequential	1000000	8.0	8.0	8.1	9.2	13.3	56.6	8.0	7.9	8.1	9.2	13.0	57.2	100%	99%	100%	100%	97%	101%
						10000000	8.0	8.0	8.2	9.4	13.2	56.3	8.0	8.0	8.0	9.3	13.1	57.0	100%	100%	98%	99%	99%	101%
						50000000	8.5	8.1	8.2	8.7	13.6	57.6	8.2	8.1	8.4	8.7	13.4	57.8	97%	99%	102%	100%	99%	100%

				xeon	cycle	1000000	9.3	9.4	9.7	11.0	23.5	105.0	9.4	9.5	9.7	11.8	22.5	107.2	102%	101%	100%	107%	96%	102%
						10000000	9.1	9.2	9.2	10.5	24.0	156.2	9.1	9.2	9.4	10.9	24.7	156.0	100%	100%	102%	103%	103%	100%
						100000000	9.3	9.2	9.7	10.8	23.3	163.3	9.3	9.1	9.4	10.6	24.6	161.4	100%	99%	98%	98%	105%	99%
					random	1000000	9.1	9.2	9.7	10.7	22.4	103.2	9.3	9.4	9.9	11.0	22.4	100.0	102%	102%	102%	103%	100%	97%
						10000000	9.1	9.2	9.2	10.4	23.4	131.1	9.0	9.1	9.1	10.4	23.7	131.3	99%	99%	99%	100%	101%	100%
						100000000	9.1	9.3	9.5	10.7	24.9	157.8	9.1	9.2	9.3	10.8	24.7	156.4	100%	99%	98%	101%	99%	99%
					sequential	1000000	9.3	9.0	9.6	10.1	15.4	68.6	9.4	9.1	9.6	10.0	15.2	67.8	101%	100%	100%	99%	99%	99%
						10000000	9.1	9.0	9.2	9.8	14.8	67.9	9.3	9.1	9.2	10.0	15.0	67.7	102%	101%	100%	102%	101%	100%
						100000000	9.3	9.2	9.4	9.8	15.2	66.0	9.3	9.0	9.5	9.9	15.1	68.1	100%	98%	101%	100%	100%	103%
			patched	i5	cycle	1000000	8.0	8.1	8.3	10.0	20.3	83.8	8.1	8.0	8.3	10.1	21.1	89.5	101%	100%	100%	101%	104%	107%
						10000000	8.1	8.1	8.3	10.3	20.4	123.5	8.0	8.1	8.3	10.3	22.3	130.0	98%	100%	99%	100%	110%	105%
						50000000	8.4	8.4	8.2	9.6	20.7	123.3	8.5	8.4	8.5	9.6	21.3	131.9	101%	100%	104%	100%	103%	107%
					random	1000000	8.0	8.2	8.2	9.7	19.3	81.9	8.1	8.1	8.2	9.6	20.4	88.1	101%	99%	100%	100%	106%	108%
						10000000	8.2	8.0	8.2	9.7	20.4	112.6	8.0	8.2	8.3	9.8	20.9	122.5	98%	102%	101%	102%	103%	109%
						50000000	8.3	8.2	8.5	10.0	20.8	121.6	8.6	8.3	8.5	10.1	21.4	130.1	103%	102%	100%	101%	103%	107%
					sequential	1000000	8.1	8.1	8.2	9.1	13.2	55.6	8.0	8.1	8.1	9.2	13.2	57.9	99%	101%	100%	101%	100%	104%
						10000000	8.1	7.9	8.1	9.4	13.2	56.7	8.0	8.1	8.1	9.4	13.4	58.1	99%	103%	100%	100%	102%	102%
						50000000	8.0	8.4	8.1	8.6	13.3	57.2	8.2	8.4	8.3	8.8	13.6	58.7	102%	101%	102%	102%	102%	103%
				xeon	cycle	1000000	9.4	9.2	9.7	11.3	23.8	108.4	9.2	9.2	9.4	11.1	24.6	113.7	98%	99%	97%	99%	104%	105%
						10000000	9.1	9.3	9.4	10.6	24.1	156.1	9.0	9.5	9.4	10.4	23.9	168.7	99%	102%	100%	98%	99%	108%
						100000000	9.0	9.2	9.3	10.8	24.1	162.0	9.0	9.2	9.4	10.9	24.8	170.0	100%	100%	101%	100%	103%	105%
					random	1000000	9.4	9.3	9.6	11.2	22.6	101.8	9.3	9.2	9.6	10.8	22.6	108.7	100%	100%	100%	97%	100%	107%
						10000000	9.0	9.2	9.3	10.6	23.1	132.7	8.9	9.1	9.4	10.2	24.6	159.2	99%	99%	100%	96%	107%	120%
						100000000	9.0	9.2	9.5	11.0	25.0	158.5	9.3	9.2	9.1	10.8	24.6	168.9	103%	100%	96%	98%	98%	107%
					sequential	1000000	9.4	9.2	9.5	10.2	15.4	68.0	9.2	9.3	9.3	10.2	15.9	70.2	97%	101%	98%	100%	104%	103%
						10000000	9.1	9.4	9.3	9.9	15.2	68.7	9.0	9.2	9.0	9.9	15.3	70.7	99%	99%	97%	99%	101%	103%
						100000000	9.1	9.1	9.4	9.8	15.1	68.4	9.2	9.2	9.2	9.9	15.2	71.8	101%	101%	99%	101%	101%	105%
	seqscan		master	i5	cycle	1000000	178.4	175.4	180.2	183.4	197.3	304.8	175.7	173.2	181.3	181.6	192.6	304.5	98%	99%	101%	99%	98%	100%
						10000000	1702.8	1708.1	1686.4	1692.6	1709.2	1919.6	1642.0	1705.9	1671.7	1663.8	1708.8	1870.1	96%	100%	99%	98%	100%	97%
						50000000	15712.4	15724.3	15604.8	16862.6	16900.7	15784.2	16798.5	15584.5	15578.0	15707.6	15703.3	15784.2	107%	99%	100%	93%	93%	100%
					random	1000000	177.0	178.4	177.6	185.8	202.4	311.2	177.3	176.2	177.7	181.4	200.4	295.9	100%	99%	100%	98%	99%	95%
						10000000	1668.3	1672.6	1683.7	1670.4	1699.3	1910.8	1672.2	1701.1	1678.2	1706.6	1741.2	1897.1	100%	102%	100%	102%	102%	99%
						50000000	15687.9	16525.8	15681.0	15622.6	16110.0	15861.6	16572.9	15642.0	18598.9	16668.3	15746.4	15841.3	106%	95%	119%	107%	98%	100%
					sequential	1000000	174.3	174.9	177.9	177.2	185.8	229.8	176.7	180.7	174.2	178.2	182.5	231.6	101%	103%	98%	101%	98%	101%
						10000000	1649.9	1709.4	1680.3	1688.7	1700.6	1720.8	1686.9	1692.3	1698.3	1691.8	1656.9	1748.3	102%	99%	101%	100%	97%	102%
						50000000	15759.6	15556.5	15655.7	15562.4	15664.1	15664.1	15657.5	15549.3	16708.7	16611.7	15607.2	15730.4	99%	100%	107%	107%	100%	100%
				xeon	cycle	1000000	199.1	200.6	199.4	199.1	221.4	330.3	199.2	199.4	204.6	202.0	226.0	333.5	100%	99%	103%	101%	102%	101%
						10000000	1856.1	1877.8	1870.0	1931.1	1903.6	2175.3	1878.3	1845.3	1906.1	1922.8	1953.8	2177.6	101%	98%	102%	100%	103%	100%
						100000000	18793.2	19177.2	18941.2	18776.3	19074.6	19203.2	18850.0	18623.2	18763.3	18526.6	19434.9	19535.1	100%	97%	99%	99%	102%	102%
					random	1000000	206.8	205.8	201.6	202.6	222.6	332.8	201.8	203.4	202.3	205.6	223.7	331.6	98%	99%	100%	101%	101%	100%
						10000000	1934.4	1885.6	1894.4	1915.1	1934.1	2169.4	1892.2	1886.2	1909.7	1920.0	1939.6	2197.1	98%	100%	101%	100%	100%	101%
						100000000	19244.7	19890.9	18917.4	19001.8	18884.4	19771.5	18855.6	18953.1	19218.7	19123.6	19283.1	19518.1	98%	95%	102%	101%	102%	99%
					sequential	1000000	204.0	198.8	200.7	202.0	202.5	260.0	203.2	206.3	199.9	197.0	209.4	258.3	100%	104%	100%	98%	103%	99%
						10000000	1898.8	1929.4	1886.1	1909.7	1906.4	1925.6	1860.5	1928.3	1881.0	1891.5	1914.9	1949.7	98%	100%	100%	99%	100%	101%
						100000000	18764.5	19739.2	18869.8	18974.4	19561.0	19165.6	18658.1	18405.1	18758.6	19029.9	18801.6	19083.4	99%	93%	99%	100%	96%	100%
			patched	i5	cycle	1000000	176.7	176.0	183.3	183.2	197.0	304.9	179.9	181.9	179.3	182.3	200.8	303.1	102%	103%	98%	99%	102%	99%
						10000000	1665.8	1696.7	1683.0	1655.8	1715.0	1965.8	1717.1	1720.4	1667.6	1693.7	1758.3	1930.6	103%	101%	99%	102%	103%	98%
						50000000	15717.6	15575.2	15707.1	15684.5	15659.3	16994.8	15689.5	15567.0	15671.3	15672.8	15753.2	15879.5	100%	100%	100%	100%	101%	93%
					random	1000000	180.8	181.4	180.9	182.1	198.5	308.8	181.3	182.0	180.2	183.9	196.5	297.7	100%	100%	100%	101%	99%	96%
						10000000	1666.8	1721.8	1682.7	1729.7	1717.8	1939.2	1717.4	1708.5	1710.9	1699.0	1756.7	1938.7	103%	99%	102%	98%	102%	100%
						50000000	15697.6	16513.9	15648.3	16199.3	15736.6	15884.4	15978.1	15620.1	16323.3	16145.2	15659.9	15805.9	102%	95%	104%	100%	100%	100%
					sequential	1000000	179.7	175.2	182.1	187.6	184.3	230.7	176.7	180.9	175.4	183.3	180.1	230.1	98%	103%	96%	98%	98%	100%

						10000000	1690.3	1736.1	1713.0	1690.2	1681.3	1759.0	1726.0	1691.9	1718.2	1653.2	1671.6	1716.0	102%	97%	100%	98%	99%	98%
						50000000	15658.9	15784.5	16672.8	15621.1	15702.5	15839.3	16399.6	15774.0	16135.6	15727.1	15930.9	15880.9	105%	100%	97%	101%	101%	100%
				xeon	cycle	1000000	204.7	203.0	204.0	210.0	221.4	334.8	199.4	207.2	208.8	207.7	225.4	341.7	97%	102%	102%	99%	102%	102%
						10000000	1938.7	1947.3	1932.6	1913.8	1975.9	2176.0	1903.1	1979.5	1922.3	1968.5	1918.8	2164.7	98%	102%	99%	103%	97%	99%
						100000000	19690.4	19405.1	18928.6	19465.1	19525.5	19669.3	19315.2	19381.4	19400.9	19424.5	19765.9	19273.2	98%	100%	102%	100%	101%	98%
					random	1000000	206.3	207.6	206.7	211.3	227.5	333.8	207.4	206.5	208.6	207.3	224.0	335.3	101%	99%	101%	98%	98%	100%
						10000000	1943.6	1940.0	1944.7	1948.7	1948.6	2200.8	1983.6	1954.5	1964.4	1964.2	1961.9	2152.6	102%	101%	101%	101%	101%	98%
						100000000	19610.5	19340.5	19472.4	19520.9	19537.6	20044.0	19331.8	19225.3	19608.4	19162.4	19753.1	20093.5	99%	99%	101%	98%	101%	100%
					sequential	1000000	202.9	202.6	205.8	205.2	215.4	265.8	204.2	209.3	209.3	201.0	212.9	264.2	101%	103%	102%	98%	99%	99%
						10000000	1920.6	1875.0	1916.7	1909.9	1948.4	2007.3	1953.9	1915.2	1896.9	1973.6	1946.1	2022.7	102%	102%	99%	103%	100%	101%
						100000000	18833.5	19245.8	18723.2	19670.8	18817.2	21053.5	19448.1	19343.6	18844.1	19226.4	19274.0	19186.7	103%	101%	101%	98%	102%	91%
	hash	bitmapscan	master	i5	cycle	1000000	7.8	7.8	8.1	9.8	24.2	103.0	7.9	7.8	8.0	9.9	25.1	107.9	101%	99%	99%	102%	104%	105%
						10000000	8.0	7.8	8.0	9.9	23.9	172.2	7.9	7.8	8.1	10.0	25.3	191.9	100%	100%	101%	101%	106%	111%
						50000000	7.9	8.3	8.4	10.0	24.1	172.2	8.0	8.1	8.3	9.9	25.4	183.0	101%	98%	98%	99%	106%	106%
					random	1000000	7.8	7.8	8.2	10.1	22.3	100.6	7.9	8.0	8.2	10.2	23.7	107.5	101%	102%	100%	100%	106%	107%
						10000000	7.9	7.9	8.0	9.7	24.1	157.3	7.9	7.9	8.1	10.1	25.8	171.2	100%	99%	101%	103%	107%	109%
						50000000	7.9	8.0	8.2	9.9	24.1	170.8	8.1	8.1	8.2	10.0	25.2	180.6	102%	101%	99%	100%	105%	106%
					sequential	1000000	8.0	7.9	7.9	8.6	13.1	57.6	7.8	7.9	7.9	8.6	13.2	59.2	98%	100%	100%	100%	101%	103%
						10000000	7.9	8.0	7.8	8.6	13.1	58.2	7.8	7.9	7.9	8.5	13.1	58.7	99%	99%	102%	99%	100%	101%
						50000000	7.9	7.9	7.9	8.9	13.2	58.2	8.1	7.9	7.9	8.8	13.2	59.0	102%	100%	100%	99%	100%	101%
				xeon	cycle	1000000	9.9	10.4	9.8	11.2	26.4	129.5	9.9	9.9	10.6	11.7	28.5	131.9	101%	96%	107%	104%	108%	102%
						10000000	10.1	10.0	9.9	12.0	30.9	205.3	9.8	9.5	9.5	12.6	28.2	220.6	97%	95%	96%	105%	91%	107%
						100000000	9.1	9.2	10.4	12.0	27.6	210.3	9.6	9.9	10.6	11.4	27.8	221.2	105%	107%	102%	95%	101%	105%
					random	1000000	9.6	10.4	9.3	12.6	27.7	132.4	9.8	10.1	9.7	10.8	27.2	131.2	102%	97%	104%	86%	98%	99%
						10000000	10.1	9.6	10.0	11.4	28.5	190.5	10.0	9.7	9.9	11.3	28.2	206.6	98%	102%	99%	99%	99%	108%
						100000000	10.2	10.1	10.2	12.4	29.0	204.9	9.5	9.8	9.9	12.0	29.0	220.1	92%	97%	97%	97%	100%	107%
					sequential	1000000	10.0	10.2	10.1	11.1	16.2	69.7	10.1	9.9	10.2	10.9	16.7	68.0	101%	97%	101%	98%	103%	98%
						10000000	9.2	9.9	9.7	10.7	16.2	70.1	10.0	10.1	9.5	10.8	15.6	67.4	108%	102%	98%	101%	96%	96%
						100000000	10.5	9.2	9.7	10.7	16.7	70.9	10.5	10.1	10.5	10.4	16.3	71.4	99%	110%	108%	98%	97%	101%
			patched	i5	cycle	1000000	7.9	7.9	8.2	10.0	24.2	101.9	7.9	7.9	8.0	10.1	25.0	108.0	99%	100%	97%	101%	103%	106%
						10000000	7.7	7.9	8.2	10.0	24.5	180.8	7.8	7.9	8.0	10.1	25.1	184.2	100%	100%	98%	101%	102%	102%
						50000000	8.0	7.9	8.2	9.9	24.2	172.6	8.0	7.9	8.1	10.1	25.1	182.5	100%	100%	99%	102%	104%	106%
					random	1000000	7.8	7.9	8.2	9.9	22.5	100.0	8.0	7.9	8.1	10.1	23.9	106.8	102%	100%	98%	101%	106%	107%
						10000000	7.9	7.9	8.2	9.9	23.9	157.0	7.9	8.0	8.2	10.0	25.2	170.1	99%	101%	100%	101%	105%	108%
						50000000	7.9	8.0	8.2	9.9	24.2	169.3	8.0	8.1	8.3	9.9	25.1	181.8	102%	101%	101%	100%	104%	107%
					sequential	1000000	8.0	7.9	8.1	8.7	13.1	58.7	8.1	7.8	8.0	8.6	13.2	58.2	101%	99%	98%	98%	101%	99%
						10000000	7.8	7.9	8.1	8.7	13.1	57.7	8.0	7.8	7.9	8.6	13.3	58.9	102%	99%	98%	98%	101%	102%
						50000000	8.2	8.1	8.0	9.2	13.4	57.9	8.0	8.0	8.1	9.3	13.4	58.7	98%	99%	100%	101%	100%	101%
				xeon	cycle	1000000	9.6	10.1	10.3	12.8	26.7	117.5	10.2	9.9	10.4	12.0	28.8	124.1	106%	97%	101%	93%	108%	106%
						10000000	10.0	9.7	9.9	11.7	28.7	205.0	10.2	9.3	10.3	11.1	27.5	219.8	102%	96%	105%	94%	96%	107%
						100000000	8.9	9.4	10.6	11.2	27.9	210.2	10.3	9.9	10.3	10.9	28.4	212.2	115%	105%	97%	98%	102%	101%
					random	1000000	9.6	9.6	10.0	12.6	27.1	124.0	10.3	9.6	9.5	11.8	26.7	131.4	107%	100%	95%	94%	99%	106%
						10000000	10.1	9.8	9.7	11.4	29.0	192.1	9.6	10.0	10.5	11.1	28.3	206.4	95%	102%	108%	97%	98%	107%
						100000000	9.9	10.6	10.2	12.3	28.6	216.8	10.3	9.8	9.8	11.6	28.8	222.2	104%	92%	96%	94%	101%	102%
					sequential	1000000	9.9	10.0	10.7	10.9	16.4	70.3	10.2	9.9	10.1	10.6	16.0	69.9	103%	99%	95%	97%	98%	100%
						10000000	9.2	9.4	9.7	10.0	15.9	67.0	9.1	9.3	9.8	10.2	15.5	68.5	99%	98%	101%	102%	98%	102%
						100000000	10.2	9.8	9.9	11.1	16.8	70.0	10.2	9.0	10.3	10.9	15.7	69.9	100%	92%	104%	98%	94%	100%
		indexscan	master	i5	cycle	1000000	7.9	7.8	8.0	9.4	21.1	90.0	7.9	7.7	8.1	9.6	20.5	90.6	100%	99%	101%	102%	98%	101%
						10000000	7.9	7.9	8.1	9.5	20.5	128.2	7.8	7.9	8.0	9.5	20.8	127.6	99%	100%	99%	100%	101%	100%
						50000000	8.1	8.0	8.1	9.4	20.8	127.1	8.0	7.8	8.2	9.5	20.9	127.5	99%	98%	100%	101%	101%	100%
					random	1000000	7.8	7.9	8.2	9.7	20.0	87.8	7.8	8.0	8.1	9.6	19.8	86.5	100%	101%	99%	99%	99%	99%
						10000000	7.8	7.8	8.1	9.5	20.6	117.7	7.9	7.8	8.0	9.5	20.6	116.6	100%	100%	99%	100%	100%	99%

					50000000	8.0	8.0	8.1	9.5	20.9	125.8	8.0	8.0	8.2	9.6	21.1	125.8	100%	100%	101%	101%	101%	100%
				sequential	1000000	7.9	7.8	8.0	8.7	13.2	57.9	8.0	7.8	8.0	8.6	13.2	58.1	101%	101%	101%	99%	100%	100%
					10000000	7.9	7.9	8.0	8.8	13.3	59.2	7.8	8.0	8.0	8.7	13.4	59.4	100%	101%	99%	99%	101%	100%
					50000000	8.0	8.1	8.1	8.8	13.5	59.7	8.0	7.9	8.2	8.9	13.7	59.4	99%	98%	101%	100%	101%	100%
			xeon	cycle	1000000	9.5	10.1	9.9	11.1	25.4	114.7	9.9	9.4	10.2	10.7	24.8	100.6	104%	93%	103%	96%	98%	88%
					10000000	9.5	9.5	9.4	12.1	23.8	159.3	9.8	9.2	9.5	12.4	24.0	158.2	103%	97%	101%	103%	101%	99%
					100000000	8.8	9.4	10.1	11.0	23.5	163.0	9.7	9.6	10.9	10.7	23.6	165.6	110%	102%	108%	97%	100%	102%
				random	1000000	10.0	10.0	9.4	11.9	23.2	125.1	9.7	9.8	9.3	10.6	23.0	108.9	96%	98%	99%	89%	99%	87%
					10000000	9.8	9.7	10.3	10.7	24.3	147.5	9.9	9.4	9.7	11.3	24.8	149.6	102%	97%	94%	106%	102%	101%
					100000000	10.3	10.0	10.5	12.3	23.8	156.7	10.0	9.9	9.8	12.0	23.8	159.3	97%	99%	93%	98%	100%	102%
				sequential	1000000	10.0	9.8	9.5	10.7	16.5	68.1	10.4	10.0	10.5	10.7	16.3	69.7	104%	102%	111%	100%	99%	102%
					10000000	8.8	9.5	9.2	10.2	15.9	71.3	10.5	10.0	9.5	11.2	15.4	69.1	118%	106%	103%	110%	97%	97%
					100000000	10.6	9.5	9.6	9.8	16.2	70.8	10.4	9.8	9.9	10.1	16.1	71.3	97%	103%	103%	103%	100%	101%
		patched	i5	cycle	1000000	7.8	7.9	8.2	9.5	21.0	89.5	7.8	7.8	8.1	9.7	21.4	92.3	100%	100%	98%	102%	102%	103%
					10000000	7.8	8.0	8.0	9.5	20.6	126.0	7.8	7.8	8.1	9.6	21.4	133.1	99%	98%	101%	101%	104%	106%
					50000000	8.0	8.0	8.3	9.5	20.7	126.7	7.9	8.1	8.1	9.9	21.6	133.9	99%	101%	97%	105%	105%	106%
				random	1000000	7.7	7.9	8.2	9.6	20.0	86.7	7.8	7.9	8.1	9.9	20.5	90.0	101%	100%	98%	103%	102%	104%
					10000000	7.9	7.9	8.0	9.6	20.9	115.3	7.9	7.9	8.0	9.7	21.5	125.5	100%	100%	99%	101%	103%	109%
					50000000	8.0	8.0	8.2	9.5	21.1	123.5	7.9	8.0	8.2	9.5	21.6	131.1	99%	100%	100%	101%	102%	106%
				sequential	1000000	8.0	7.9	8.0	8.7	13.3	58.7	8.0	7.8	7.9	8.8	13.4	60.0	101%	99%	98%	101%	101%	102%
					10000000	7.9	7.8	8.0	8.6	13.5	58.0	7.9	8.0	7.8	8.7	13.4	61.2	100%	102%	97%	101%	99%	106%
					50000000	8.0	7.9	8.0	9.2	13.7	58.0	7.9	8.0	8.2	9.3	13.7	60.4	98%	102%	103%	101%	100%	104%
			xeon	cycle	1000000	9.6	10.4	10.4	12.2	25.8	100.6	9.6	9.9	10.7	12.0	24.1	107.3	100%	95%	103%	98%	93%	107%
					10000000	10.0	9.6	9.8	11.4	24.6	158.7	10.0	9.4	10.2	10.9	24.1	171.4	100%	98%	104%	95%	98%	108%
					100000000	8.9	9.4	11.1	11.0	23.8	161.0	10.0	9.5	10.4	10.3	24.3	157.2	112%	101%	94%	94%	102%	98%
				random	1000000	9.8	9.7	9.4	12.3	24.8	108.8	10.1	9.2	9.4	11.1	22.9	117.5	103%	94%	101%	91%	92%	108%
					10000000	10.5	9.9	9.7	11.0	25.1	149.7	9.7	10.3	9.8	10.6	24.6	160.2	92%	105%	101%	96%	98%	107%
					100000000	9.7	10.8	10.1	11.7	24.3	163.1	10.8	10.0	9.7	11.2	24.9	179.4	111%	92%	96%	96%	102%	110%
				sequential	1000000	9.5	9.8	10.3	11.3	16.1	68.1	9.9	9.6	10.3	10.6	15.5	72.6	104%	98%	100%	93%	96%	107%
					10000000	9.0	9.6	9.4	10.2	15.7	70.0	8.8	9.4	9.0	10.1	16.0	73.5	99%	98%	96%	99%	102%	105%
					100000000	10.3	9.9	10.1	11.0	16.1	71.6	10.6	8.6	10.4	11.3	16.1	73.9	103%	87%	102%	103%	100%	103%
	seqscan	master	i5	cycle	1000000	140.2	141.1	141.1	142.3	146.5	180.5	141.0	139.0	139.5	140.7	147.1	181.1	101%	99%	99%	99%	100%	100%
					10000000	1313.8	1301.6	1303.9	1311.1	1310.6	1360.7	1298.4	1304.0	1306.3	1296.5	1323.1	1370.4	99%	100%	100%	99%	101%	101%
					50000000	15248.1	15318.9	15395.2	15357.5	15400.5	15194.6	15244.8	15277.4	15358.8	15804.2	15337.6	15316.5	100%	100%	100%	103%	100%	101%
				random	1000000	141.6	139.2	139.4	142.6	152.6	182.1	140.2	139.7	140.0	141.1	145.8	182.1	99%	100%	100%	99%	96%	100%
					10000000	1311.0	1299.7	1303.3	1299.9	1317.6	1378.0	1302.4	1302.0	1300.1	1300.5	1328.9	1362.6	99%	100%	100%	100%	101%	99%
					50000000	15475.0	15388.8	15348.3	15369.1	15386.7	15265.3	15340.0	15349.3	16307.1	15355.9	16260.5	15387.3	99%	100%	106%	100%	106%	101%
				sequential	1000000	139.3	139.7	140.2	140.4	144.1	139.9	139.8	139.5	140.0	140.9	143.8	140.0	100%	100%	100%	100%	100%	100%
					10000000	1298.5	1305.6	1329.0	1299.4	1310.9	1334.6	1298.1	1300.9	1303.3	1301.9	1303.9	1349.2	100%	100%	98%	100%	99%	101%
					50000000	15351.5	15292.0	15420.2	15296.5	15862.1	15569.0	15280.4	15315.6	15369.8	15375.5	15322.7	15328.2	100%	100%	100%	101%	97%	98%
			xeon	cycle	1000000	161.4	162.2	160.2	159.9	167.7	226.0	160.3	164.0	159.7	162.6	165.9	212.3	99%	101%	100%	102%	99%	94%
					10000000	1600.8	1473.3	1479.7	1494.3	1499.8	1562.0	1466.7	1471.3	1472.8	1493.7	1509.3	1550.7	92%	100%	100%	100%	101%	99%
					100000000	16100.5	14707.3	16381.2	16351.5	15112.0	16421.8	14869.5	14687.0	14946.9	14936.6	14849.7	15025.6	92%	100%	91%	91%	98%	91%
				random	1000000	159.9	160.3	158.2	162.3	165.9	211.0	161.8	160.3	160.7	167.4	165.4	186.1	101%	100%	102%	103%	100%	88%
					10000000	1529.4	1509.7	1471.2	1489.9	1489.7	1577.0	1503.1	1498.5	1467.6	1467.5	1487.8	1552.9	98%	99%	100%	98%	100%	98%
					100000000	15606.7	16170.4	14795.1	16069.3	14770.1	16321.6	15222.1	14973.2	14696.1	14869.4	14941.8	15060.5	98%	93%	99%	93%	101%	92%
				sequential	1000000	162.8	163.7	160.8	160.9	163.7	200.4	163.3	160.8	159.7	161.0	163.9	200.4	100%	98%	99%	100%	100%	100%
					10000000	1476.2	1504.4	1476.7	1494.5	1509.9	1515.0	1489.5	1499.3	1488.1	1492.4	1479.4	1508.2	101%	100%	101%	100%	98%	100%
					100000000	16104.2	16049.0	14835.4	16222.8	14679.1	14956.3	16155.3	14779.9	14978.6	14686.4	14831.1	14735.5	100%	92%	101%	91%	101%	99%
		patched	i5	cycle	1000000	139.1	139.8	141.1	141.6	148.2	140.0	140.5	139.9	140.0	141.3	146.5	179.7	101%	100%	99%	100%	99%	128%
					10000000	1297.7	1293.9	1299.7	1312.4	1313.6	1368.7	1300.8	1304.6	1304.9	1301.5	1308.6	1416.7	100%	101%	100%	99%	100%	104%
					50000000	15384.9	16455.5	15339.6	15246.0	15216.8	15337.5	15217.7	15656.8	15819.3	15323.7	15371.8	15319.6	99%	95%	103%	101%	101%	100%

					random	1000000	140.1	140.4	139.7	141.5	145.8	180.6	139.2	140.7	139.3	142.5	146.2	184.2	99%	100%	100%	101%	100%	102%
						10000000	1300.0	1300.0	1301.1	1301.1	1320.6	1370.5	1312.2	1311.6	1298.2	1308.2	1308.6	1357.0	101%	101%	100%	101%	99%	99%
						50000000	15316.0	15370.6	15290.2	15383.7	15275.7	15375.2	15319.8	15306.5	16365.7	15290.1	15296.4	15374.7	100%	100%	107%	99%	100%	100%
					sequential	1000000	139.5	140.9	140.3	140.5	144.2	139.6	140.0	141.6	140.4	140.7	143.8	174.2	100%	101%	100%	100%	100%	125%
						10000000	1303.6	1306.2	1306.4	1300.1	1313.6	1338.2	1298.8	1303.5	1309.0	1309.9	1338.9	1341.6	100%	100%	100%	101%	102%	100%
						50000000	16329.3	15318.2	15350.1	15375.8	15548.6	15343.0	15276.8	15377.9	16805.8	15426.8	15277.4	15390.9	94%	100%	109%	100%	98%	100%
				xeon	cycle	1000000	163.0	161.7	162.1	166.2	171.8	162.4	163.3	162.0	162.9	167.3	168.5	215.1	100%	100%	100%	101%	98%	132%
						10000000	1568.2	1487.8	1515.3	1530.1	1534.6	1568.1	1507.1	1509.3	1505.9	1492.7	1503.4	1565.9	96%	101%	99%	98%	98%	100%
						100000000	14912.7	15225.4	15105.8	14896.4	15018.2	15290.9	14969.8	14946.6	14946.1	15028.3	14980.6	15259.8	100%	98%	99%	101%	100%	100%
					random	1000000	163.2	165.2	164.7	165.0	169.5	214.5	163.1	163.9	161.8	166.0	170.5	215.6	100%	99%	98%	101%	101%	101%
						10000000	1521.2	1488.5	1497.4	1493.5	1531.9	1604.1	1513.4	1524.5	1502.7	1505.6	1560.1	1579.4	99%	102%	100%	101%	102%	98%
						100000000	14917.8	15086.3	14876.7	15156.8	15158.3	15277.8	14906.5	15310.7	14925.8	15086.0	15281.5	15227.7	100%	101%	100%	100%	101%	100%
					sequential	1000000	161.2	165.4	163.9	163.7	166.2	202.4	161.7	160.0	164.6	164.2	165.3	201.1	100%	97%	100%	100%	99%	99%
						10000000	1509.9	1524.6	1517.7	1501.8	1508.9	1530.7	1512.0	1494.0	1497.0	1492.6	1518.0	1542.5	100%	98%	99%	99%	101%	101%
						100000000	15150.8	15177.9	15378.3	14960.7	15105.5	15020.2	15456.4	14986.2	15180.0	15102.7	14952.1	15178.3	102%	99%	99%	101%	99%	101%
uncached	btree	bitmapscan	master	i5	cycle	1000000	12.3	13.2	25.0	169.0	1563.9	485.2	11.6	12.0	13.7	32.2	188.1	509.2	94%	91%	55%	19%	12%	105%
						10000000	11.0	13.0	25.4	166.4	1538.5	15212.0	11.3	11.9	13.5	33.3	180.5	1683.2	103%	92%	53%	20%	12%	11%
						50000000	11.7	13.6	26.7	169.6	1608.9	15314.8	11.8	14.2	15.4	32.6	181.4	1696.8	101%	104%	58%	19%	11%	11%
					random	1000000	12.2	13.6	25.7	168.8	793.8	446.3	11.7	12.0	13.8	31.8	165.9	732.3	96%	88%	54%	19%	21%	164%
						10000000	11.3	13.2	27.6	165.8	1581.8	7549.0	11.2	12.3	13.7	31.1	176.6	1479.4	99%	93%	50%	19%	11%	20%
						50000000	12.1	13.7	27.0	166.1	1547.7	15421.9	12.0	12.4	16.3	31.9	190.5	1637.4	99%	90%	60%	19%	12%	11%
					sequential	1000000	11.5	11.9	12.4	16.5	24.9	98.5	11.5	11.3	11.4	14.8	25.2	131.1	100%	95%	92%	90%	102%	133%
						10000000	11.0	11.6	12.1	15.2	23.5	86.8	10.8	11.8	11.9	16.1	25.0	126.1	98%	102%	99%	106%	107%	145%
						50000000	11.2	11.8	12.0	14.4	21.3	90.1	11.4	11.8	11.7	14.3	25.0	125.8	102%	100%	98%	99%	117%	140%
				xeon	cycle	1000000	13.0	14.8	24.8	123.9	700.6	373.8	12.9	13.5	14.5	27.5	126.3	508.8	99%	91%	59%	22%	18%	136%
						10000000	13.1	14.2	24.6	123.9	1021.5	4370.3	13.3	13.2	15.5	26.0	142.9	1294.0	102%	93%	63%	21%	14%	30%
						100000000	13.8	15.1	27.5	125.5	1101.9	10087.0	13.9	15.8	17.0	30.9	128.9	1287.8	100%	104%	62%	25%	12%	13%
					random	1000000	12.7	14.2	24.6	125.4	540.7	372.6	12.8	13.7	15.3	28.2	113.3	479.0	101%	97%	62%	22%	21%	129%
						10000000	13.9	13.6	25.3	122.2	1087.3	4854.6	13.9	14.0	16.6	27.0	138.4	1028.6	100%	102%	66%	22%	13%	21%
						100000000	15.4	14.8	27.2	123.7	1110.5	10733.0	14.2	14.5	16.7	30.3	125.7	1259.6	93%	98%	62%	24%	11%	12%
					sequential	1000000	14.5	14.0	13.9	15.4	24.1	96.1	14.0	13.9	13.6	14.4	24.7	106.3	97%	99%	98%	93%	103%	111%
						10000000	14.4	13.7	13.7	14.1	24.9	94.6	13.5	13.7	13.9	14.7	25.3	107.4	94%	100%	101%	104%	102%	114%
						100000000	14.6	13.1	14.0	16.3	24.8	95.3	13.6	13.5	13.8	16.0	24.4	108.8	93%	103%	98%	98%	99%	114%
			patched	i5	cycle	1000000	11.4	12.9	28.1	166.6	1547.0	460.6	11.7	12.2	13.3	32.7	183.9	485.6	103%	94%	47%	20%	12%	105%
						10000000	11.9	14.1	26.5	167.3	1546.3	15289.2	11.8	12.5	13.7	31.4	184.8	1708.2	100%	88%	52%	19%	12%	11%
						50000000	11.3	13.8	27.1	166.1	1590.4	15420.3	11.7	13.8	16.1	33.2	184.9	1667.3	104%	100%	59%	20%	12%	11%
					random	1000000	11.5	13.3	26.1	175.3	775.8	412.2	12.0	11.7	14.1	32.1	165.3	673.6	104%	88%	54%	18%	21%	163%
						10000000	11.9	13.3	27.4	163.1	1572.8	7480.3	11.5	12.2	15.5	32.9	183.0	1509.3	97%	91%	56%	20%	12%	20%
						50000000	11.7	13.9	28.0	171.5	1620.9	15286.7	12.1	13.3	15.1	32.2	185.2	1637.2	104%	96%	54%	19%	11%	11%
					sequential	1000000	11.2	11.6	12.5	16.2	24.0	94.5	12.5	12.1	12.3	17.1	25.3	136.5	112%	105%	98%	106%	105%	144%
						10000000	11.8	11.6	12.3	16.1	22.3	89.0	11.7	11.6	11.6	14.3	25.6	125.7	99%	100%	95%	88%	114%	141%
						50000000	11.9	11.8	11.9	14.6	22.0	90.0	12.0	11.8	11.8	16.0	25.2	127.1	101%	100%	99%	109%	115%	141%
				xeon	cycle	1000000	13.8	13.9	25.1	122.8	702.2	365.9	13.7	13.7	14.8	26.4	130.7	511.9	99%	99%	59%	21%	19%	140%
						10000000	13.3	14.0	24.7	124.2	1037.8	4375.8	13.7	13.7	16.2	26.5	142.8	1295.8	103%	98%	65%	21%	14%	30%
						100000000	13.7	15.1	27.3	127.1	1103.3	10098.4	14.6	14.5	17.7	31.1	123.2	1281.6	107%	96%	65%	24%	11%	13%
					random	1000000	12.6	14.8	24.6	123.0	545.6	360.7	12.9	13.5	14.5	26.7	118.6	498.9	102%	91%	59%	22%	22%	138%
						10000000	13.4	15.2	23.7	128.3	1080.8	4883.5	13.6	13.2	14.6	29.1	142.5	1040.0	102%	87%	62%	23%	13%	21%
						100000000	14.3	15.1	26.1	124.8	1112.3	10788.0	13.8	13.9	17.1	27.3	127.4	1257.9	97%	92%	65%	22%	11%	12%
					sequential	1000000	14.6	13.8	14.3	14.5	24.6	94.5	14.4	13.8	14.1	14.0	24.9	107.4	98%	100%	98%	96%	101%	114%
						10000000	13.0	13.6	14.3	14.7	22.8	94.9	13.0	13.2	13.2	13.5	24.9	108.2	100%	97%	92%	92%	109%	114%
						100000000	13.9	13.4	14.5	15.4	24.8	97.3	13.3	13.9	15.1	16.3	25.2	108.7	95%	103%	104%	106%	102%	112%
		indexscan	master	i5	cycle	1000000	10.9	12.7	26.3	167.2	1557.9	470.4	12.5	12.6	25.2	167.5	1537.6	438.7	114%	99%	96%	100%	99%	93%

					10000000	10.5	12.7	25.8	165.6	1522.3	15293.0	10.7	12.7	25.1	165.4	1509.9	15236.8	102%	100%	98%	100%	99%	100%
					50000000	11.6	13.8	27.2	172.8	1564.3	15488.0	11.5	14.0	28.2	165.6	1555.9	15419.6	99%	102%	104%	96%	99%	100%
				random	1000000	12.1	12.1	28.2	171.4	853.4	437.3	11.1	12.7	26.3	176.2	816.8	399.5	92%	105%	93%	103%	96%	91%
					10000000	10.8	12.7	27.2	170.4	1624.9	7552.1	11.7	13.8	25.0	170.7	1610.9	7467.3	109%	108%	92%	100%	99%	99%
					50000000	12.3	14.8	27.0	162.6	1565.1	15265.8	11.1	14.2	26.3	165.0	1595.4	15870.8	90%	96%	97%	102%	102%	104%
				sequential	1000000	11.4	11.3	11.8	15.0	22.3	89.8	11.9	11.4	11.5	16.7	22.4	92.7	105%	101%	97%	112%	100%	103%
					10000000	11.4	12.4	11.5	15.3	21.5	90.1	11.1	11.2	12.1	15.1	23.2	90.8	97%	91%	105%	99%	108%	101%
					50000000	11.5	11.4	12.2	14.0	23.9	92.1	11.2	11.5	12.6	16.0	23.8	91.9	97%	101%	104%	114%	99%	100%
			xeon	cycle	1000000	12.3	14.3	24.1	122.1	697.9	351.0	13.0	13.3	23.0	120.5	741.8	352.5	105%	93%	96%	99%	106%	100%
					10000000	14.1	13.8	24.8	122.7	1019.7	4398.7	13.5	12.6	23.8	121.6	1017.7	4435.0	96%	91%	96%	99%	100%	101%
					100000000	13.7	15.3	27.5	125.4	1100.8	10196.5	14.2	15.6	27.2	125.0	1098.7	10134.4	104%	103%	99%	100%	100%	99%
				random	1000000	12.7	14.0	24.8	121.8	520.0	349.6	12.5	14.5	24.8	123.7	528.4	350.3	99%	103%	100%	102%	102%	100%
					10000000	13.2	13.6	23.7	121.1	1087.1	4757.4	13.5	14.3	24.6	121.3	1088.6	4798.6	103%	105%	104%	100%	100%	101%
					100000000	14.1	13.6	27.6	127.1	1111.3	10714.8	14.3	14.2	25.7	126.2	1101.8	10803.2	101%	104%	93%	99%	99%	101%
				sequential	1000000	14.0	13.6	14.0	15.2	23.7	95.7	14.2	13.9	14.1	14.2	24.3	95.9	102%	102%	101%	93%	103%	100%
					10000000	13.1	13.0	12.9	14.0	22.4	94.7	13.0	13.5	14.5	14.3	23.3	95.3	99%	104%	112%	102%	104%	101%
					100000000	13.5	13.6	13.7	16.1	23.4	94.8	13.1	13.3	14.4	16.1	23.2	97.1	97%	98%	105%	99%	99%	102%
		patched	i5	cycle	1000000	11.4	12.5	26.9	164.6	1548.5	441.4	11.8	12.7	13.2	32.4	179.0	433.9	103%	101%	49%	20%	12%	98%
					10000000	11.6	13.0	26.2	169.7	1529.0	15193.1	12.2	12.3	13.5	32.7	185.1	1634.0	105%	95%	52%	19%	12%	11%
					50000000	11.7	14.4	26.9	170.5	1577.0	15288.4	11.5	13.5	15.7	32.7	182.9	1626.5	98%	94%	58%	19%	12%	11%
				random	1000000	11.4	13.0	27.3	174.1	782.3	401.8	10.9	12.1	13.4	32.4	167.3	802.4	96%	93%	49%	19%	21%	200%
					10000000	11.6	13.3	26.5	171.0	1578.5	7440.0	12.2	12.5	14.6	29.9	181.8	1477.4	105%	94%	55%	17%	12%	20%
					50000000	12.0	13.0	27.6	167.9	1622.3	15262.1	11.9	12.7	15.5	33.1	181.1	1628.2	99%	97%	56%	20%	11%	11%
				sequential	1000000	12.8	11.1	11.8	17.4	22.4	91.8	11.5	11.2	12.3	18.2	53.0	358.0	89%	101%	104%	105%	236%	390%
					10000000	11.3	11.6	11.9	14.6	23.1	93.6	11.1	11.1	11.7	18.6	51.7	355.5	99%	95%	99%	127%	223%	380%
					50000000	11.9	11.6	12.0	15.6	21.7	90.3	11.6	11.5	12.8	18.0	52.2	356.1	97%	99%	107%	116%	241%	394%
			xeon	cycle	1000000	13.4	13.3	24.9	121.3	747.5	351.4	13.2	13.5	15.5	25.6	123.2	380.8	99%	102%	62%	21%	16%	108%
					10000000	12.9	14.3	24.5	123.7	1031.2	4415.8	13.3	13.3	15.9	25.3	139.2	1157.9	103%	93%	65%	20%	13%	26%
					100000000	13.9	15.1	27.6	126.3	1100.6	10090.1	14.0	15.0	16.7	30.0	120.4	1253.9	100%	100%	61%	24%	11%	12%
				random	1000000	12.8	14.3	25.1	123.2	532.7	353.1	12.7	13.2	13.7	25.0	115.9	634.8	99%	92%	54%	20%	22%	180%
					10000000	13.6	15.0	23.1	122.7	1078.3	4848.5	12.7	13.3	14.2	28.4	135.2	1020.6	93%	89%	62%	23%	13%	21%
					100000000	13.5	14.9	28.2	128.9	1110.6	10768.2	14.1	14.2	16.7	26.7	119.9	1214.7	104%	95%	59%	21%	11%	11%
				sequential	1000000	15.0	13.9	14.0	14.2	24.7	96.1	14.3	13.7	14.5	16.2	42.1	225.9	95%	98%	103%	114%	171%	235%
					10000000	13.2	13.3	13.5	14.3	23.4	95.7	12.4	13.3	12.8	14.8	41.8	212.1	94%	100%	95%	103%	178%	222%
					100000000	13.0	13.1	13.9	15.1	23.3	96.8	14.6	13.5	14.4	17.3	42.5	211.5	112%	103%	104%	115%	182%	218%
	seqscan	master	i5	cycle	1000000	353.2	370.6	370.3	359.1	410.0	383.4	392.8	357.9	352.9	386.1	351.5	394.3	111%	97%	95%	108%	86%	103%
					10000000	3203.2	3345.0	3135.3	3139.2	3177.4	3162.8	3193.6	3148.2	3107.8	3162.8	3140.6	3382.1	100%	94%	99%	101%	99%	107%
					50000000	16423.9	15218.5	15191.9	17158.8	15986.9	15325.9	15200.9	15220.0	15210.4	16059.5	15235.2	15323.9	93%	100%	100%	94%	95%	100%
				random	1000000	388.1	340.0	382.8	391.2	392.2	412.3	359.6	367.2	364.2	356.5	337.2	353.7	93%	108%	95%	91%	86%	86%
					10000000	3177.5	3432.8	3500.4	3246.6	3114.3	3372.5	3117.7	3118.2	3159.2	3229.8	3084.6	3186.0	98%	91%	90%	99%	99%	94%
					50000000	15201.0	15275.0	15236.3	16251.9	16579.2	15355.8	15566.7	15758.1	15236.9	15243.2	15246.9	15436.0	102%	103%	100%	94%	92%	101%
				sequential	1000000	403.7	356.9	344.0	359.6	382.6	468.3	335.5	353.3	323.9	356.2	391.3	424.4	83%	99%	94%	99%	102%	91%
					10000000	3392.5	3273.2	3588.9	3323.2	3184.4	3442.5	3373.9	3170.8	3389.5	3316.8	3153.0	3191.2	99%	97%	94%	100%	99%	93%
					50000000	15247.4	15289.4	15429.4	16172.4	16143.9	16056.5	15237.9	15235.0	15217.9	15169.0	15218.1	15232.6	100%	100%	99%	94%	94%	95%
			xeon	cycle	1000000	251.0	246.4	251.3	253.0	248.6	297.3	251.5	250.8	251.7	254.1	248.0	246.6	100%	102%	100%	100%	100%	83%
					10000000	2203.9	2180.6	2198.5	2182.8	2198.8	2233.6	2199.1	2175.1	2220.7	2203.1	2207.5	2232.3	100%	100%	101%	101%	100%	100%
					100000000	21479.9	21622.8	21398.5	21850.7	21801.0	21799.5	21489.4	21458.0	21491.9	21570.4	21741.0	21819.3	100%	99%	100%	99%	100%	100%
				random	1000000	245.7	247.2	253.0	251.9	246.0	281.9	247.0	249.2	252.9	249.7	249.3	284.5	101%	101%	100%	99%	101%	101%
					10000000	2210.4	2196.6	2187.9	2216.4	2214.3	2233.8	2211.1	2214.7	2191.8	2201.1	2189.7	2237.0	100%	101%	100%	99%	99%	100%
					100000000	21566.0	21268.2	21870.1	21374.9	21493.6	21861.5	21541.0	21591.1	21879.6	21460.8	21508.1	21701.9	100%	102%	100%	100%	100%	99%
				sequential	1000000	253.2	250.5	251.2	250.3	253.8	249.2	250.8	253.3	250.8	250.6	225.9	250.0	99%	101%	100%	100%	89%	100%
					10000000	2204.1	2180.9	2194.9	2213.1	2219.5	2180.1	2198.6	2194.1	2213.6	2223.7	2214.6	2145.8	100%	101%	101%	100%	100%	98%

					100000000	21855.2	21686.4	21458.0	21696.9	21594.7	21480.5	21879.7	21740.3	21464.7	21620.6	21630.1	21510.9	100%	100%	100%	100%	100%	100%
			patched	i5	cycle	1000000	370.2	385.5	435.5	358.4	376.1	422.7	373.7	340.3	425.7	362.7	348.2	383.5	101%	88%	98%	101%	93%	91%
						10000000	3201.3	3138.8	3150.5	3145.9	3112.1	3128.0	3091.9	3133.6	3089.3	3116.9	3096.5	3153.6	97%	100%	98%	99%	99%	101%
						50000000	15251.3	15683.9	15231.0	15228.3	15206.8	15255.4	15189.4	15242.8	15204.3	16254.2	15256.8	15245.7	100%	97%	100%	107%	100%	100%
					random	1000000	380.6	386.9	349.0	361.8	343.0	421.8	404.5	373.2	365.4	356.7	347.7	371.6	106%	96%	105%	99%	101%	88%
						10000000	3149.2	3165.1	3106.6	3245.5	3155.4	3110.2	3094.1	3154.5	3120.4	3263.8	3143.7	3126.7	98%	100%	100%	101%	100%	101%
						50000000	15267.8	15272.2	15198.4	15246.8	16250.7	15285.3	15272.0	15300.1	16596.8	15242.2	15142.9	15284.5	100%	100%	109%	100%	93%	100%
					sequential	1000000	392.2	375.3	317.3	345.5	361.5	417.5	392.9	353.1	341.4	356.9	381.8	402.2	100%	94%	108%	103%	106%	96%
						10000000	3059.9	3084.3	3108.1	3085.0	3161.7	3205.3	3263.7	3097.3	3126.8	3323.4	3132.5	3160.4	107%	100%	101%	108%	99%	99%
						50000000	15221.6	15245.2	16105.8	15191.7	15269.5	16323.7	15180.9	15266.8	15720.5	15181.3	15188.2	15246.2	100%	100%	98%	100%	99%	93%
				xeon	cycle	1000000	250.7	249.8	258.4	252.6	253.1	247.8	251.0	249.6	251.7	255.7	251.5	246.0	100%	100%	97%	101%	99%	99%
						10000000	2204.8	2197.4	2214.5	2186.4	2234.3	2246.6	2200.2	2199.2	2224.5	2192.0	2218.2	2247.0	100%	100%	100%	100%	99%	100%
						100000000	21637.0	21539.6	21620.1	21759.4	21850.5	21893.3	21762.3	21522.6	21631.4	21835.6	21843.5	22000.7	101%	100%	100%	100%	100%	100%
					random	1000000	252.2	250.2	254.1	249.3	247.1	285.1	252.1	250.4	250.6	255.2	248.1	281.9	100%	100%	99%	102%	100%	99%
						10000000	2222.5	2222.7	2186.2	2218.1	2224.0	2246.6	2221.8	2223.6	2213.8	2227.8	2190.1	2251.6	100%	100%	101%	100%	98%	100%
						100000000	21585.5	21531.5	21897.3	21300.5	21599.0	21756.5	21560.6	21500.5	22026.7	21319.1	21629.5	21765.1	100%	100%	101%	100%	100%	100%
					sequential	1000000	252.8	252.4	251.4	250.0	254.7	284.3	255.6	250.9	254.5	251.0	256.3	282.4	101%	99%	101%	100%	101%	99%
						10000000	2216.6	2192.1	2209.9	2225.9	2219.0	2138.1	2203.8	2194.0	2196.0	2216.2	2234.9	2209.9	99%	100%	99%	100%	101%	103%
						100000000	21930.8	21611.0	21511.8	21740.3	21742.9	21623.9	21888.9	21589.7	21624.4	21782.1	21787.6	21781.1	100%	100%	101%	100%	100%	101%
	btree-sort	bitmapscan	master	i5	cycle	1000000	224.7	222.0	426.7	553.1	486.0	569.6	389.3	398.2	548.8	428.2	612.5	924.4	173%	179%	129%	77%	126%	162%
						10000000	2369.5	2374.2	2296.4	3481.4	5275.5	4272.9	4087.9	4287.2	3844.4	3794.9	4621.6	5709.7	173%	181%	167%	109%	88%	134%
						50000000	11812.4	7929.3	8544.3	12468.2	21541.2	25303.3	16212.7	20516.2	20710.6	16484.4	18977.7	18682.5	137%	259%	242%	132%	88%	74%
					random	1000000	527.6	458.6	495.2	446.9	509.6	569.6	976.9	857.3	866.6	826.3	895.6	919.1	185%	187%	175%	185%	176%	161%
						10000000	4167.1	3862.4	4069.4	3866.4	3908.6	4159.0	7304.3	6844.2	7008.8	7120.2	6741.2	7095.6	175%	177%	172%	184%	172%	171%
						50000000	18826.8	18508.6	18367.3	17780.1	17388.5	17735.5	33714.0	33732.8	34927.5	34563.9	33998.8	34112.2	179%	182%	190%	194%	196%	192%
					sequential	1000000	291.9	251.2	238.2	277.8	255.1	270.0	593.9	518.1	393.7	390.5	529.7	508.9	203%	206%	165%	141%	208%	188%
						10000000	2395.2	2247.3	2108.0	2431.2	2119.6	2338.6	3462.9	3571.6	4149.2	3938.8	3588.8	4216.6	145%	159%	197%	162%	169%	180%
						50000000	9455.0	9934.6	10781.3	10663.4	8365.6	10617.3	17594.4	18833.3	21404.9	20796.8	15269.0	16556.6	186%	190%	199%	195%	183%	156%
				xeon	cycle	1000000	230.3	250.7	384.0	493.1	433.1	519.2	276.1	316.4	317.6	394.5	405.2	568.2	120%	126%	83%	80%	94%	109%
						10000000	2140.9	1985.1	2113.0	3559.0	3869.1	3844.6	2512.0	2307.3	3100.5	3849.3	3065.5	3538.4	117%	116%	147%	108%	79%	92%
						100000000	15574.5	18485.4	13886.8	19537.3	33314.7	35827.3	24741.7	21434.7	27692.6	29195.8	36180.6	29407.3	159%	116%	199%	149%	109%	82%
					random	1000000	391.5	393.9	396.4	397.6	414.0	523.3	641.5	628.5	644.6	600.1	586.9	683.1	164%	160%	163%	151%	142%	131%
						10000000	3413.8	3213.3	3212.4	3253.6	3224.4	3594.3	4883.2	4879.8	4515.2	4476.2	4864.5	4870.8	143%	152%	141%	138%	151%	136%
						100000000	33405.7	31370.7	32009.7	31746.8	31331.0	30733.0	45365.5	44828.6	45265.5	44418.2	45744.9	45092.5	136%	143%	141%	140%	146%	147%
					sequential	1000000	194.0	258.9	232.4	221.2	246.4	371.6	276.7	384.7	288.6	287.2	320.2	428.3	143%	149%	124%	130%	130%	115%
						10000000	2198.3	1904.8	2134.9	1893.7	1632.5	2061.7	2774.1	2407.4	2522.9	3011.6	2362.3	2589.6	126%	126%	118%	159%	145%	126%
						100000000	15316.2	14002.8	18843.4	19439.6	14368.1	13391.4	23196.8	21622.9	19420.1	26385.4	23373.5	20834.7	151%	154%	103%	136%	163%	156%
			patched	i5	cycle	1000000	245.7	293.3	544.6	646.8	501.0	485.8	424.6	430.0	386.8	602.1	628.2	918.1	173%	147%	71%	93%	125%	189%
						10000000	2344.2	1748.7	2156.2	4114.6	5261.9	4420.2	4376.6	3932.7	4018.9	4067.8	4504.1	6015.9	187%	225%	186%	99%	86%	136%
						50000000	11244.7	10804.8	10542.0	12578.0	21879.4	28320.1	18070.6	14527.5	18949.9	17452.1	20143.9	21444.9	161%	134%	180%	139%	92%	76%
					random	1000000	503.6	462.5	440.6	433.2	488.1	549.4	906.8	845.4	804.0	790.4	821.1	917.5	180%	183%	182%	182%	168%	167%
						10000000	4112.1	3921.6	3816.6	3917.1	3811.7	4063.9	6884.0	6954.8	6857.5	6995.9	6960.5	7149.2	167%	177%	180%	179%	183%	176%
						50000000	18541.2	18040.8	17952.8	17591.0	17585.0	17889.5	34597.6	33690.7	34424.1	33838.8	34275.1	34000.8	187%	187%	192%	192%	195%	190%
					sequential	1000000	288.2	220.2	235.0	281.3	292.1	287.0	408.3	468.8	405.7	551.4	391.7	408.4	142%	213%	173%	196%	134%	142%
						10000000	2346.1	1914.0	2369.7	2242.9	2163.2	2143.0	4552.3	3202.9	4144.2	3523.5	4132.9	3565.1	194%	167%	175%	157%	191%	166%
						50000000	11691.0	9293.0	8097.9	7760.9	10726.7	8604.2	20403.0	16480.4	17629.4	16564.8	18599.2	14541.6	175%	177%	218%	213%	173%	169%
				xeon	cycle	1000000	258.1	255.8	397.5	453.4	413.8	505.6	353.9	341.1	377.0	311.0	422.8	593.2	137%	133%	95%	69%	102%	117%
						10000000	2006.5	1833.5	2248.9	3580.1	3673.9	3834.9	3268.1	2233.4	3573.7	3801.9	2981.9	3916.4	163%	122%	159%	106%	81%	102%
						100000000	14319.1	14578.9	18121.9	24126.0	33218.4	39966.5	25744.0	20407.8	27712.6	26660.3	36807.7	27512.1	180%	140%	153%	111%	111%	69%
					random	1000000	407.4	377.6	416.6	408.2	402.9	517.9	650.6	628.3	636.6	594.0	617.2	706.1	160%	166%	153%	146%	153%	136%
						10000000	3356.6	3125.8	3090.6	3173.5	3333.3	3429.5	5172.1	4975.3	4460.0	4628.0	4969.1	4935.5	154%	159%	144%	146%	149%	144%
						100000000	31745.4	31931.4	31327.9	32061.0	31531.7	31152.9	45477.8	45780.8	44859.2	45106.8	44639.5	45644.2	143%	143%	143%	141%	142%	147%

				sequential	1000000	223.6	233.6	204.0	230.1	246.3	312.8	327.4	274.1	325.7	313.7	282.7	347.2	146%	117%	160%	136%	115%	111%
					10000000	1662.3	1762.4	2126.9	1721.0	2060.3	2153.8	3034.1	2153.7	2427.3	2611.0	2087.8	3039.8	183%	122%	114%	152%	101%	141%
					100000000	15119.6	17881.8	19095.4	17311.6	17773.5	17063.3	25808.2	25680.5	24194.6	27148.8	19488.2	24491.8	171%	144%	127%	157%	110%	144%
	indexscan	master	i5	cycle	1000000	11.6	12.9	26.0	167.0	1544.6	451.3	11.7	12.9	26.8	167.4	1560.1	443.7	101%	99%	103%	100%	101%	98%
					10000000	11.9	13.0	27.2	167.5	1537.8	15170.3	11.6	13.2	26.8	167.8	1546.3	15233.7	97%	102%	99%	100%	101%	100%
					50000000	12.5	15.0	27.7	169.2	1573.1	15279.8	11.9	13.9	29.3	167.9	1545.9	15443.3	95%	93%	106%	99%	98%	101%
				random	1000000	11.8	12.8	27.0	174.1	828.4	405.1	11.7	12.6	25.7	172.4	886.3	393.6	99%	99%	96%	99%	107%	97%
					10000000	11.8	13.9	27.8	166.7	1644.1	7489.2	11.3	12.9	25.6	168.0	1599.0	7484.3	96%	93%	92%	101%	97%	100%
					50000000	13.0	14.6	28.1	169.8	1562.5	15291.6	13.1	14.9	28.5	170.7	1557.6	15994.4	101%	103%	101%	101%	100%	105%
				sequential	1000000	11.5	11.7	12.1	15.5	23.0	91.1	11.4	11.5	13.0	14.7	23.3	92.1	99%	98%	107%	94%	102%	101%
					10000000	11.4	11.5	12.8	15.9	22.4	90.2	11.6	12.3	12.7	16.6	23.6	91.9	102%	106%	99%	104%	106%	102%
					50000000	12.7	12.4	13.1	15.8	24.6	91.9	12.0	12.4	12.9	15.3	22.6	94.1	94%	100%	99%	97%	92%	102%
			xeon	cycle	1000000	12.8	13.3	23.5	120.9	377.4	352.8	12.7	13.9	23.6	122.0	378.2	356.7	100%	105%	100%	101%	100%	101%
					10000000	12.3	13.1	24.6	120.5	1053.3	3618.1	11.7	13.1	24.0	120.2	1042.5	3680.1	95%	99%	97%	100%	99%	102%
					100000000	13.5	14.4	27.6	143.3	1117.7	10038.5	12.7	13.8	27.7	144.5	1128.1	10202.3	94%	96%	100%	101%	101%	102%
				random	1000000	12.2	14.2	22.6	121.2	571.6	358.2	12.8	13.8	23.0	120.5	531.3	350.7	105%	97%	102%	99%	93%	98%
					10000000	12.5	13.4	22.9	120.8	1084.2	4944.8	12.3	13.3	23.0	122.2	1081.9	4937.5	99%	99%	100%	101%	100%	100%
					100000000	13.1	15.7	29.7	145.1	1131.4	10769.9	13.4	14.8	29.3	145.6	1147.5	10811.7	102%	95%	99%	100%	101%	100%
				sequential	1000000	12.6	12.7	12.8	14.6	22.6	96.8	12.2	12.1	13.5	15.4	22.0	97.7	97%	96%	105%	106%	97%	101%
					10000000	12.2	13.2	12.8	14.4	22.8	96.1	12.3	12.5	12.8	14.1	22.6	96.1	101%	95%	100%	97%	99%	100%
					100000000	12.8	12.9	12.7	14.4	22.5	96.8	12.8	12.1	13.7	13.8	22.8	97.5	100%	94%	108%	96%	101%	101%
		patched	i5	cycle	1000000	12.6	14.1	27.5	168.5	1569.4	418.9	11.8	12.4	14.5	30.7	178.9	435.7	94%	88%	53%	18%	11%	104%
					10000000	11.6	13.4	26.2	164.8	1530.4	15312.1	11.8	13.4	14.4	30.8	182.4	1619.9	102%	100%	55%	19%	12%	11%
					50000000	12.6	14.3	28.5	169.1	1561.6	15474.6	12.6	13.7	15.4	32.7	182.9	1654.5	100%	96%	54%	19%	12%	11%
				random	1000000	11.7	13.9	28.2	170.7	824.2	393.1	12.1	12.6	14.9	31.5	160.7	708.8	104%	91%	53%	18%	19%	180%
					10000000	12.3	13.4	27.1	167.8	1564.7	7486.7	12.2	13.5	15.1	32.1	176.1	1506.9	99%	101%	56%	19%	11%	20%
					50000000	12.6	15.1	28.9	166.1	1568.6	15376.9	13.1	15.2	16.5	33.8	188.2	1643.8	104%	101%	57%	20%	12%	11%
				sequential	1000000	11.6	11.8	12.8	16.1	23.4	90.8	11.7	11.9	12.5	18.2	52.3	361.5	101%	101%	98%	113%	224%	398%
					10000000	12.0	12.3	13.1	15.6	22.2	98.0	12.3	12.7	12.7	19.0	54.0	357.2	102%	104%	97%	122%	244%	364%
					50000000	12.1	12.2	12.6	16.4	22.2	91.7	11.9	12.0	12.9	20.2	52.1	360.4	98%	98%	102%	124%	235%	393%
			xeon	cycle	1000000	12.5	13.8	23.2	122.0	329.9	350.5	12.1	13.1	14.0	25.7	122.7	376.5	97%	95%	60%	21%	37%	107%
					10000000	12.2	13.8	23.1	120.5	1027.1	3702.6	12.0	13.0	13.3	24.9	135.9	1185.4	99%	94%	58%	21%	13%	32%
					100000000	12.6	14.0	26.9	144.0	1122.5	10127.3	12.7	14.1	16.2	26.9	120.0	1219.2	100%	101%	60%	19%	11%	12%
				random	1000000	12.7	13.4	23.6	122.0	568.7	348.6	12.5	13.1	14.5	26.1	118.6	832.8	98%	98%	61%	21%	21%	239%
					10000000	12.2	13.3	23.2	121.5	1089.4	4950.1	12.4	13.0	13.8	24.6	134.2	1035.4	102%	98%	60%	20%	12%	21%
					100000000	13.1	14.4	27.9	146.3	1115.3	10796.0	13.1	14.8	17.3	27.9	119.2	1214.3	100%	103%	62%	19%	11%	11%
				sequential	1000000	12.8	12.8	13.0	14.6	22.5	97.7	12.6	12.7	12.6	16.1	40.5	212.5	98%	99%	97%	110%	180%	218%
					10000000	12.5	12.2	12.4	14.0	22.3	96.3	12.7	12.0	12.6	15.3	41.2	213.0	102%	98%	102%	109%	185%	221%
					100000000	12.2	12.2	13.6	14.1	22.2	97.6	12.3	12.4	13.1	15.7	39.9	239.2	102%	102%	96%	111%	180%	245%
	seqscan	master	i5	cycle	1000000	368.1	361.2	395.3	408.0	377.9	488.6	381.0	342.7	452.6	400.2	384.5	480.3	104%	95%	114%	98%	102%	98%
					10000000	3282.3	3257.2	3602.3	3357.0	3240.3	3267.4	3112.3	3237.8	3211.6	3230.1	3233.0	3297.8	95%	99%	89%	96%	100%	101%
					50000000	15843.2	15479.6	15483.2	16488.3	16422.9	15719.7	15496.5	15401.0	15422.5	15532.6	15478.7	15629.5	98%	99%	100%	94%	94%	99%
				random	1000000	414.2	377.1	400.9	434.8	440.6	462.1	445.0	409.2	363.2	325.0	436.4	447.4	107%	109%	91%	75%	99%	97%
					10000000	3205.2	3169.8	3295.5	3153.9	3449.4	3377.2	3150.8	3148.6	3197.5	3197.8	3198.4	3288.6	98%	99%	97%	101%	93%	97%
					50000000	15452.7	16674.2	15473.1	15777.2	16832.2	15715.7	15646.6	15529.8	18023.6	16008.6	15634.8	15689.1	101%	93%	116%	101%	93%	100%
				sequential	1000000	379.9	359.3	391.3	343.6	389.2	394.1	408.2	410.8	388.4	364.3	415.3	457.0	107%	114%	99%	106%	107%	116%
					10000000	3212.3	3477.9	3306.9	3253.8	3115.6	3713.2	3195.5	3130.4	3153.4	3236.2	3110.2	3249.6	99%	90%	95%	99%	100%	88%
					50000000	15544.2	15495.4	15459.0	15866.1	15507.2	15521.1	15497.7	15485.0	17282.7	16329.1	15498.5	15785.5	100%	100%	112%	103%	100%	102%
			xeon	cycle	1000000	269.0	266.7	270.8	270.3	285.0	374.8	267.0	271.0	270.6	270.9	283.7	353.8	99%	102%	100%	100%	100%	94%
					10000000	2341.8	2310.4	2311.1	2355.7	2361.5	2513.2	2344.4	2221.9	2368.5	2354.1	2252.8	2498.5	100%	96%	102%	100%	95%	99%
					100000000	22469.5	22746.4	22438.0	22980.6	22659.2	22562.3	22588.3	22448.4	22570.6	22975.2	22759.1	22784.9	101%	99%	101%	100%	100%	101%
				random	1000000	271.3	275.2	267.9	269.0	286.2	367.4	273.6	269.6	272.7	276.1	274.9	368.2	101%	98%	102%	103%	96%	100%

						10000000	2358.5	2329.8	2369.5	2315.2	2351.1	2570.1	2354.3	2340.2	2356.7	2334.3	2365.7	2545.3	100%	100%	99%	101%	101%	99%
						100000000	22528.6	22518.4	22543.4	22688.9	22631.2	22829.4	22424.5	22532.8	22485.0	22623.0	22845.5	22796.5	100%	100%	100%	100%	101%	100%
					sequential	1000000	270.7	266.7	268.7	276.4	272.1	320.1	271.7	272.1	268.7	269.1	274.7	318.3	100%	102%	100%	97%	101%	99%
						10000000	2319.0	2359.4	2324.1	2341.6	2257.2	2363.4	2302.4	2373.2	2307.3	2318.9	2336.5	2403.0	99%	101%	99%	99%	104%	102%
						100000000	22515.8	22490.5	22889.0	22386.3	22534.7	22729.3	22544.2	22461.1	22978.3	22538.7	22525.5	22720.0	100%	100%	100%	101%	100%	100%
			patched	i5	cycle	1000000	364.4	333.3	434.2	432.2	420.4	486.6	355.5	342.1	394.2	435.1	374.0	454.7	98%	103%	91%	101%	89%	93%
						10000000	3127.9	3221.9	3604.2	3172.9	3432.4	3309.9	3439.5	3172.3	3202.4	3258.6	3152.1	3566.6	110%	98%	89%	103%	92%	108%
						50000000	15430.4	15395.4	15579.6	15470.3	15493.7	16828.1	15504.9	15485.4	15585.6	15453.3	15598.4	15668.7	100%	101%	100%	100%	101%	93%
					random	1000000	385.7	370.7	358.8	331.3	390.2	437.8	418.9	359.4	360.3	332.9	342.0	441.2	109%	97%	100%	100%	88%	101%
						10000000	3204.3	3180.0	3147.3	3188.1	3187.9	3304.8	3195.0	3604.4	3167.6	3172.6	3172.7	3336.4	100%	113%	101%	100%	100%	101%
						50000000	15524.1	16712.8	15474.8	16601.1	16368.3	15674.3	15992.0	15497.2	15514.5	15977.3	15457.4	16198.7	103%	93%	100%	96%	94%	103%
					sequential	1000000	383.9	415.6	420.6	357.9	397.7	410.3	393.7	391.7	371.7	350.9	422.0	398.1	103%	94%	88%	98%	106%	97%
						10000000	3204.3	3149.6	3188.5	3208.8	3176.5	3270.8	3134.0	3161.6	3186.2	3094.8	3128.0	3147.4	98%	100%	100%	96%	98%	96%
						50000000	15520.2	15566.8	15656.7	15481.5	15500.9	16283.3	15521.6	16443.7	15489.0	15548.8	15581.3	16294.5	100%	106%	99%	100%	101%	100%
				xeon	cycle	1000000	268.2	268.4	272.1	274.8	284.0	357.4	264.9	272.4	272.0	273.4	244.6	357.5	99%	101%	100%	99%	86%	100%
						10000000	2358.7	2217.3	2372.5	2357.9	2280.6	2555.4	2348.5	2361.1	2361.3	2380.1	2376.4	2485.8	100%	106%	100%	101%	104%	97%
						100000000	22708.1	22690.7	22394.3	23182.1	22636.8	22854.4	22703.2	22700.6	22552.8	23090.1	22716.0	22572.4	100%	100%	101%	100%	100%	99%
					random	1000000	274.3	271.7	272.5	274.0	283.8	365.0	274.8	272.0	273.7	274.8	287.6	367.2	100%	100%	100%	100%	101%	101%
						10000000	2357.1	2348.7	2374.6	2294.6	2367.9	2518.0	2383.3	2348.1	2262.3	2361.4	2371.8	2521.4	101%	100%	95%	103%	100%	100%
						100000000	22687.4	22491.7	22578.1	22737.4	22751.4	22954.3	22640.3	22679.3	22632.9	22631.9	22772.3	22824.0	100%	101%	100%	100%	100%	99%
					sequential	1000000	267.9	268.4	272.2	272.1	279.2	319.3	270.1	271.3	272.6	268.9	280.5	323.0	101%	101%	100%	99%	100%	101%
						10000000	2301.8	2189.0	2332.5	2274.1	2285.3	2313.2	2327.5	2197.8	2318.3	2308.6	2280.1	2370.8	101%	100%	99%	102%	100%	102%
						100000000	22540.9	22652.0	23008.0	22618.4	22642.2	23712.7	22633.4	22618.4	22951.6	22509.8	22561.4	22826.9	100%	100%	100%	100%	100%	96%
	hash	bitmapscan	master	i5	cycle	1000000	12.0	13.2	25.9	167.6	1553.2	470.1	11.2	11.8	14.8	32.6	206.6	487.9	93%	89%	57%	19%	13%	104%
						10000000	11.1	12.8	26.8	177.5	1547.3	15310.6	11.3	12.1	14.4	34.2	198.4	1706.9	102%	95%	54%	19%	13%	11%
						50000000	12.1	14.2	27.6	164.7	1555.1	15377.7	13.5	13.5	15.8	32.3	183.6	1676.7	111%	96%	57%	20%	12%	11%
					random	1000000	11.7	13.0	25.4	178.9	848.8	475.7	11.2	12.0	14.5	32.1	186.5	815.0	96%	92%	57%	18%	22%	171%
						10000000	11.3	12.9	28.1	166.6	1590.3	7497.4	11.8	12.3	15.5	32.1	185.4	1516.4	104%	95%	55%	19%	12%	20%
						50000000	11.6	13.5	27.6	165.2	1566.0	15333.3	12.3	14.7	15.9	32.9	186.0	1643.7	106%	109%	57%	20%	12%	11%
					sequential	1000000	11.2	12.2	11.9	15.7	25.1	95.2	12.0	11.8	12.3	16.7	26.4	131.1	107%	97%	103%	107%	105%	138%
						10000000	11.5	11.2	11.9	17.7	24.3	94.5	11.9	11.5	12.5	14.5	25.1	130.9	103%	103%	105%	82%	103%	139%
						50000000	12.1	12.9	13.1	17.4	22.6	94.7	12.3	12.3	12.3	16.9	27.5	131.4	101%	96%	94%	97%	122%	139%
				xeon	cycle	1000000	13.6	14.5	24.6	117.9	294.2	409.9	12.9	13.9	15.6	28.1	154.7	571.3	95%	96%	63%	24%	53%	139%
						10000000	13.7	13.8	24.2	124.5	1046.6	3611.5	13.9	13.5	14.1	31.0	143.2	1335.0	101%	98%	58%	25%	14%	37%
						100000000	13.1	14.4	26.1	126.0	1102.2	10217.8	14.0	13.8	18.5	28.0	125.3	1295.9	107%	96%	71%	22%	11%	13%
					random	1000000	13.5	15.1	24.2	125.4	499.8	408.1	13.5	14.0	14.7	25.8	118.1	489.4	100%	93%	61%	21%	24%	120%
						10000000	14.0	14.5	24.7	122.6	1082.3	4836.9	13.1	13.6	14.5	28.1	143.0	1054.0	94%	93%	59%	23%	13%	22%
						100000000	14.0	16.7	26.3	128.2	1109.9	10722.7	13.8	14.6	16.3	29.8	128.4	1276.1	99%	87%	62%	23%	12%	12%
					sequential	1000000	13.7	13.4	13.5	16.0	25.1	101.0	14.0	14.3	14.3	15.3	26.0	113.1	102%	107%	106%	96%	104%	112%
						10000000	13.4	13.3	13.7	15.8	25.1	99.9	13.7	13.8	13.5	15.0	24.4	113.4	102%	104%	98%	95%	97%	114%
						100000000	14.7	13.5	15.3	16.2	25.8	101.4	14.1	13.8	14.5	15.3	27.2	112.3	96%	103%	95%	94%	105%	111%
			patched	i5	cycle	1000000	12.3	14.8	26.4	165.6	1598.5	466.4	11.8	13.2	13.8	32.4	191.9	505.5	96%	89%	52%	20%	12%	108%
						10000000	11.6	13.4	26.4	168.9	1540.1	15497.5	11.8	12.1	14.4	31.4	184.8	1686.6	102%	90%	55%	19%	12%	11%
						50000000	12.0	14.0	29.2	167.8	1557.1	15647.5	12.6	14.9	15.7	33.0	184.5	1691.5	105%	106%	54%	20%	12%	11%
					random	1000000	12.1	13.0	26.0	171.4	812.0	488.4	11.7	12.6	14.8	32.8	173.0	813.6	97%	97%	57%	19%	21%	167%
						10000000	12.0	13.0	24.2	170.5	1590.4	7764.2	11.0	12.8	13.8	33.2	189.0	1528.7	92%	98%	57%	19%	12%	20%
						50000000	11.9	13.3	31.0	165.2	1576.9	15351.5	12.4	12.8	16.1	33.4	184.1	1667.5	104%	96%	52%	20%	12%	11%
					sequential	1000000	11.4	11.7	12.6	14.9	25.0	95.9	11.8	12.0	12.0	15.2	27.6	133.9	103%	103%	95%	102%	110%	140%
						10000000	11.5	11.6	12.6	16.1	22.9	96.9	12.0	11.9	12.3	14.7	26.0	131.7	105%	103%	98%	91%	113%	136%
						50000000	12.3	12.8	12.8	16.9	22.7	94.6	11.9	12.4	12.1	15.0	28.9	131.5	97%	97%	95%	89%	127%	139%
				xeon	cycle	1000000	13.1	15.3	24.6	122.3	294.2	399.3	14.2	14.1	15.4	28.0	152.6	561.1	108%	92%	63%	23%	52%	141%
						10000000	13.0	14.5	24.6	126.2	1020.0	3632.6	14.0	14.3	15.4	27.7	145.8	1317.5	108%	99%	63%	22%	14%	36%

					100000000	12.2	15.0	27.5	124.6	1102.6	10109.9	13.5	14.5	18.1	27.6	125.9	1304.9	110%	97%	66%	22%	11%	13%
				random	1000000	13.1	14.4	25.9	126.0	505.3	399.2	13.9	13.2	14.3	27.2	116.6	485.4	106%	91%	55%	22%	23%	122%
					10000000	14.2	14.1	24.8	123.6	1090.9	4777.4	13.6	13.9	16.1	26.8	141.5	1048.4	95%	99%	65%	22%	13%	22%
					100000000	13.2	16.0	27.4	127.0	1112.5	10785.3	14.1	14.7	16.2	28.7	126.1	1271.2	107%	92%	59%	23%	11%	12%
				sequential	1000000	13.5	13.6	14.3	15.2	25.7	101.7	13.4	13.0	13.7	15.6	25.5	111.5	99%	96%	95%	103%	99%	110%
					10000000	12.7	13.2	13.6	15.5	24.6	99.4	12.7	12.1	13.4	14.1	24.4	111.9	100%	92%	99%	91%	99%	113%
					100000000	14.2	14.3	14.0	16.0	25.4	99.4	13.7	12.7	14.6	15.7	25.7	111.9	97%	89%	104%	98%	101%	113%
	indexscan	master	i5	cycle	1000000	11.2	12.2	24.9	164.7	1535.9	6846.0	11.2	12.2	26.9	164.3	1540.5	6848.4	100%	99%	108%	100%	100%	100%
					10000000	11.0	13.4	25.7	176.9	1533.6	15393.7	11.0	12.3	25.1	173.8	1585.1	15240.7	100%	92%	98%	98%	103%	99%
					50000000	12.8	14.6	28.9	166.7	1551.2	15315.3	11.6	13.5	28.3	165.5	1549.8	15458.5	91%	92%	98%	99%	100%	101%
				random	1000000	11.2	12.1	25.5	174.7	1391.9	6245.6	11.1	13.0	24.4	166.9	1396.7	6354.1	99%	107%	96%	96%	100%	102%
					10000000	11.3	13.0	27.8	172.2	1643.4	7483.0	11.1	12.6	28.2	163.9	1575.2	7551.0	98%	97%	101%	95%	96%	101%
					50000000	11.7	14.4	29.2	171.0	1561.1	15271.9	11.5	14.2	28.2	169.2	1570.8	15393.5	98%	98%	97%	99%	101%	101%
				sequential	1000000	11.0	11.6	12.7	20.3	30.6	102.8	10.7	11.8	11.4	19.6	29.8	102.4	97%	102%	90%	97%	97%	100%
					10000000	11.2	12.4	12.7	14.8	22.7	97.3	11.0	11.6	12.0	16.6	25.3	93.9	98%	93%	95%	113%	112%	97%
					50000000	12.1	11.8	12.6	14.8	23.0	97.6	11.5	12.6	12.7	15.4	24.6	95.7	95%	107%	101%	104%	107%	98%
			xeon	cycle	1000000	13.2	14.7	24.0	121.5	1089.6	550.1	12.6	14.2	24.4	121.6	1090.4	540.8	96%	97%	102%	100%	100%	98%
					10000000	12.5	13.6	23.9	125.0	1028.8	3725.9	12.9	14.1	22.9	125.1	1024.8	3698.7	104%	104%	96%	100%	100%	99%
					100000000	12.4	14.1	27.2	125.1	1096.4	10163.3	13.6	15.1	27.5	123.8	1093.1	10129.6	109%	107%	101%	99%	100%	100%
				random	1000000	13.1	15.1	23.6	119.9	933.5	3435.8	13.9	14.7	23.6	120.1	934.1	3406.4	106%	97%	100%	100%	100%	99%
					10000000	13.8	13.5	24.8	121.4	1075.3	4741.4	13.3	14.3	24.3	159.2	1083.8	4786.4	97%	106%	98%	131%	101%	101%
					100000000	14.1	15.9	27.0	134.6	1093.9	10766.8	13.6	15.6	26.3	130.6	1110.0	10767.6	96%	98%	97%	97%	101%	100%
				sequential	1000000	13.4	13.8	13.2	19.5	28.5	107.9	14.1	15.1	14.2	18.4	28.1	105.8	105%	109%	108%	94%	99%	98%
					10000000	12.0	12.8	13.1	15.1	23.8	99.2	14.7	13.1	13.8	15.2	23.2	99.5	122%	103%	106%	100%	97%	100%
					100000000	14.2	13.0	13.9	15.3	24.2	101.8	13.8	13.6	14.5	15.2	25.3	100.2	98%	104%	104%	99%	105%	99%
		patched	i5	cycle	1000000	11.7	12.5	26.0	165.2	1574.5	6874.6	11.7	12.9	14.5	31.8	195.2	967.5	100%	103%	56%	19%	12%	14%
					10000000	11.4	12.8	25.6	173.1	1545.5	15333.0	12.2	12.9	13.9	32.3	187.4	1759.5	107%	101%	54%	19%	12%	11%
					50000000	12.3	14.7	28.1	174.4	1553.8	15374.9	11.7	13.3	16.0	33.3	184.1	1682.7	95%	91%	57%	19%	12%	11%
				random	1000000	11.1	12.4	27.4	164.3	1392.3	6316.1	12.5	12.7	15.2	30.6	170.9	931.4	113%	103%	56%	19%	12%	15%
					10000000	11.2	13.4	26.2	163.5	1585.9	7512.6	11.3	12.1	13.5	31.5	184.0	1532.0	101%	90%	52%	19%	12%	20%
					50000000	12.1	14.5	29.3	167.4	1570.6	15176.6	11.6	13.1	17.2	34.0	183.7	1653.9	96%	91%	58%	20%	12%	11%
				sequential	1000000	12.1	11.7	11.8	22.1	29.9	106.2	12.1	11.2	11.9	17.9	51.5	400.1	100%	95%	101%	81%	172%	377%
					10000000	11.4	11.9	13.1	14.9	24.6	93.6	11.1	11.4	12.7	18.0	60.2	458.7	97%	96%	96%	121%	244%	490%
					50000000	12.5	12.6	12.6	15.6	24.7	94.8	11.9	12.8	12.2	21.1	61.5	451.3	95%	102%	97%	135%	248%	476%
			xeon	cycle	1000000	13.1	15.2	24.2	124.8	1090.7	546.1	13.0	13.7	15.3	27.9	119.8	469.9	99%	90%	63%	22%	11%	86%
					10000000	14.4	14.8	23.8	122.9	1020.6	3765.7	13.6	13.7	14.9	25.6	138.0	1130.5	95%	93%	63%	21%	14%	30%
					100000000	12.2	14.7	27.4	124.5	1094.1	10111.7	14.3	14.5	18.0	26.6	122.8	1273.1	117%	99%	66%	21%	11%	13%
				random	1000000	13.6	13.9	22.7	124.0	937.4	3401.2	13.2	13.2	13.7	27.3	117.0	1061.2	97%	95%	60%	22%	12%	31%
					10000000	13.6	14.1	24.1	129.8	1082.2	4741.0	13.4	13.7	14.5	27.1	141.1	1052.1	98%	97%	60%	21%	13%	22%
					100000000	13.2	15.7	27.3	127.6	1115.8	10723.8	14.3	14.8	16.6	28.7	123.3	1247.8	108%	95%	61%	22%	11%	12%
				sequential	1000000	12.9	13.2	14.6	18.7	28.7	107.5	13.5	13.0	13.9	16.7	41.8	299.9	105%	99%	95%	89%	146%	279%
					10000000	12.6	13.6	13.2	15.1	24.1	101.2	12.2	12.8	13.6	16.1	41.4	219.7	96%	95%	103%	107%	172%	217%
					100000000	13.6	13.7	14.2	15.7	24.9	100.6	14.1	13.2	14.6	17.8	41.2	216.7	104%	97%	103%	113%	165%	215%
	seqscan	master	i5	cycle	1000000	374.5	343.1	353.3	375.5	412.1	370.0	391.4	334.0	322.0	347.3	375.2	420.9	105%	97%	91%	92%	91%	114%
					10000000	3074.3	3135.2	3166.9	3688.7	3121.5	3168.0	3662.4	3123.4	3156.8	3589.0	3358.7	3115.3	119%	100%	100%	97%	108%	98%
					50000000	16380.7	15258.0	15325.8	15231.0	15259.5	15183.5	15198.7	15219.8	15321.3	15902.8	15266.8	15188.5	93%	100%	100%	104%	100%	100%
				random	1000000	449.4	377.9	356.9	369.0	379.8	419.2	343.2	380.5	363.9	410.0	378.9	437.6	76%	101%	102%	111%	100%	104%
					10000000	3558.0	3143.3	3158.1	3133.5	3147.9	3176.9	3114.6	3121.4	3144.9	3151.0	3140.0	3281.5	88%	99%	100%	101%	100%	103%
					50000000	15836.8	15243.9	15236.0	15242.6	15214.7	15298.5	15224.1	15172.3	15186.7	15270.2	15271.3	15218.4	96%	100%	100%	100%	100%	99%
				sequential	1000000	325.2	372.1	347.3	319.3	365.4	372.4	369.2	328.7	376.1	370.2	361.2	363.8	114%	88%	108%	116%	99%	98%
					10000000	3207.2	3158.6	3382.1	3098.1	3206.3	3218.3	3140.4	3155.8	3720.8	3124.1	3179.5	3238.5	98%	100%	110%	101%	99%	101%
					50000000	15850.7	15202.5	15268.4	15244.1	15276.6	15894.7	15241.1	15250.3	15275.2	15232.2	15214.9	15294.7	96%	100%	100%	100%	100%	96%

			xeon	cycle	1000000	252.6	256.1	257.1	253.9	193.6	300.2	252.3	254.4	253.6	255.1	255.8	296.3	100%	99%	99%	101%	132%	99%
					10000000	2207.5	2177.0	2201.9	2193.8	2195.9	2224.0	2195.3	2186.2	2177.1	2189.6	2210.2	2227.1	99%	100%	99%	100%	101%	100%
					100000000	21455.9	21478.2	21549.4	21524.5	21497.1	21605.8	21594.8	21582.8	21555.1	21555.1	21497.6	21653.9	101%	100%	100%	100%	100%	100%
				random	1000000	251.0	242.9	252.9	248.5	189.7	284.5	254.3	244.8	251.5	246.8	189.9	265.6	101%	101%	99%	99%	100%	93%
					10000000	2197.3	2180.1	2201.2	2183.8	2186.8	2232.9	2215.8	2197.2	2197.8	2196.6	2167.2	2224.9	101%	101%	100%	101%	99%	100%
					100000000	21889.6	21556.7	21412.8	21557.0	21495.2	21624.4	21737.0	21485.4	21702.7	21512.6	21507.4	21624.6	99%	100%	101%	100%	100%	100%
				sequential	1000000	256.0	255.1	254.5	252.3	256.1	222.6	253.9	256.0	253.3	254.3	195.9	228.8	99%	100%	100%	101%	76%	103%
					10000000	2217.2	2183.9	2188.5	2188.8	2201.3	2174.5	2213.3	2194.7	2201.2	2183.0	2195.1	2103.4	100%	100%	101%	100%	100%	97%
					100000000	21481.7	21537.0	21705.3	21665.5	21368.1	21374.6	21592.6	21383.5	21621.3	21620.8	21331.7	21407.4	101%	99%	100%	100%	100%	100%
			patched	i5	cycle	1000000	408.8	374.0	367.1	381.1	395.2	346.7	381.2	346.0	321.2	366.2	381.4	387.1	93%	93%	87%	96%	96%	112%
					10000000	3826.4	3158.7	3181.3	3350.3	3179.9	3152.5	3403.9	3145.0	3119.2	3107.4	3140.8	3454.1	89%	100%	98%	93%	99%	110%
					50000000	15164.2	16746.8	15296.0	15230.7	15254.1	15209.8	15920.7	15208.7	15210.0	15236.8	15244.4	15290.0	105%	91%	99%	100%	100%	101%
				random	1000000	325.3	320.4	346.2	385.9	356.2	403.9	355.9	328.0	356.5	376.8	338.3	379.3	109%	102%	103%	98%	95%	94%
					10000000	3136.1	3104.4	3133.4	3146.4	3126.3	3300.5	3159.1	3104.0	3197.1	3141.5	3155.7	3132.4	101%	100%	102%	100%	101%	95%
					50000000	15222.9	15235.0	15197.4	15219.7	15172.6	15297.7	15208.2	15203.4	15794.9	15306.4	15268.7	15688.0	100%	100%	104%	101%	101%	103%
				sequential	1000000	355.2	361.1	374.7	370.5	339.1	408.1	356.6	339.9	364.8	348.1	359.7	348.1	100%	94%	97%	94%	106%	85%
					10000000	3169.3	3141.9	3301.7	3148.6	3166.2	3121.1	3176.5	3126.6	3320.3	3138.0	3174.4	3168.3	100%	100%	101%	100%	100%	102%
					50000000	15282.9	15218.8	15298.8	15306.0	15775.1	15278.5	15218.6	15223.4	16475.4	15321.5	15200.6	15305.1	100%	100%	108%	100%	96%	100%
				xeon	cycle	1000000	255.5	255.2	256.2	256.4	258.3	254.0	255.9	255.4	255.2	256.6	193.9	284.5	100%	100%	100%	100%	75%	112%
					10000000	2239.8	2181.1	2202.2	2202.3	2215.3	2220.1	2211.3	2179.6	2201.8	2198.5	2205.2	2231.2	99%	100%	100%	100%	100%	101%
					100000000	21593.6	21677.4	21628.9	21652.2	21571.4	21738.4	21520.9	21502.0	21582.2	21614.7	21557.3	21729.4	100%	99%	100%	100%	100%	100%
				random	1000000	252.6	241.0	255.8	247.4	188.1	279.7	257.1	243.9	251.5	243.8	249.0	250.7	102%	101%	98%	99%	132%	90%
					10000000	2214.7	2188.6	2214.3	2175.9	2190.8	2236.7	2216.4	2199.0	2210.1	2187.7	2205.6	2226.2	100%	100%	100%	101%	101%	100%
					100000000	21518.4	21687.6	21602.6	21683.1	21592.6	21735.8	21727.9	21757.0	21581.7	21650.2	21659.4	21748.6	101%	100%	100%	100%	100%	100%
				sequential	1000000	253.5	255.8	254.3	254.1	257.9	222.8	255.1	251.5	254.3	256.3	254.6	225.1	101%	98%	100%	101%	99%	101%
					10000000	2193.6	2202.4	2195.9	2179.6	2084.5	2130.3	2199.5	2192.7	2191.6	2186.9	2168.5	2219.3	100%	100%	100%	100%	104%	104%
					100000000	21675.7	21694.0	21777.4	21638.3	21497.8	21634.0	21838.2	21547.3	21698.7	21831.0	21358.1	21519.3	101%	99%	100%	101%	99%	99%