

MEDIAN of duration							prefetch matches																	
caching	test	scan_type	build	machine	dataset	rows	0								32									
							1	10	100	1000	10000	100000	1	10	100	1000	10000	100000	1	10	100	1000	10000	100000
cached	btree	bitmapscan	master	i5	cycle	1000000	7.2	7.2	7.4	8.8	22.9	98.1	7.1	7.1	7.4	8.9	23.7	104.4	99%	99%	100%	101%	104%	106%
						10000000	7.2	7.1	7.3	8.9	22.8	176.4	7.1	7.2	7.3	9.0	23.8	185.9	99%	100%	100%	101%	104%	105%
					random	50000000	7.2	7.3	7.5	9.1	22.8	168.4	7.2	7.2	7.3	9.2	23.7	178.9	100%	99%	97%	102%	104%	106%
						1000000	7.1	7.2	7.4	9.0	21.3	95.0	7.1	7.2	7.5	9.1	22.6	102.3	101%	100%	100%	101%	106%	108%
						10000000	7.1	7.3	7.3	9.0	22.8	153.2	7.3	7.2	7.4	9.0	23.6	164.7	102%	99%	101%	101%	103%	108%
						50000000	7.0	7.3	7.5	9.0	22.8	163.6	7.2	7.2	7.4	9.2	23.9	174.8	102%	100%	98%	102%	105%	107%
					sequential	1000000	7.3	7.2	7.2	7.7	11.9	53.4	7.2	7.1	7.3	7.7	12.0	54.1	98%	98%	101%	100%	101%	101%
						10000000	7.2	7.2	7.2	7.7	11.9	53.8	7.1	7.2	7.2	7.8	12.0	54.4	99%	100%	100%	100%	101%	101%
						50000000	7.1	7.2	7.2	7.9	11.9	53.8	7.2	7.2	7.3	7.7	12.1	54.5	101%	101%	101%	98%	101%	101%
				xeon	cycle	1000000	9.1	9.1	9.5	10.8	25.0	123.7	9.5	9.3	9.3	11.3	26.8	132.7	103%	102%	97%	105%	107%	107%
						10000000	8.8	9.3	9.0	11.7	29.9	200.2	9.0	9.1	9.2	11.4	26.9	227.7	103%	98%	103%	98%	90%	114%
					random	100000000	8.8	9.5	9.0	11.5	27.0	203.9	8.8	9.0	9.0	9.9	27.3	214.1	100%	94%	100%	86%	101%	105%
						1000000	8.8	8.7	9.0	10.8	25.1	109.3	9.1	9.2	9.4	10.1	25.6	126.3	104%	106%	103%	94%	102%	115%
						10000000	8.7	9.0	8.5	10.5	26.3	185.2	8.9	9.5	8.7	10.8	26.5	202.3	102%	106%	102%	103%	101%	109%
						100000000	8.6	9.1	9.0	10.8	28.7	199.1	9.0	8.7	9.1	10.6	27.3	213.7	104%	96%	102%	98%	95%	107%
					sequential	1000000	9.3	9.8	9.3	9.7	15.4	65.5	9.0	9.3	9.3	10.3	14.6	66.1	97%	95%	100%	106%	95%	101%
						10000000	9.0	8.8	9.2	9.6	14.5	65.5	9.2	8.8	9.2	9.3	15.0	66.3	102%	100%	101%	96%	103%	101%
						100000000	9.3	9.9	8.8	9.6	14.3	65.6	9.4	9.5	9.4	9.2	14.5	66.4	101%	97%	106%	96%	101%	101%
			patched	i5	cycle	1000000	7.1	7.2	7.4	8.8	22.6	96.7	7.1	7.2	7.3	9.0	23.4	103.5	101%	101%	99%	101%	104%	107%
						10000000	7.2	7.2	7.3	8.9	22.7	174.3	7.1	7.2	7.4	8.9	23.4	174.6	99%	99%	101%	100%	103%	100%
					random	50000000	7.2	7.2	7.3	9.1	22.6	164.8	7.1	7.2	7.4	9.3	23.4	175.2	100%	100%	101%	102%	103%	106%
						1000000	7.0	7.1	7.3	8.9	21.2	94.6	7.1	7.2	7.4	9.1	22.5	101.1	102%	101%	101%	102%	106%	107%
						10000000	7.1	7.2	7.4	8.8	22.4	151.9	7.1	7.2	7.4	8.9	23.4	163.6	99%	101%	100%	101%	104%	108%
						50000000	7.1	7.1	7.4	9.1	22.4	162.4	7.2	7.2	7.4	9.2	23.6	172.4	101%	101%	100%	102%	105%	106%
					sequential	1000000	7.1	7.1	7.2	7.7	12.0	53.1	7.2	7.0	7.1	7.7	12.0	53.9	101%	98%	99%	101%	100%	102%
						10000000	7.2	7.3	7.1	7.5	11.9	53.8	7.2	7.2	7.2	7.8	12.0	54.2	101%	99%	102%	103%	101%	101%
				xeon	cycle	1000000	7.1	7.1	7.3	7.7	12.0	53.6	7.2	7.2	7.3	7.6	12.0	54.5	101%	101%	100%	99%	99%	102%
						1000000	9.0	9.4	8.8	11.7	25.8	123.7	9.1	9.2	9.1	11.7	27.5	132.6	101%	97%	103%	100%	107%	107%
					random	10000000	8.2	9.4	8.6	11.1	28.3	199.6	8.8	9.3	8.4	10.9	26.8	214.7	108%	98%	97%	98%	95%	108%
						100000000	9.4	9.3	8.9	10.7	26.7	200.7	9.3	9.4	8.4	11.0	27.6	215.9	99%	101%	94%	102%	103%	108%
						1000000	8.9	8.6	9.3	11.6	25.2	118.3	8.9	9.1	9.2	10.0	26.2	119.3	100%	106%	99%	86%	104%	101%
						10000000	9.3	9.4	8.8	10.6	27.3	172.1	9.0	9.1	8.7	10.9	27.2	201.3	97%	98%	98%	102%	100%	117%
					sequential	100000000	9.0	9.5	10.0	10.7	27.7	185.2	8.7	8.4	9.2	10.6	27.5	212.9	97%	89%	92%	99%	100%	115%
						1000000	9.2	9.2	9.4	10.0	14.7	62.6	8.9	8.9	9.4	10.0	14.6	64.6	96%	97%	101%	101%	99%	103%
			indexscan	master	i5	10000000	9.0	8.9	9.3	9.6	15.5	64.7	9.2	9.3	9.2	9.7	14.2	65.6	101%	105%	99%	101%	91%	101%
						100000000	9.5	9.3	9.5	9.4	14.4	66.2	9.3	9.5	9.5	9.7	14.8	66.8	98%	103%	100%	103%	103%	101%
					random	1000000	7.1	7.2	7.4	8.6	19.6	83.8	7.2	7.2	7.4	8.5	19.4	81.7	101%	100%	101%	98%	99%	97%
						10000000	7.0	7.1	7.3	8.6	19.1	119.2	7.1	7.3	7.3	8.5	19.1	119.9	101%	102%	101%	98%	100%	101%
						50000000	7.2	7.2	7.4	8.8	19.5	120.0	7.1	7.0	7.4	8.7	19.2	119.9	99%	98%	101%	99%	99%	100%
						1000000	7.2	7.1	7.3	8.8	18.4	78.9	7.1	7.1	7.4	8.7	18.2	79.0	98%	100%	101%	98%	99%	100%
					sequential	10000000	7.2	7.2	7.5	8.7	19.4	110.4	7.1	7.1	7.3	8.4	19.3	111.1	99%	98%	97%	97%	100%	101%
						50000000	7.3	7.2	7.6	8.7	19.6	118.0	7.1	7.2	7.3	8.8	19.4	118.3	98%	99%	97%	101%	99%	100%
			1000000			7.1	7.2	7.2	7.6	12.2	54.4	7.1	7.2	7.2	7.7	11.9	54.4	100%	101%	100%	101%	98%	100%	
			10000000			7.1	7.0	7.2	7.8	12.2	54.6	7.1	7.1	7.2	7.8	12.1	54.5	100%	100%	99%	99%	99%	100%	
			50000000			7.3	7.3	7.2	7.8	12.3	54.7	7.2	7.2	7.3	7.8	12.2	54.7	99%	99%	101%	100%	99%	100%	
			xeon	cycle	1000000	9.4	9.4	9.3	10.2	21.7	103.2	9.4	9.1	9.2	10.9	21.8	105.9	100%	97%	100%	107%	100%	103%	
					10000000	8.7	8.9	8.6	10.7	22.7	155.1	8.8	9.1	8.8	11.3	25.2	154.2	102%	103%	103%	106%	111%	99%	
				100000000	8.4	8.8	9.1	11.6	22.9	158.3	8.6	9.3	9.1	9.9	23.1	157.8	102%	105%	100%	86%	101%	100%		
			random	1000000	8.6	8.7	9.0	10.3	21.3	95.1	9.1	9.1	9.1	9.4	21.5	97.9	105%	104%	101%	91%	101%	103%		

			sequential	10000000	8.4	8.8	8.3	10.0	22.4	144.7	9.3	9.7	8.6	10.3	21.9	144.2	111%	109%	103%	103%	98%	100%	
				100000000	8.4	8.6	8.9	10.3	24.2	155.1	9.1	9.2	9.2	10.6	24.0	154.8	108%	108%	103%	103%	99%	100%	
				1000000	9.4	8.9	9.4	9.4	15.1	66.2	8.9	9.1	9.6	10.2	15.6	66.4	95%	102%	102%	109%	103%	100%	
				10000000	9.4	8.7	8.9	9.6	14.4	66.8	9.3	8.7	8.9	9.2	15.2	67.0	99%	100%	100%	95%	105%	100%	
				100000000	9.2	9.0	8.7	9.0	14.3	65.8	9.3	9.8	9.2	9.5	15.0	66.4	101%	109%	105%	105%	105%	101%	
		patched	i5	cycle	1000000	7.1	7.1	7.3	8.6	19.1	80.5	7.1	7.2	7.3	8.6	20.0	82.5	99%	101%	100%	101%	104%	102%
					10000000	7.1	7.1	7.3	8.5	19.0	118.5	7.2	7.2	7.3	8.7	20.2	129.0	101%	102%	100%	102%	107%	109%
					50000000	7.1	7.3	7.4	8.8	19.3	118.8	7.2	7.3	7.3	8.9	20.1	129.7	101%	101%	99%	101%	104%	109%
					1000000	7.1	7.2	7.3	8.7	18.0	79.0	7.2	7.0	7.3	8.7	19.6	82.2	100%	97%	100%	101%	109%	104%
					10000000	7.0	7.2	7.2	8.5	18.9	109.8	7.2	7.2	7.4	8.7	20.3	120.9	102%	100%	102%	102%	107%	110%
			sequential	50000000	7.1	7.3	7.4	8.6	19.5	117.6	7.3	7.3	7.3	8.9	20.3	127.2	102%	101%	99%	103%	104%	108%	
				1000000	7.0	7.1	7.2	7.7	12.1	53.2	7.2	7.1	7.3	7.7	12.2	55.0	102%	101%	102%	100%	101%	103%	
				10000000	7.1	7.2	7.2	7.8	12.1	53.6	7.2	7.1	7.1	7.7	12.4	57.1	102%	99%	100%	98%	102%	107%	
				50000000	7.1	7.1	7.2	7.8	12.2	54.1	7.2	7.2	7.2	7.8	12.6	56.2	102%	102%	100%	100%	103%	104%	
				xeon	cycle	1000000	9.1	9.2	8.8	11.6	21.9	106.4	9.1	9.3	9.0	11.3	24.2	108.4	99%	101%	103%	97%	110%
	10000000	8.6	9.2			8.7	10.7	23.8	154.0	8.7	9.5	8.7	10.3	23.7	169.7	101%	103%	100%	96%	100%	110%		
	100000000	8.8	9.1			8.7	9.9	22.9	159.9	9.3	9.5	8.3	10.5	24.1	169.5	105%	105%	96%	107%	105%	106%		
	1000000	8.9	8.4			9.2	11.1	21.8	92.8	9.4	9.0	9.2	9.7	21.9	97.4	105%	107%	99%	88%	101%	105%		
	10000000	8.9	9.4			8.3	10.3	24.0	140.2	8.9	8.6	8.7	10.3	23.3	159.8	100%	91%	104%	100%	97%	114%		
	random	100000000	8.8		9.3	9.6	10.3	23.8	155.4	9.1	8.7	9.2	10.5	23.6	169.7	104%	93%	96%	102%	99%	109%		
		1000000	9.2		8.9	9.6	10.4	15.0	66.8	8.9	8.8	9.2	9.7	15.2	67.0	97%	99%	96%	94%	102%	100%		
		10000000	9.1		8.6	9.0	9.3	15.2	66.6	9.0	9.6	9.5	9.7	15.0	66.7	99%	111%	106%	104%	99%	100%		
		100000000	9.3		9.9	9.2	9.4	14.8	67.1	9.1	9.2	9.4	9.5	15.6	66.0	98%	93%	102%	101%	106%	98%		
seqscan		master	i5		cycle	1000000	143.2	143.3	143.7	144.2	148.6	184.0	142.8	151.4	144.0	144.4	149.7	185.0	100%	106%	100%	100%	101%
	10000000			1373.5		1343.8	1343.4	1359.8	1348.6	1407.0	1345.0	1347.6	1372.5	1346.6	1362.7	1403.0	98%	100%	102%	99%	101%	100%	
	50000000			16516.4		15210.6	15221.9	15176.0	15192.0	15253.8	15674.3	15075.9	15452.8	15226.7	15185.8	15227.5	95%	99%	102%	100%	100%	100%	
	1000000			144.7		145.4	143.1	144.7	149.0	184.4	143.1	143.2	143.1	143.5	150.3	187.2	99%	98%	100%	99%	101%	101%	
	10000000			1344.4		1347.3	1358.7	1349.1	1358.2	1406.5	1335.6	1362.2	1336.4	1342.6	1356.6	1410.5	99%	101%	98%	100%	100%	100%	
	random			50000000	15174.2	15710.4	15184.9	15466.7	15177.2	16337.1	15238.6	15163.2	15172.3	15071.9	15171.9	15183.4	100%	97%	100%	97%	100%	93%	
				1000000	142.9	143.1	142.6	143.2	147.4	182.0	144.9	149.1	142.8	143.4	146.2	143.0	101%	104%	100%	100%	99%	79%	
				10000000	1341.4	1349.5	1354.5	1339.8	1344.9	1376.8	1348.8	1343.6	1357.3	1342.2	1355.0	1386.0	101%	100%	100%	100%	101%	101%	
				50000000	15140.3	15201.9	15152.6	15239.6	15219.7	15201.0	15182.8	15236.8	15203.9	15245.2	16568.9	15223.1	100%	100%	100%	100%	109%	100%	
	xeon		cycle	1000000	159.0	160.4	160.1	161.5	157.9	213.5	161.3	158.5	160.4	161.2	166.7	213.4	101%	99%	100%	100%	106%	100%	
				10000000	1493.5	1473.3	1486.7	1500.8	1496.1	1572.4	1466.0	1474.1	1468.4	1496.9	1510.3	1563.2	98%	100%	99%	100%	101%	99%	
				100000000	14682.8	14973.8	14742.0	14830.5	14965.7	15130.0	14808.4	15107.0	14737.9	14785.0	14793.5	15062.5	101%	101%	100%	100%	99%	100%	
				1000000	160.8	160.8	161.6	161.1	167.5	211.9	161.3	159.0	160.2	161.2	170.6	212.8	100%	99%	99%	100%	102%	100%	
10000000	1475.0	1496.7		1476.0	1474.3	1502.5	1574.1	1467.6	1485.1	1468.9	1477.8	1500.7	1569.1	99%	99%	100%	100%	100%	100%				
random	100000000	14793.1	14719.1	14765.6	14914.9	14919.0	15041.7	14650.5	14748.7	14775.3	14682.8	15018.3	15017.1	99%	100%	100%	98%	101%	100%				
	1000000	161.0	159.9	159.2	161.3	165.4	159.3	160.7	159.7	159.2	161.6	164.1	199.5	100%	100%	100%	100%	99%	125%				
	10000000	1468.0	1475.4	1463.5	1497.9	1480.0	1506.0	1481.6	1466.2	1464.7	1495.6	1486.5	1539.0	101%	99%	100%	100%	100%	102%				
	100000000	14936.5	14766.1	14850.8	14836.1	14681.1	14730.1	15283.6	14865.9	14673.0	14670.5	14696.7	14705.0	102%	101%	99%	99%	100%	100%				
patched	i5	cycle	1000000	142.0	142.2	143.0	144.3	148.0	182.1	142.9	142.5	142.3	143.5	148.2	143.2	101%	100%	99%	99%	100%	79%		
			10000000	1340.8	1337.6	1348.2	1344.8	1351.0	1393.3	1338.2	1343.2	1353.7	1335.3	1359.1	1403.9	100%	100%	100%	99%	101%	101%		
			50000000	15215.5	15186.1	15110.2	15203.3	15591.3	15242.3	15244.5	15124.4	15061.2	15136.3	15886.0	15238.9	100%	100%	100%	100%	102%	100%		
			1000000	142.7	142.9	142.1	143.6	150.9	183.2	141.9	144.4	143.4	142.6	150.4	163.6	99%	101%	101%	99%	100%	89%		
			10000000	1338.5	1336.8	1339.6	1335.9	1346.2	1388.3	1337.8	1334.0	1338.3	1339.1	1348.9	1387.7	100%	100%	100%	100%	100%	100%		
		random	50000000	15180.6	15239.5	15153.3	15129.1	15196.9	17608.3	15225.2	15172.3	15173.5	15147.6	15190.8	15213.9	100%	100%	100%	100%	100%	86%		
			1000000	142.0	142.7	142.5	141.9	145.7	177.8	142.8	146.5	142.8	143.7	146.5	142.3	101%	103%	100%	101%	101%	80%		
			10000000	1340.7	1346.2	1344.1	1333.5	1338.5	1397.8	1335.1	1336.9	1347.3	1366.7	1336.1	1375.8	100%	99%	100%	102%	100%	98%		
			50000000	15114.2	16217.3	15223.7	15210.4	15164.1	15252.0	15130.5	15142.3	15184.2	16834.4	15208.4	15147.2	100%	93%	100%	111%	100%	99%		
			xeon	cycle	1000000	159.3	157.6	155.4	158.3	164.7	159.4	156.0	158.3	157.8	159.0	164.2	159.8	98%	100%	102%	100%	100%	100%
10000000	1463.9	1462.2			1475.2	1479.2	1468.6	1548.6	1456.5	1463.7	1450.8	1478.1	1472.7	1580.8	99%	100%	98%	100%	100%	102%			

					random	100000000	14551.2	14605.9	14588.2	14492.3	14517.6	14865.1	14740.7	14720.9	15147.0	14605.3	14733.1	14906.7	101%	101%	104%	101%	101%	100%					
						10000000	158.2	156.6	157.3	159.7	163.1	210.4	157.3	156.7	157.9	165.9	165.0	207.3	99%	100%	100%	104%	101%	99%					
						100000000	1472.7	1471.0	1468.5	1461.1	1474.1	1551.9	1525.3	1472.7	1459.1	1460.7	1472.7	1554.4	104%	100%	99%	100%	100%	100%					
						1000000000	14495.7	14561.8	14706.2	14499.0	14738.6	14762.5	14652.4	15004.3	14872.9	14667.9	14592.3	14872.7	101%	103%	101%	101%	99%	101%					
						10000000	155.2	157.7	161.6	159.9	162.0	158.2	161.5	156.0	158.4	160.4	166.7	156.3	104%	99%	98%	100%	103%	99%					
						100000000	1464.6	1471.6	1490.4	1462.4	1472.7	1486.9	1455.6	1462.9	1462.5	1456.0	1522.0	1490.2	99%	99%	98%	100%	103%	100%					
						1000000000	14696.6	14648.5	14667.4	14636.9	14756.1	14609.7	14660.4	14735.8	14744.4	14642.5	14844.6	14617.1	100%	101%	101%	100%	101%	100%					
						btree-sort	bitmaps-can	master	i5	cycle	10000000	84.0	93.0	89.4	83.8	112.8	247.7	103.2	99.0	81.9	87.8	116.4	239.5	123%	107%	92%	105%	103%	97%
											100000000	1371.0	1547.4	1418.2	1634.8	1637.5	1932.0	1441.6	1317.6	1718.7	1787.9	1632.1	2118.0	105%	85%	121%	109%	100%	110%
											500000000	8528.2	9894.1	8747.9	11507.3	21724.0	27081.7	16357.7	21510.3	16945.2	17894.9	21112.1	20228.5	192%	217%	194%	156%	97%	75%
											10000000	144.6	130.7	114.0	121.3	136.0	244.2	130.1	140.4	122.8	127.2	153.9	220.1	90%	107%	108%	105%	113%	90%
											100000000	2503.3	2303.5	2538.4	2541.5	2367.9	2736.2	2745.4	2830.1	2629.6	2753.6	2823.8	3007.9	110%	123%	104%	108%	119%	110%
											500000000	19178.2	17867.5	18058.3	18328.7	18175.8	18037.2	34867.8	34100.8	35108.5	34870.0	34874.0	35566.6	182%	191%	194%	190%	192%	197%
											10000000	96.8	90.7	76.2	98.4	99.3	148.8	103.7	102.1	83.8	100.4	103.5	152.3	107%	113%	110%	102%	104%	102%
					sequential	100000000	1326.7	1486.8	1548.7	1527.4	1682.9	1709.8	1577.9	1675.7	1710.2	1671.1	1539.0	1711.2	119%	113%	110%	109%	91%	100%					
						500000000	11146.8	9144.5	7720.1	10565.8	7737.8	8753.6	20677.7	20806.6	17997.6	16693.2	18847.9	15782.0	186%	228%	233%	158%	244%	180%					
						xeon	cycle	10000000	129.5	132.9	127.6	100.2	154.5	219.7	117.1	118.7	114.5	108.1	165.9	219.4	90%	89%	90%	108%	107%	100%			
								100000000	1623.4	1740.0	1443.8	1774.1	2113.5	1952.8	2247.5	1615.3	2127.2	1581.7	2261.8	2698.8	138%	93%	147%	89%	107%	138%			
								1000000000	19065.9	15741.2	17730.1	14320.9	15815.2	21778.9	16341.6	18960.6	17505.3	19995.4	19037.8	15829.1	86%	120%	99%	140%	120%	73%			
								10000000	137.8	156.9	162.1	157.1	190.5	272.7	147.1	165.1	151.9	189.9	167.6	285.7	107%	105%	94%	121%	88%	105%			
								100000000	2913.5	3062.2	2838.1	2852.3	2932.1	2906.2	3294.0	3148.6	3067.1	3300.2	3125.1	3295.3	113%	103%	108%	116%	107%	113%			
	1000000000	31197.1	29759.7	30382.8	28997.4			28839.2	30039.5	32610.3	32380.7	32496.1	32911.1	31654.1	33102.2	105%	109%	107%	113%	110%	110%								
		random	10000000	130.9	115.2	103.1	122.8	122.2	146.0	116.2	112.9	94.4	102.8	96.0	147.1	89%	98%	92%	84%	79%	101%								
			100000000	2062.8	1625.4	1814.6	1932.0	1598.8	1665.7	2190.1	2019.6	1487.5	1688.2	2043.3	1651.1	106%	124%	82%	87%	128%	99%								
			1000000000	15314.8	12605.8	13234.7	12604.4	15796.3	14172.5	15896.2	16005.4	15054.2	16185.0	14301.6	14270.5	104%	127%	114%	128%	91%	101%								
			patched	i5	cycle	10000000	80.7	75.2	99.3	101.6	106.3	215.3	107.3	104.8	95.4	80.1	131.4	256.3	133%	139%	96%	79%	124%	119%					
						100000000	1782.3	1352.2	1285.8	1599.3	1754.8	2241.0	1762.2	1715.9	1481.0	1469.6	1744.1	2297.7	99%	127%	115%	92%	99%	103%					
						500000000	8697.4	8579.2	8589.6	11626.4	21663.7	27943.1	15861.3	19954.8	20034.2	19062.8	21717.8	22891.2	182%	233%	233%	164%	100%	82%					
		random	10000000	124.0	119.7	133.3	127.9	150.1	209.1	131.6	130.8	139.9	135.8	150.0	237.3	106%	109%	105%	106%	100%	114%								
			100000000	2522.3	2451.3	2419.1	2565.1	2561.0	2500.8	2721.0	2767.0	2815.5	2666.3	2698.6	2913.0	108%	113%	116%	104%	105%	116%								
			500000000	18175.2	18076.5	17664.2	17960.4	17712.9	18120.6	35001.1	34723.7	34862.9	34262.0	34012.3	34901.7	193%	192%	197%	191%	192%	193%								
			10000000	98.6	84.3	78.2	91.5	81.2	164.7	78.1	73.4	79.5	82.9	87.1	151.4	79%	87%	102%	91%	107%	92%								
			100000000	1540.5	1451.2	1473.2	1658.4	1613.6	1650.8	1745.8	1745.1	1575.9	1461.8	1388.6	1593.1	113%	120%	107%	88%	86%	97%								
			500000000	9373.3	9365.2	8175.3	7464.7	10642.9	9072.0	19513.4	15936.2	17172.3	14889.4	15024.5	14562.5	208%	170%	210%	199%	141%	161%								
					xeon	cycle	10000000	96.6	129.1	83.2	101.0	166.5	214.1	111.5	99.2	111.9	100.5	137.0	219.6	116%	77%	134%	100%	82%	103%				
							100000000	1598.3	1555.3	1867.6	1502.7	1698.9	2663.5	1725.6	1649.6	1543.9	2233.5	1859.6	2883.2	108%	106%	83%	149%	109%	108%				
							1000000000	14346.0	15311.6	15716.6	18402.5	18925.8	17652.9	14543.1	19260.3	16262.0	18524.2	20103.5	17552.5	101%	126%	103%	101%	106%	99%				
							10000000	177.5	154.5	154.1	150.9	166.6	274.8	187.1	190.5	170.9	188.1	186.7	283.6	105%	123%	111%	125%	112%	103%				
							100000000	2896.8	2937.8	2769.9	2955.4	2834.0	3060.5	3405.5	3338.8	3210.3	3252.9	3050.6	3319.3	118%	114%	116%	110%	108%	108%				
							1000000000	31218.3	30512.6	29652.6	30201.3	29095.7	29564.1	34084.9	33065.6	33202.7	31790.0	32969.2	32569.6	109%	108%	112%	105%	113%	110%				
							random	10000000	91.2	109.8	95.4	80.0	117.8	163.4	101.1	110.5	88.4	126.0	125.4	167.2	111%	101%	93%	158%	106%	102%			
								100000000	1931.0	1867.4	1479.2	1909.8	1590.3	1893.6	1888.8	1917.4	1761.4	1672.6	1629.1	1787.5	98%	103%	119%	88%	102%	94%			
								1000000000	15547.6	17056.0	16325.3	15557.7	14258.6	14074.6	17991.5	16380.8	14570.4	16433.6	19184.2	16003.2	116%	96%	89%	106%	135%	114%			
sequential								10000000	7.3	7.3	7.4	8.9	19.6	82.8	7.1	7.2	7.4	8.6	19.6	83.1	97%	99%	100%	96%	100%	100%			
								100000000	7.3	7.3	7.4	8.7	19.9	121.9	7.4	7.3	7.4	8.7	19.3	122.6	100%	100%	100%	100%	97%	101%			
								500000000	7.3	7.5	7.8	9.7	20.1	124.1	7.4	7.5	7.5	9.6	19.9	121.8	101%	100%	96%	99%	99%	98%			
						10000000	7.3	7.3	7.5	9.0	18.8	81.5	7.2	7.2	7.5	8.9	18.9	80.9	99%	99%	100%	98%	100%	99%					
						sequential	100000000	7.2	7.3	7.6	8.6	19.7	113.0	7.3	7.3	7.6	8.7	19.7	113.9	101%	99%	100%	100%	100%	101%				
	500000000	7.3	7.3	7.8	8.8		19.8	121.3	7.4	7.2	7.6	9.0	20.1	120.7	101%	99%	98%	102%	101%	99%									
	10000000	7.3	7.2	7.3	7.8		12.5	55.5	7.1	7.2	7.2	7.8	12.3	56.6	98%	100%	99%	100%	99%	102%									
	100000000	7.3	7.2	7.5	7.9		12.5	56.9	7.2	7.2	7.4	7.8	12.5	56.0	100%	99%	99%	98%	99%	98%									
500000000	7.3	7.2	7.5	8.8	12.8	56.0	7.3	7.3	7.5	8.6	12.7	56.2	101%	101%	99%	98%	99%	100%											

				xeon	cycle	1000000	8.5	8.9	9.0	10.2	22.8	96.4	8.6	8.8	9.0	10.0	22.3	96.3	102%	99%	100%	98%	98%	100%
						10000000	8.5	8.4	8.7	10.1	22.7	154.5	8.6	8.7	8.9	10.0	22.6	154.9	102%	104%	102%	99%	100%	100%
						100000000	8.6	8.7	8.9	10.1	23.5	159.5	8.6	8.3	8.8	10.1	23.9	158.1	100%	95%	98%	100%	102%	99%
						10000000	8.5	8.6	9.1	10.2	21.9	94.1	8.5	8.5	8.8	10.0	22.2	93.5	100%	99%	96%	98%	102%	99%
					random	10000000	8.5	8.3	8.7	9.7	22.4	145.3	8.5	8.4	8.7	10.1	22.9	145.8	101%	100%	99%	105%	102%	100%
						100000000	8.4	8.6	8.5	10.1	23.5	155.7	8.5	8.4	8.7	10.0	24.0	154.9	100%	97%	102%	99%	102%	99%
						10000000	8.7	8.6	8.6	9.3	14.8	64.4	8.3	8.7	8.9	9.4	14.5	66.4	96%	101%	103%	101%	98%	103%
						100000000	8.6	8.2	8.5	9.2	14.3	68.0	8.6	8.5	8.6	9.1	14.2	67.2	99%	103%	101%	98%	99%	99%
						1000000000	8.5	8.6	8.4	9.1	14.5	67.0	8.5	8.3	8.7	9.1	14.3	66.0	100%	97%	104%	100%	99%	99%
					patched	i5	cycle	10000000	7.2	7.3	7.5	8.9	19.3	81.9	7.3	7.3	7.4	8.7	20.6	83.6	102%	99%	99%	98%
100000000	7.4	7.3	7.4	8.6				19.4	120.6	7.3	7.3	7.4	8.9	20.4	130.5	99%	100%	101%	104%	105%	108%			
500000000	7.3	7.4	7.5	9.6				19.8	120.1	7.3	7.6	7.5	9.8	20.8	130.1	99%	103%	101%	103%	105%	108%			
10000000	7.2	7.4	7.6	8.6				18.5	79.8	7.3	7.3	7.7	8.8	20.0	82.9	101%	99%	101%	102%	108%	104%			
random	100000000	7.3	7.2	7.5			8.6	19.4	111.1	7.3	7.3	7.5	8.9	20.8	122.6	100%	102%	100%	103%	107%	110%			
	500000000	7.4	7.5	7.6			8.7	19.8	119.1	7.6	7.3	7.6	9.9	20.8	129.9	103%	97%	101%	114%	105%	109%			
	10000000	7.3	7.2	7.3			7.8	12.3	54.7	7.2	7.3	7.4	7.9	13.1	56.9	99%	102%	101%	101%	107%	104%			
	100000000	7.3	7.4	7.3			7.8	12.4	54.9	7.3	7.3	7.4	7.9	12.5	57.5	100%	99%	101%	101%	101%	105%			
500000000	7.4	7.4	7.4	8.7		12.9	55.7	7.4	7.4	7.4	8.8	12.9	57.8	100%	101%	99%	100%	100%	104%					
xeon	cycle	10000000	8.7	8.8		8.8	10.1	22.7	97.7	8.6	8.8	8.6	10.3	24.0	99.6	99%	100%	98%	102%	106%	102%			
		100000000	8.6	8.6	8.7	9.9	23.3	156.4	8.7	8.7	8.5	10.1	23.5	171.0	101%	101%	98%	102%	101%	109%				
		1000000000	8.8	8.4	8.5	10.1	23.7	145.3	8.6	8.7	8.7	10.2	23.6	172.3	99%	103%	103%	101%	99%	119%				
		10000000	8.7	8.5	9.0	10.1	21.9	101.7	8.6	8.5	8.7	9.9	22.7	104.7	100%	101%	97%	98%	104%	103%				
	random	100000000	8.3	8.4	8.7	10.0	22.7	146.6	8.7	8.5	8.6	10.1	24.2	162.5	104%	101%	99%	101%	106%	111%				
		1000000000	8.6	8.5	8.5	9.9	24.0	155.3	8.3	8.5	8.9	10.1	24.1	171.9	97%	101%	104%	102%	100%	111%				
		10000000	8.5	8.6	8.7	9.1	14.4	66.8	8.6	9.0	8.8	9.2	14.7	66.0	101%	104%	101%	102%	102%	99%				
		100000000	8.7	8.6	8.7	9.3	14.3	66.7	8.6	8.4	8.6	9.2	14.6	68.8	99%	98%	100%	99%	102%	103%				
1000000000	8.6	8.4	8.3	9.2	14.3	67.4	8.4	8.5	8.6	9.2	14.6	68.9	98%	101%	104%	101%	102%	102%						
seqscan	master	i5	cycle	10000000	182.4	182.1	184.6	187.5	201.0	301.5	183.0	183.5	179.0	186.1	205.2	301.1	100%	101%	97%	99%	102%	100%		
				100000000	1745.0	1741.1	1754.6	1703.8	1799.6	1948.1	1758.8	1703.4	1764.8	1772.4	1786.2	1957.9	101%	98%	101%	104%	99%	101%		
				500000000	16020.3	15402.1	15445.5	15392.6	15661.5	16921.0	15410.8	15394.8	15398.3	15372.6	15742.8	15695.1	96%	100%	100%	100%	101%	93%		
				10000000	184.0	181.5	184.5	189.3	207.2	300.0	186.6	181.0	184.7	187.4	207.7	299.5	101%	100%	100%	99%	100%	100%		
				100000000	1754.7	1743.8	1776.7	1736.5	1766.5	2009.1	1721.9	1770.0	1767.2	1760.2	1801.0	1989.9	98%	102%	99%	101%	102%	99%		
				500000000	16612.2	15470.3	15460.7	15404.1	15491.2	15615.4	15362.1	15873.1	15474.0	15594.9	15465.1	15609.0	92%	103%	100%	101%	100%	100%		
				10000000	185.9	181.1	182.2	188.0	189.6	234.8	183.3	184.5	179.1	187.7	192.3	235.8	99%	102%	98%	100%	101%	100%		
				100000000	1753.1	1744.8	1746.6	1802.3	1790.2	1823.4	1742.5	1697.9	1761.0	1742.3	1738.8	1770.6	99%	97%	101%	97%	97%	97%		
			500000000	15446.4	15620.9	15418.1	15816.6	15529.9	15527.9	15943.6	15497.3	15525.1	15415.1	15811.0	16982.6	103%	99%	101%	97%	102%	109%			
			xeon	cycle	10000000	198.9	203.8	200.8	208.2	219.1	329.8	201.0	199.1	200.5	202.8	223.4	332.2	101%	98%	100%	97%	102%	101%	
100000000	1932.3	1895.9			1920.6	1863.8	1931.2	2128.8	1902.4	1944.7	1903.9	1894.5	1962.4	2187.8	98%	103%	99%	102%	102%	103%				
1000000000	19020.9	18778.3			19668.9	18609.0	19293.8	19563.2	18720.2	18800.1	19118.2	19079.2	19449.8	19787.9	98%	100%	97%	103%	101%	101%				
10000000	205.0	203.9			201.5	209.3	219.0	333.0	199.9	207.1	208.4	203.8	223.8	346.8	98%	102%	103%	97%	102%	104%				
random	100000000	1908.6		1911.9	1925.8	1905.7	1943.5	2155.9	1932.7	1934.2	1906.9	1922.9	1914.7	2193.0	101%	101%	99%	101%	99%	102%				
	1000000000	19326.9		19191.0	18867.7	19244.6	18863.6	19752.8	19198.6	19425.4	19001.7	19043.8	19223.6	19560.2	99%	101%	101%	99%	102%	99%				
	10000000	196.4		207.6	210.8	202.7	214.0	258.5	202.3	199.1	205.1	203.3	209.1	258.1	103%	96%	97%	100%	98%	100%				
	100000000	1885.5		1892.4	1884.9	1927.6	1872.4	1966.4	1924.6	1861.1	1867.0	1931.9	1903.1	1931.2	102%	98%	99%	100%	102%	98%				
1000000000	19119.3	19254.4	19147.5	18869.1	19357.9	19417.3	19143.8	18728.2	19319.5	18999.8	18575.0	19355.7	100%	97%	101%	101%	96%	100%						
patched	i5	cycle	10000000	182.4	178.8	180.4	184.2	199.1	308.1	184.0	187.7	179.0	185.8	203.3	301.5	101%	105%	99%	101%	102%	98%			
			100000000	1744.8	1769.8	1747.3	1723.5	1756.7	1941.0	1761.8	1723.3	1701.0	1738.0	1766.0	1973.2	101%	97%	97%	101%	101%	102%			
			500000000	16347.6	15332.8	15393.2	15455.5	15437.4	15573.9	15439.8	15381.6	15439.2	15478.5	15413.9	15497.4	94%	100%	100%	100%	100%	100%			
			10000000	185.7	183.0	185.6	186.5	208.3	309.4	183.3	187.1	184.9	187.8	202.0	306.2	99%	102%	100%	101%	97%	99%			
			100000000	1735.2	1744.1	1717.3	1812.6	1818.6	2028.7	1753.4	1745.9	1770.4	1770.6	1795.8	2032.9	101%	100%	103%	98%	99%	100%			
			500000000	15864.1	15439.8	15560.4	15426.4	15495.4	15519.4	15494.0	16545.4	15527.1	15415.8	15418.4	15675.5	98%	107%	100%	100%	100%	101%			
			10000000	181.5	182.0	179.3	180.7	184.8	235.8	182.1	179.0	186.1	185.9	184.2	235.0	100%	98%	104%	103%	100%	100%			
			random	100000000	185.7	183.0	185.6	186.5	208.3	309.4	183.3	187.1	184.9	187.8	202.0	306.2	99%	102%	100%	101%	97%	99%		
		1000000000		1735.2	1744.1	1717.3	1812.6	1818.6	2028.7	1753.4	1745.9	1770.4	1770.6	1795.8	2032.9	101%	100%	103%	98%	99%	100%			
		sequential	10000000	181.5	182.0	179.3	180.7	184.8	235.8	182.1	179.0	186.1	185.9	184.2	235.0	100%	98%	104%	103%	100%	100%			

						10000000	1719.9	1729.6	1726.9	1767.5	1759.9	1743.5	1707.5	1740.1	1726.4	1718.2	1735.5	1762.4	99%	101%	100%	97%	99%	101%
						50000000	15447.0	15385.1	15492.2	15891.4	15448.2	15512.8	16366.5	15411.6	17501.2	15442.0	15464.5	16353.1	106%	100%	113%	97%	100%	105%
				xeon	cycle	1000000	197.7	200.7	196.2	207.1	221.0	324.4	195.9	204.2	225.9	206.3	221.4	336.8	99%	102%	115%	100%	100%	104%
						10000000	1858.5	1829.1	1887.4	1870.3	1911.0	2153.4	1856.2	1872.0	1926.2	1930.2	1883.8	2113.8	100%	102%	102%	103%	99%	98%
					random	100000000	19376.8	19040.5	18633.1	18788.2	18721.1	18791.7	18608.4	18717.3	18620.5	19024.9	19090.2	19636.2	96%	98%	100%	101%	102%	104%
						10000000	204.7	201.4	201.5	202.6	221.6	347.3	202.0	202.0	206.9	201.9	219.3	339.2	99%	100%	103%	100%	99%	98%
						100000000	1891.3	1923.6	1884.5	1923.4	1979.9	2193.9	1910.3	1939.0	1882.5	1888.2	1954.6	2177.9	101%	101%	100%	98%	99%	99%
					sequential	1000000000	18896.0	19060.9	19243.0	18831.6	19405.6	19486.5	19296.5	18824.5	18925.3	19211.9	19252.1	19686.4	102%	99%	98%	102%	99%	101%
						10000000	199.6	199.4	195.8	197.0	205.4	260.6	199.9	197.4	201.5	200.8	204.7	255.8	100%	99%	103%	102%	100%	98%
						100000000	1869.9	1900.7	1881.3	1853.5	1854.1	1919.3	1827.3	1908.3	1895.8	1850.9	1896.2	1964.1	98%	100%	101%	100%	102%	102%
						1000000000	19358.7	19336.9	18497.4	18788.4	18909.4	18767.9	18912.8	18797.2	18455.2	18556.4	18760.1	18557.3	98%	97%	100%	99%	99%	99%
	hash	bitmapscan	master	i5	cycle	1000000	7.1	7.2	7.4	9.0	23.2	101.6	7.2	7.2	7.2	9.1	23.9	108.6	101%	100%	97%	101%	103%	107%
						100000000	7.2	7.0	7.4	8.9	23.3	169.9	7.2	7.0	7.3	9.0	24.1	190.8	99%	99%	99%	101%	103%	112%
					random	500000000	7.3	7.1	7.4	8.9	23.1	171.1	7.1	7.3	7.3	9.1	24.3	186.4	97%	103%	99%	101%	105%	109%
						10000000	7.1	7.2	7.2	8.9	21.9	99.3	7.1	7.2	7.3	9.0	23.1	106.8	99%	100%	101%	102%	106%	108%
						10000000	7.1	7.1	7.5	9.0	22.9	155.9	7.0	7.0	7.4	9.1	24.0	169.4	99%	98%	99%	101%	105%	109%
					sequential	500000000	7.3	7.2	7.3	8.9	23.3	168.4	7.2	7.2	7.5	9.3	24.1	179.1	98%	100%	102%	104%	103%	106%
						10000000	7.2	7.1	7.2	7.7	12.3	58.0	7.1	7.1	7.2	7.7	12.4	57.6	99%	100%	100%	100%	101%	99%
						100000000	7.2	7.1	7.2	7.7	12.3	56.9	7.2	7.1	7.2	7.6	12.4	57.9	100%	100%	99%	99%	101%	102%
						500000000	7.2	7.2	7.3	7.7	12.3	57.0	7.1	7.2	7.2	7.7	12.5	58.1	99%	100%	99%	99%	101%	102%
				xeon	cycle	1000000	8.6	8.7	8.8	10.4	26.3	129.3	9.0	9.0	8.8	10.4	27.3	136.2	104%	103%	100%	100%	104%	105%
						100000000	8.5	8.6	8.7	11.6	27.1	203.8	8.8	8.6	9.2	10.5	27.4	210.2	103%	100%	105%	90%	101%	103%
					random	1000000000	9.1	9.0	8.3	11.1	28.0	193.0	9.4	9.7	9.6	11.3	27.9	219.6	104%	108%	116%	101%	99%	114%
						10000000	8.1	8.8	8.8	10.2	26.3	111.7	9.4	8.8	8.4	10.0	25.5	122.1	116%	99%	96%	98%	97%	109%
						10000000	9.0	9.1	8.8	10.6	26.3	189.0	8.9	9.3	8.7	11.4	26.7	204.2	99%	103%	99%	107%	102%	108%
					sequential	1000000000	9.3	9.4	9.4	10.1	26.9	203.8	9.5	8.8	9.9	11.2	27.7	218.0	102%	94%	105%	110%	103%	107%
						10000000	9.0	9.0	9.2	9.9	15.5	68.3	9.1	9.3	8.8	9.9	15.7	68.7	101%	102%	95%	101%	101%	101%
						100000000	9.0	8.6	9.0	9.8	14.9	69.3	9.1	8.9	9.0	10.0	15.8	67.1	101%	104%	99%	102%	106%	97%
						1000000000	9.0	9.1	9.8	9.6	15.1	70.7	9.3	8.8	9.7	9.9	16.1	71.0	103%	97%	99%	103%	107%	100%
			patched	i5	cycle	1000000	7.2	7.1	7.3	9.0	22.9	100.8	7.1	7.1	7.3	8.9	23.8	107.5	98%	101%	100%	99%	104%	107%
						100000000	7.1	7.2	7.3	8.9	22.9	177.0	7.2	7.1	7.4	8.9	23.8	187.4	102%	98%	100%	101%	104%	106%
					random	500000000	7.2	7.1	7.4	8.8	22.9	168.2	7.2	7.3	7.4	9.0	23.9	179.5	100%	102%	99%	102%	104%	107%
						10000000	7.1	7.1	7.4	8.9	21.6	98.2	7.2	7.2	7.4	8.9	22.8	105.0	101%	102%	100%	101%	105%	107%
						10000000	7.0	7.1	7.3	9.0	22.8	154.8	7.1	7.2	7.4	9.1	23.8	167.2	101%	100%	101%	101%	105%	108%
					sequential	500000000	7.2	7.2	7.4	8.9	23.1	167.3	7.2	7.2	7.4	9.2	24.2	176.3	99%	100%	101%	104%	105%	105%
						10000000	7.1	7.1	7.2	7.7	12.4	57.1	7.1	7.1	7.1	7.7	12.4	57.6	100%	99%	99%	100%	101%	101%
						100000000	7.1	7.1	7.2	7.8	12.3	57.0	7.1	7.1	7.3	7.6	12.4	58.0	100%	100%	101%	98%	101%	102%
						500000000	7.2	7.2	7.3	7.8	12.4	57.4	7.1	7.1	7.2	7.7	12.4	57.8	100%	98%	98%	100%	100%	101%
				xeon	cycle	1000000	8.9	8.7	9.5	10.4	26.1	127.6	8.7	8.7	9.3	10.5	28.1	136.8	98%	100%	98%	101%	107%	107%
						100000000	9.3	9.3	9.0	10.1	29.0	203.2	9.0	8.4	8.9	10.8	28.0	226.7	96%	90%	99%	107%	97%	112%
					random	1000000000	8.7	8.8	9.0	11.2	28.4	206.0	8.9	9.0	9.0	11.3	28.3	221.5	102%	103%	100%	101%	100%	108%
						10000000	9.0	9.4	8.6	10.2	26.5	121.6	9.2	8.9	8.4	11.2	26.6	131.0	102%	95%	98%	109%	100%	108%
						10000000	8.8	8.7	8.5	11.2	27.2	188.3	8.9	8.4	9.0	10.4	27.1	203.8	101%	96%	105%	93%	100%	108%
					sequential	1000000000	8.9	9.1	9.8	11.7	29.0	189.2	9.4	8.7	9.5	10.7	28.4	205.2	105%	96%	97%	91%	98%	108%
						10000000	9.0	9.0	9.0	10.1	15.0	68.2	9.4	8.9	9.2	9.7	15.8	70.3	104%	99%	102%	96%	105%	103%
						100000000	8.8	8.8	9.2	10.0	15.4	69.6	9.2	8.8	9.3	9.8	15.3	69.7	105%	100%	101%	98%	100%	100%
						1000000000	8.8	8.1	10.0	10.2	15.1	69.3	9.2	9.6	9.4	10.1	15.2	70.2	105%	118%	93%	99%	101%	101%
		indexscan	master	i5	cycle	1000000	7.2	7.2	7.3	8.5	19.4	87.7	7.1	7.1	8.4	8.5	19.3	88.4	98%	99%	115%	100%	100%	101%
						100000000	7.1	7.1	7.4	8.6	19.5	122.0	7.1	7.1	7.4	8.5	19.3	122.3	99%	100%	100%	99%	99%	100%
					random	500000000	7.2	7.1	7.5	8.7	19.7	123.0	7.2	7.3	7.4	8.8	19.5	123.0	100%	103%	99%	101%	99%	100%
						10000000	7.1	7.1	7.4	8.4	18.7	86.6	7.0	7.1	7.3	8.5	18.7	84.7	99%	99%	99%	101%	100%	98%
						100000000	7.2	7.2	7.4	8.8	19.4	113.9	7.0	7.1	7.4	9.0	19.3	113.3	97%	99%	100%	102%	100%	99%

							50000000	7.1	7.1	7.4	8.7	19.7	120.6	7.2	7.2	7.3	8.7	19.5	122.1	102%	102%	98%	100%	99%	101%
							10000000	7.1	7.0	7.1	7.7	12.3	56.8	7.2	7.1	7.1	7.8	12.4	56.8	102%	102%	99%	100%	100%	100%
							100000000	7.1	7.1	7.2	7.8	12.5	57.6	7.0	7.2	7.2	7.7	12.6	57.3	99%	101%	100%	99%	101%	99%
							500000000	7.1	7.1	7.3	7.7	12.4	58.7	7.2	7.2	7.3	7.7	12.5	57.5	100%	101%	99%	101%	101%	98%
				xeon	cycle		10000000	9.0	8.6	9.0	10.1	24.1	106.0	8.5	8.7	8.6	10.1	22.9	106.0	95%	101%	96%	99%	95%	100%
							100000000	8.7	8.3	9.0	10.9	23.7	149.3	8.6	8.3	9.1	10.2	23.9	156.6	100%	100%	101%	93%	101%	105%
					random		1000000000	8.9	8.8	8.8	11.2	23.4	154.6	9.1	9.3	9.2	10.7	23.8	161.3	102%	106%	105%	95%	102%	104%
							10000000	8.2	8.7	8.4	9.7	22.2	98.1	9.2	9.0	8.3	9.9	21.6	103.0	113%	103%	98%	102%	97%	105%
							100000000	8.5	9.0	8.6	9.6	22.1	137.6	8.8	9.3	8.6	10.2	23.0	147.6	104%	104%	100%	107%	104%	107%
					sequential		1000000000	9.1	9.2	9.9	9.8	22.9	158.9	9.2	8.2	9.3	11.6	23.7	160.8	101%	89%	94%	119%	103%	101%
							10000000	8.6	9.1	8.9	10.0	15.2	68.9	9.2	8.8	8.7	9.7	15.8	69.7	107%	97%	97%	97%	104%	101%
							100000000	8.6	8.5	9.2	9.6	15.3	67.3	8.8	8.9	9.0	9.7	15.8	69.1	102%	104%	98%	101%	103%	103%
							1000000000	9.4	8.8	9.2	9.8	14.9	70.5	9.4	8.6	9.3	10.2	15.6	70.1	100%	97%	102%	103%	104%	99%
	patched	i5	cycle		10000000	7.1	7.1	7.4	8.5	19.5	87.2	7.1	7.2	7.3	8.7	20.6	92.3	101%	101%	98%	102%	106%	106%		
					100000000	7.1	7.1	7.3	8.6	19.4	122.7	7.2	7.1	7.3	8.8	20.5	132.6	101%	99%	100%	103%	105%	108%		
			random		500000000	7.1	7.2	7.4	8.5	19.7	123.0	7.1	7.2	7.4	8.7	20.6	132.5	100%	100%	100%	102%	105%	108%		
					10000000	7.1	7.2	7.1	8.6	18.8	83.7	7.0	7.1	7.4	8.7	19.7	88.6	99%	99%	103%	101%	105%	106%		
					100000000	7.1	7.2	7.6	8.7	19.5	113.8	7.1	7.2	7.3	9.0	20.6	124.8	101%	101%	97%	104%	106%	110%		
			sequential		500000000	7.2	7.2	7.6	8.9	19.5	121.1	7.1	7.2	7.5	8.9	20.7	130.7	99%	99%	99%	100%	106%	108%		
					10000000	7.2	7.1	7.2	7.7	12.3	56.8	7.2	7.2	7.3	7.8	12.5	60.5	100%	101%	101%	100%	102%	107%		
					100000000	7.0	7.2	7.3	7.7	12.5	57.0	7.2	7.3	7.2	7.8	12.6	58.7	102%	101%	99%	100%	100%	103%		
					500000000	7.1	7.2	7.2	7.7	12.5	57.4	7.1	7.1	7.3	7.7	12.7	58.8	100%	99%	101%	101%	102%	102%		
		xeon	cycle		10000000	8.8	8.7	9.2	10.2	24.5	105.2	8.9	9.0	9.1	10.0	23.7	116.3	101%	103%	98%	98%	97%	111%		
					100000000	9.1	9.2	9.3	10.0	24.8	157.6	8.7	8.3	9.0	10.3	23.7	164.8	96%	91%	97%	104%	96%	105%		
			random		1000000000	8.5	9.5	9.1	11.3	24.9	162.0	8.9	9.1	9.1	10.9	24.5	175.2	105%	96%	100%	96%	98%	108%		
					10000000	8.7	9.0	8.5	9.5	22.3	104.5	9.0	9.0	8.7	10.7	22.9	111.1	104%	100%	103%	112%	103%	106%		
					100000000	8.7	8.7	8.7	11.7	22.8	148.4	8.6	8.7	8.8	10.7	24.0	163.1	99%	100%	101%	92%	105%	110%		
			sequential		1000000000	8.9	9.4	9.9	11.1	25.0	149.4	9.3	8.3	9.5	10.6	25.0	159.4	105%	88%	96%	96%	100%	107%		
					10000000	9.2	9.1	9.4	9.7	15.3	68.9	8.9	8.8	8.7	9.8	15.2	71.5	97%	96%	93%	101%	99%	104%		
					100000000	8.9	8.8	9.1	10.0	14.8	69.8	9.1	9.0	9.5	9.8	15.6	71.8	103%	102%	105%	98%	105%	103%		
					1000000000	8.9	8.6	9.6	10.3	14.6	68.0	8.8	9.2	9.5	9.9	15.5	72.7	98%	108%	99%	96%	106%	107%		
	seqscan	master	i5	cycle		10000000	143.0	144.8	142.9	145.8	152.0	185.4	145.5	142.6	143.6	147.1	148.9	184.7	102%	98%	100%	101%	98%	100%	
						100000000	1340.9	1348.1	1343.1	1341.1	1357.4	1400.2	1345.0	1351.3	1343.1	1349.0	1355.2	1414.7	100%	100%	100%	101%	100%	101%	
						500000000	15223.6	15206.9	15161.3	15224.9	15148.9	15179.4	15133.2	15132.3	15701.1	15220.2	15159.9	15240.8	99%	100%	104%	100%	100%	100%	
						10000000	142.4	142.3	143.4	144.5	149.0	184.0	142.9	142.1	142.8	144.4	151.0	187.4	100%	100%	100%	100%	101%	102%	
						100000000	1357.2	1349.0	1384.8	1346.6	1374.4	1409.6	1339.2	1337.5	1387.0	1347.8	1358.2	1399.0	99%	99%	100%	100%	99%	99%	
				sequential		500000000	15132.8	16885.3	16219.7	15346.4	15134.4	15154.5	15149.0	16006.3	15144.9	15126.3	15163.3	15203.1	100%	95%	93%	99%	100%	100%	
						10000000	142.7	142.3	142.3	144.1	146.7	145.8	142.8	146.2	142.3	143.8	146.6	179.8	100%	103%	100%	100%	100%	123%	
						100000000	1341.3	1347.0	1348.0	1353.6	1345.4	1350.0	1344.9	1354.2	1341.1	1354.9	1347.1	1384.6	100%	101%	99%	100%	100%	103%	
						500000000	15184.9	15102.9	15092.5	15468.1	15190.4	16220.3	15166.7	15175.9	15080.0	15178.2	15200.9	15170.5	100%	100%	100%	98%	100%	94%	
		xeon	cycle		10000000	161.5	160.0	158.3	161.1	172.1	214.0	161.3	159.4	161.0	159.8	165.5	213.0	100%	100%	102%	99%	96%	100%		
					100000000	1467.8	1473.6	1476.7	1489.5	1500.4	1553.5	1466.2	1473.0	1493.0	1479.9	1496.0	1551.1	100%	100%	101%	99%	100%	100%		
			random		1000000000	14988.3	14889.1	15020.0	14778.7	14980.2	15130.9	14989.6	14870.5	15036.7	15000.9	15013.6	15099.6	100%	100%	100%	102%	100%	100%		
					10000000	159.3	160.8	160.3	160.4	163.2	213.6	160.8	158.5	158.9	158.9	165.6	211.9	101%	99%	99%	99%	101%	99%		
					100000000	1481.0	1499.0	1475.0	1477.2	1507.2	1545.9	1493.3	1523.4	1465.6	1463.9	1497.2	1564.5	101%	102%	99%	99%	99%	101%		
			sequential		1000000000	14885.0	14957.7	14810.9	14998.1	14988.2	15374.8	15233.1	14967.9	15112.7	14784.1	14736.2	15116.3	102%	100%	102%	99%	98%	98%		
					10000000	160.4	159.9	164.7	162.3	164.9	198.6	159.1	158.5	158.8	161.2	162.2	201.8	99%	99%	96%	99%	98%	102%		
					100000000	1492.7	1466.3	1479.6	1488.1	1471.3	1515.9	1471.7	1489.6	1559.1	1491.7	1500.0	1517.8	99%	102%	105%	100%	102%	100%		
					1000000000	14861.5	14790.1	14873.2	15433.2	14748.9	14854.9	14960.2	14681.2	14815.9	14967.7	15068.4	14999.4	101%	99%	100%	97%	102%	101%		
		patched	i5	cycle		10000000	142.5	145.8	142.5	143.7	153.1	183.3	145.1	144.8	142.4	144.6	148.9	184.7	102%	99%	100%	101%	97%	101%	
						100000000	1345.5	1336.1	1336.7	1343.0	1357.2	1419.4	1361.2	1337.9	1335.2	1342.2	1352.2	1392.5	101%	100%	100%	100%	100%	98%	
						500000000	15158.8	16268.3	16481.2	15182.8	15106.3	15070.0	15144.6	15077.1	15155.1	15141.4	15106.7	15151.7	100%	93%	92%	100%	100%	101%	



uncached	btree	bitmapscan	master	i5	random	1000000	143.0	141.8	142.7	143.4	148.7	185.0	144.9	141.7	143.5	143.2	148.7	189.0	101%	100%	101%	100%	100%	102%	
						10000000	1337.6	1351.9	1341.1	1358.5	1348.2	1390.9	1349.4	1339.6	1332.7	1347.4	1353.4	1415.4	101%	99%	99%	99%	100%	102%	
						50000000	15191.4	15155.8	15184.5	15135.8	15206.2	15154.2	15133.8	15101.3	15131.9	16351.9	15654.8	15086.3	100%	100%	100%	108%	103%	100%	
					sequential	1000000	141.8	141.8	142.5	145.0	149.2	143.0	142.9	141.9	142.4	142.0	146.9	177.6	101%	100%	100%	98%	98%	124%	
						10000000	1341.5	1348.1	1334.9	1337.8	1352.9	1389.6	1334.7	1341.7	1340.0	1334.3	1340.3	1382.7	99%	100%	100%	100%	99%	100%	
						50000000	15489.9	15021.0	15087.2	15151.8	15115.8	15250.2	16506.0	15130.2	15105.9	15087.6	15172.8	15180.4	107%	101%	100%	100%	100%	100%	
					xeon	cycle	1000000	156.4	158.5	158.9	156.8	164.7	208.6	156.3	157.1	159.8	158.1	165.8	214.0	100%	99%	101%	101%	101%	103%
							10000000	1455.5	1456.3	1474.5	1448.8	1455.1	1530.8	1478.4	1466.9	1461.1	1452.5	1471.4	1542.4	102%	101%	99%	100%	101%	101%
							100000000	14749.7	14745.9	14781.8	14680.1	14892.0	14907.6	14529.1	14515.6	14629.9	14739.9	14610.7	14866.5	99%	98%	99%	100%	98%	100%
						random	1000000	159.8	158.5	158.0	157.3	165.6	209.7	158.8	158.8	157.9	159.0	165.2	216.0	99%	100%	100%	101%	100%	103%
							10000000	1473.3	1476.9	1454.9	1468.7	1474.9	1531.4	1536.5	1455.2	1449.7	1470.2	1468.4	1523.8	104%	99%	100%	100%	100%	100%
							100000000	15193.5	14779.2	14692.8	14859.8	14599.1	14878.7	14749.7	14746.1	14930.5	14757.3	14807.7	14944.4	97%	100%	102%	99%	101%	100%
		sequential	1000000	160.5		155.6	156.6	161.5	161.7	157.1	156.5	157.6	156.6	158.5	161.5	157.3	98%	101%	100%	98%	100%	100%			
			10000000	1475.1		1459.3	1503.7	1493.4	1457.1	1450.0	1471.4	1469.8	1474.4	1472.5	1458.9	1493.7	100%	101%	98%	99%	100%	103%			
			100000000	14693.7		14507.7	14803.6	14781.7	14810.0	14562.5	14601.4	14551.6	14559.3	14785.9	14893.4	14642.1	99%	100%	98%	100%	101%	101%			
		btree	bitmapscan	master	i5	cycle	1000000	11.3	12.8	25.3	164.6	1553.9	502.2	11.7	12.3	13.7	33.3	197.4	833.1	104%	96%	54%	20%	13%	166%
							10000000	12.0	13.7	25.4	167.6	1545.6	15283.5	11.9	12.4	14.0	33.0	185.8	1709.2	100%	90%	55%	20%	12%	11%
							50000000	12.5	14.7	28.3	167.6	1560.5	15484.7	12.7	13.4	16.4	32.3	188.8	1783.3	101%	91%	58%	19%	12%	12%
						random	1000000	11.5	14.3	25.5	172.4	1025.7	444.7	11.7	12.0	15.6	30.6	187.7	735.4	102%	84%	61%	18%	18%	165%
							10000000	13.0	13.5	23.9	167.6	1583.1	9425.5	12.4	12.1	15.4	32.4	187.0	1642.0	96%	90%	65%	19%	12%	17%
							50000000	12.5	14.8	27.8	169.8	1554.9	15659.4	12.5	13.1	16.1	32.2	193.4	1695.8	100%	88%	58%	19%	12%	11%
			sequential			1000000	11.9	12.1	12.6	15.1	23.0	87.5	11.7	11.6	12.6	15.5	25.1	130.3	98%	96%	99%	103%	109%	149%	
						10000000	11.9	11.5	12.2	17.3	23.9	118.2	11.5	11.4	12.0	17.6	30.0	126.6	97%	99%	98%	102%	125%	107%	
						50000000	12.4	12.3	12.6	15.8	23.2	90.4	12.6	12.2	12.9	15.6	24.6	156.6	102%	99%	103%	99%	106%	173%	
	xeon		cycle			1000000	12.4	13.1	23.7	113.8	286.4	375.7	12.0	13.0	14.1	25.6	144.1	503.2	97%	99%	59%	22%	50%	134%	
						10000000	13.0	12.8	22.3	123.9	1021.4	4314.6	11.8	12.2	13.1	28.6	144.0	1305.5	91%	95%	59%	23%	14%	30%	
						100000000	12.6	15.0	25.7	124.7	1099.6	10079.1	12.2	12.9	15.6	26.3	125.7	1307.6	97%	86%	61%	21%	11%	13%	
		random	1000000	11.7	12.8	22.6	118.7	503.5	373.5	12.1	12.9	14.0	24.8	111.9	457.8	103%	101%	62%	21%	22%	123%				
			10000000	11.0	13.8	20.9	122.7	1079.6	4928.5	11.9	12.4	12.4	25.9	138.1	1051.3	108%	90%	59%	21%	13%	21%				
			100000000	12.2	12.8	25.3	124.6	1106.3	10803.1	12.0	12.1	15.7	27.3	128.3	1269.7	98%	95%	62%	22%	12%	12%				
	sequential	1000000	12.1	14.1	14.2	14.4	23.6	93.0	11.7	12.8	13.0	13.9	24.0	105.7	96%	91%	92%	96%	101%	114%					
		10000000	12.3	12.5	12.9	13.8	22.2	94.4	12.5	11.5	12.8	13.2	23.9	107.4	102%	92%	99%	96%	108%	114%					
		100000000	12.2	13.0	12.2	13.5	22.9	95.4	13.3	13.1	13.1	13.1	23.6	108.8	109%	101%	107%	97%	103%	114%					
		btree	master	i5	cycle	1000000	12.4	12.7	25.7	163.2	1563.8	426.0	11.7	12.3	13.9	30.8	185.0	806.2	95%	97%	54%	19%	12%	189%	
						10000000	12.1	13.5	26.5	168.3	1534.2	15250.1	11.6	12.8	14.0	34.2	192.1	1720.2	96%	95%	53%	20%	13%	11%	
						50000000	12.4	13.7	27.3	168.6	1616.5	15427.9	11.9	13.5	15.1	33.6	193.6	1711.5	96%	98%	55%	20%	12%	11%	
	random				1000000	11.6	13.4	25.3	172.2	1106.8	481.8	11.9	12.1	14.0	30.7	182.4	722.3	102%	91%	55%	18%	16%	150%		
					10000000	12.4	13.6	27.9	167.7	1615.8	9408.9	11.7	12.8	14.6	31.2	201.0	1529.3	94%	94%	52%	19%	12%	16%		
					50000000	12.0	13.3	28.9	162.7	1554.8	15657.7	12.4	13.2	15.8	32.2	191.2	1670.1	103%	99%	55%	20%	12%	11%		
	sequential	1000000	11.7	11.5	12.7	14.9	24.4	115.5	12.3	12.2	12.3	15.4	25.3	126.7	106%	106%	97%	103%	104%	110%					
		10000000	12.1	11.6	12.6	16.0	25.3	121.9	11.7	12.0	11.9	16.1	26.4	127.0	97%	103%	94%	101%	104%	104%					
		50000000	12.7	12.4	12.1	15.4	24.3	90.0	12.0	11.7	12.0	17.3	26.5	155.3	94%	95%	100%	112%	109%	173%					
	xeon	cycle	1000000	12.6	14.3	23.4	114.6	286.9	380.3	12.1	13.1	13.6	27.6	144.6	505.7	96%	92%	58%	24%	50%	133%				
			10000000	12.2	15.0	22.4	122.3	1028.3	4360.2	12.7	12.8	13.3	26.6	141.1	1302.5	104%	86%	59%	22%	14%	30%				
			100000000	13.1	14.5	26.6	122.4	1102.2	10202.6	13.6	13.4	15.2	28.3	123.2	1290.1	104%	92%	57%	23%	11%	13%				
		random	1000000	12.3	13.4	24.1	120.3	514.8	358.9	12.1	13.0	14.0	24.5	112.1	458.0	98%	98%	58%	20%	22%	128%				
			10000000	12.8	14.4	23.2	123.3	1084.6	4958.2	12.0	13.7	14.0	26.8	138.9	1042.8	94%	95%	60%	22%	13%	21%				
			100000000	13.2	15.6	26.4	125.9	1091.5	10766.6	13.0	12.7	17.3	29.9	124.3	1252.3	98%	82%	65%	24%	11%	12%				
		sequential	1000000	12.7	13.5	13.0	15.2	23.2	93.4	12.5	12.6	13.2	14.3	24.1	107.1	98%	94%	102%	94%	104%	115%				
			10000000	12.8	13.2	13.2	13.9	24.1	95.4	12.3	12.5	12.4	14.2	23.6	108.5	96%	95%	94%	102%	98%	114%				
			100000000	14.4	13.1	14.2	14.2	23.3	96.6	14.5	13.7	14.0	14.8	24.5	107.3	101%	104%	99%	105%	105%	111%				
	indexscan	master	i5	cycle	1000000	11.6	13.0	26.0	165.9	1532.2	446.8	11.8	12.6	25.4	164.1	1551.2	439.9	101%	97%	97%	99%	101%	98%		

					10000000	11.5	13.0	26.0	166.8	1527.0	15197.9	11.3	13.2	25.4	164.6	1524.1	15191.8	98%	101%	98%	99%	100%	100%		
					50000000	11.9	14.1	28.0	167.9	1524.3	15403.3	13.0	14.2	28.9	170.1	1547.7	15740.4	109%	101%	103%	101%	102%	102%		
					1000000	12.1	12.5	27.4	168.9	1032.2	468.3	11.1	13.4	26.1	164.9	1101.2	464.0	91%	107%	96%	98%	107%	99%		
					10000000	11.9	13.5	26.9	168.4	1572.4	9368.1	11.8	12.7	26.3	164.7	1571.7	10521.8	99%	94%	98%	98%	100%	112%		
					50000000	12.0	13.3	25.6	164.6	1544.5	15605.6	11.9	14.2	26.4	169.4	1553.8	15210.0	99%	107%	103%	103%	101%	97%		
					1000000	11.4	11.2	11.7	15.7	22.4	122.1	11.2	11.8	12.1	15.7	24.7	119.0	99%	105%	104%	100%	110%	97%		
					10000000	12.0	11.6	12.8	17.2	25.7	88.2	11.6	11.5	11.8	17.0	23.1	121.1	96%	99%	92%	99%	90%	137%		
					50000000	12.4	12.7	12.8	15.7	24.0	119.4	11.9	12.3	12.5	16.5	25.4	91.7	96%	97%	98%	105%	106%	77%		
					xeon	cycle	1000000	12.2	12.6	22.2	111.0	355.9	355.6	11.9	13.1	22.8	111.8	376.7	357.5	97%	104%	103%	101%	106%	101%
							10000000	12.6	12.3	22.0	122.5	1029.4	4310.8	11.5	12.9	22.1	122.6	1025.2	4388.2	91%	104%	100%	100%	100%	102%
						random	100000000	12.4	14.6	26.4	125.3	1097.8	10150.1	11.8	13.9	25.7	120.8	1093.1	10070.9	95%	95%	97%	96%	100%	99%
							1000000	11.5	13.0	22.1	120.9	505.6	352.5	11.7	12.6	23.3	116.8	506.7	351.5	102%	97%	105%	97%	100%	100%
						sequential	10000000	11.1	12.8	21.6	117.5	1073.7	4882.0	11.6	13.4	21.1	121.7	1078.9	4893.3	105%	105%	98%	104%	100%	100%
							100000000	12.5	12.8	27.3	123.3	1102.2	10711.3	12.2	14.2	26.4	126.2	1102.7	10728.2	97%	111%	97%	102%	100%	100%
							1000000	12.1	12.9	13.2	14.3	22.2	94.2	11.8	12.4	12.6	14.1	22.3	94.5	98%	96%	95%	98%	100%	100%
							10000000	12.0	11.5	12.3	14.3	21.6	95.5	12.2	12.5	11.7	14.0	23.6	99.2	102%	109%	95%	98%	109%	104%
100000000	12.3	12.2	12.6	14.1	21.9	95.0	13.1	12.5	13.0	13.1	22.4	95.6	107%	102%	103%	93%	102%	101%							
patched	i5	cycle	1000000	12.2	12.7	25.5	163.7	1538.9	438.0	11.6	11.8	13.7	31.5	193.2	423.2	95%	93%	54%	19%	13%	97%				
			10000000	11.5	13.5	25.8	163.2	1525.3	15291.1	11.7	12.3	14.0	33.0	183.5	1686.2	102%	91%	54%	20%	12%	11%				
		random	50000000	12.2	13.9	27.5	166.2	1649.0	15467.3	11.9	13.0	16.2	34.2	184.5	1693.6	97%	93%	59%	21%	11%	11%				
			1000000	11.7	13.6	25.2	165.2	1083.1	439.6	11.2	12.5	13.4	30.5	191.3	449.1	96%	92%	53%	18%	18%	102%				
		sequential	10000000	11.6	13.5	26.2	163.7	1609.3	9397.6	11.3	12.0	14.6	31.9	196.9	1643.4	97%	89%	56%	19%	12%	17%				
			50000000	11.9	14.4	27.1	175.3	1552.4	15084.1	12.0	13.4	15.4	32.6	188.0	1641.5	101%	93%	57%	19%	12%	11%				
			1000000	11.5	11.5	11.6	16.3	22.9	90.1	12.0	12.4	12.1	14.9	25.6	120.0	105%	108%	104%	91%	112%	133%				
			10000000	12.2	11.6	12.0	17.8	22.4	87.5	11.5	12.0	12.4	16.1	25.7	122.7	95%	104%	103%	90%	115%	140%				
			50000000	12.0	11.8	12.6	16.1	23.0	118.5	11.8	11.8	12.4	15.7	22.8	92.8	98%	100%	98%	97%	99%	78%				
	xeon	cycle	1000000	12.2	13.2	22.3	114.4	328.2	361.3	11.8	13.1	13.8	26.8	138.0	362.7	97%	99%	62%	23%	42%	100%				
			10000000	12.5	13.2	22.0	123.4	1022.0	4366.7	12.0	12.6	13.0	26.4	137.7	1258.0	97%	96%	59%	21%	13%	29%				
		random	100000000	12.6	14.5	26.7	123.1	1099.8	10228.3	13.2	14.4	16.3	28.2	121.5	1249.1	105%	99%	61%	23%	11%	12%				
			1000000	12.2	13.1	24.6	121.5	484.1	347.7	12.2	12.9	13.8	23.7	123.5	359.9	100%	99%	56%	19%	26%	104%				
		sequential	10000000	13.2	13.5	22.9	124.9	1096.4	4895.7	12.6	13.0	13.0	26.3	134.8	1122.8	95%	97%	57%	21%	12%	23%				
			100000000	13.1	15.2	25.7	125.1	1109.5	10773.6	12.2	12.5	16.8	28.0	120.2	1217.3	93%	82%	65%	22%	11%	11%				
			1000000	12.4	12.3	13.3	15.1	22.9	95.7	12.3	12.5	13.2	14.3	22.7	98.7	99%	101%	99%	95%	99%	103%				
			10000000	12.1	12.2	13.8	14.0	23.6	97.3	12.1	12.5	12.7	13.7	23.0	97.8	100%	102%	92%	98%	97%	100%				
			100000000	14.7	13.4	13.6	13.9	23.9	96.2	13.7	13.9	13.9	14.4	24.4	99.4	93%	103%	102%	104%	102%	103%				
seqscan	master	i5	cycle	1000000	362.4	401.0	382.3	354.4	369.4	462.5	369.1	375.9	346.1	350.5	365.5	377.5	102%	94%	91%	99%	99%	82%			
				10000000	3157.5	3135.0	3181.9	3156.4	3190.1	3209.6	3136.6	3190.6	3147.7	3114.0	3077.5	3170.5	99%	102%	99%	99%	96%	99%			
			random	50000000	15608.6	15231.0	16458.6	15195.5	15143.1	15099.9	15711.4	15131.5	14976.3	15083.5	15114.0	15142.8	101%	99%	91%	99%	100%	100%			
				1000000	411.7	375.1	377.5	353.7	395.5	402.2	372.4	401.7	375.5	375.6	338.5	381.2	90%	107%	99%	106%	86%	95%			
			sequential	10000000	3052.6	3183.1	3181.3	3175.9	3211.7	3154.5	3109.0	3163.7	3155.6	3209.9	3186.7	3560.6	102%	99%	99%	101%	99%	113%			
				50000000	15148.0	16398.9	15204.7	15621.1	15115.3	15146.1	15141.5	15153.4	15189.8	16091.5	15089.2	15138.0	100%	92%	100%	103%	100%	100%			
				1000000	369.2	364.0	339.7	366.0	436.0	435.0	404.3	386.5	335.3	397.6	383.9	425.4	110%	106%	99%	109%	88%	98%			
				10000000	3121.2	3379.1	3395.9	3165.8	3679.2	3164.7	3099.6	3175.0	3186.4	3137.8	3420.5	3129.2	99%	94%	94%	99%	93%	99%			
				50000000	15086.9	15199.5	15740.7	15195.8	16197.5	15191.5	15118.1	15199.5	15115.9	15123.5	15808.3	15130.1	100%	100%	96%	100%	98%	100%			
		xeon	cycle	1000000	249.7	245.5	243.1	245.7	181.9	279.0	244.7	245.4	243.8	247.2	244.9	278.0	98%	100%	100%	101%	135%	100%			
				10000000	2179.6	2191.6	2193.7	2187.4	2196.9	2234.4	2189.5	2190.6	2181.4	2176.8	2210.7	2219.9	100%	100%	99%	100%	101%	99%			
			random	100000000	21311.0	21434.6	21394.4	21405.6	21566.6	21676.8	21313.9	21519.2	21479.4	21436.1	21480.4	21628.6	100%	100%	100%	100%	100%	100%			
				1000000	247.8	240.9	245.2	244.5	246.3	275.5	245.9	239.8	245.2	243.7	190.9	276.6	99%	100%	100%	100%	78%	100%			
			sequential	10000000	2190.3	2186.4	2178.1	2175.3	2188.4	2253.1	2203.7	2194.3	2191.4	2176.3	2199.1	2244.7	101%	100%	101%	100%	100%	100%			
				100000000	21725.5	21379.6	21393.1	21260.1	21518.2	21575.4	21759.7	21423.0	21334.5	21160.1	21549.6	21491.3	100%	100%	100%	100%	100%	100%			
				1000000	244.1	247.2	248.7	245.4	251.1	243.4	243.4	247.4	245.3	245.4	253.0	225.3	100%	100%	99%	100%	101%	93%			
				10000000	2185.8	2186.7	2192.7	2205.3	2204.8	2212.2	2193.7	2183.8	2201.4	2192.1	2199.4	2205.7	100%	100%	100%	99%	100%	100%			



					100000000	21848.8	21442.5	21388.6	21380.0	21551.0	21495.5	22039.2	21547.6	21437.0	21419.9	21442.7	21460.2	101%	100%	100%	100%	99%	100%			
					patched	i5	cycle	10000000	359.5	413.4	361.8	365.4	350.1	368.6	373.8	370.4	360.5	354.6	346.2	343.2	104%	90%	100%	97%	99%	93%
					100000000	3149.3		3307.8	3171.0	3076.2	3165.2	3180.9	3105.3	3125.1	3085.7	3078.8	3118.1	3138.9	99%	94%	97%	100%	99%	99%		
					500000000	15174.4		15128.6	15851.4	15180.4	15082.3	15193.9	15157.7	15188.0	15107.9	15199.9	15595.2	15172.1	100%	100%	95%	100%	103%	100%		
					10000000	388.1		360.2	346.2	371.1	426.8	394.9	402.8	354.6	332.2	360.2	357.0	364.1	104%	98%	96%	97%	84%	92%		
					100000000	3104.4		3112.4	3172.0	3208.1	3423.2	3122.3	3124.0	3175.2	3176.0	3155.6	3302.3	3135.7	101%	102%	100%	98%	96%	100%		
					500000000	15139.5		15160.3	15180.2	15114.5	15161.1	15377.1	16081.9	15215.7	15089.8	15121.2	15158.5	15151.4	106%	100%	99%	100%	100%	99%		
					sequential	10000000	396.9	389.5	345.7	401.7	356.1	448.3	371.6	357.8	320.0	354.5	389.8	368.1	94%	92%	93%	88%	109%	82%		
						100000000	3163.6	3147.5	3236.5	3164.1	3199.0	3170.8	3089.6	3157.1	3167.5	3114.7	3202.6	3123.1	98%	100%	98%	98%	100%	98%		
						500000000	15110.1	15138.5	15106.2	15137.8	15901.3	15297.0	15064.3	15813.9	15132.4	15811.9	15126.0	15197.4	100%	104%	100%	104%	95%	99%		
						xeon	cycle	10000000	247.1	248.5	244.4	245.0	244.6	240.7	244.1	244.4	241.4	247.8	244.6	240.5	99%	98%	99%	101%	100%	100%
								100000000	2179.3	2191.0	2179.2	2180.9	2196.2	2215.7	2167.6	2191.7	2179.2	2177.5	2192.7	2215.1	99%	100%	100%	100%	100%	100%
								1000000000	21211.1	21330.0	21248.7	21343.5	21399.1	21501.8	21249.0	21376.0	21492.5	21381.9	21478.9	21537.8	100%	100%	101%	100%	100%	100%
					random		10000000	243.7	237.3	243.0	246.3	185.1	277.1	243.8	241.6	243.1	243.9	241.4	274.7	100%	102%	100%	99%	130%	99%	
							100000000	2206.4	2195.9	2186.8	2177.1	2185.7	2228.4	2198.9	2185.8	2185.8	2165.6	2156.7	2241.7	100%	100%	100%	99%	99%	101%	
							1000000000	21628.2	21300.0	21351.6	21111.6	21462.8	21426.7	21738.6	21430.5	21383.7	21198.5	21344.9	21450.2	101%	101%	100%	100%	99%	100%	
					sequential	10000000	243.2	247.9	248.1	244.5	244.7	241.7	244.8	242.7	243.5	248.3	249.6	238.8	101%	98%	98%	102%	102%	99%		
						100000000	2183.9	2178.6	2184.0	2189.3	2195.4	2197.2	2178.0	2185.3	2223.4	2176.3	2195.1	2205.8	100%	100%	102%	99%	100%	100%		
						1000000000	21727.5	21450.5	21394.8	21273.6	21429.9	21366.4	21733.5	21414.9	21490.1	21305.2	21552.1	21364.4	100%	100%	100%	100%	101%	100%		
						btree-sort	bitmapscan	master	i5	cycle	10000000	231.2	273.4	501.3	592.8	575.5	640.6	548.3	468.9	406.3	441.7	659.1	994.4	237%	172%	81%
100000000	2009.7	2294.0	2287.5	4113.4	4668.8						4743.0	3444.2	3245.7	4194.1	4276.9	3795.5	4948.6	171%	141%	183%	104%	81%	104%			
500000000	9496.9	10014.6	8857.5	11557.1	21661.6						26880.5	16267.3	20956.6	16860.0	17368.7	20908.7	20001.4	171%	209%	190%	150%	97%	74%			
10000000	499.0	461.4	454.2	477.8	550.0						564.9	909.8	887.8	889.6	843.6	892.0	934.3	182%	192%	196%	177%	162%	165%			
100000000	3973.2	3772.4	3861.5	3920.1	3791.7						4229.0	7063.6	7019.4	6857.6	7005.5	7178.3	7608.8	178%	186%	178%	179%	189%	180%			
500000000	18799.9	17997.4	17629.0	18469.6	18197.0						17995.6	35028.6	33691.3	33898.2	34409.1	34781.8	35240.7	186%	187%	192%	186%	191%	196%			
sequential	10000000	286.3	290.0	218.4	269.6					263.0	367.6	525.3	561.6	449.8	538.3	510.2	580.2	183%	194%	206%	200%	194%	158%			
	100000000	1865.3	2162.1	2284.2	2229.9					2419.8	2521.7	4140.8	4058.8	4191.4	4106.0	3948.1	4138.8	222%	188%	183%	184%	163%	164%			
	500000000	11251.0	9760.3	7803.8	11434.4					7729.7	8849.9	20481.5	20683.5	17946.9	16553.8	18654.0	16422.5	182%	212%	230%	145%	241%	186%			
	xeon	cycle	10000000	269.8	287.6					354.5	430.2	436.8	468.7	368.5	354.3	420.2	309.5	435.7	608.5	137%	123%	119%	72%	100%	130%	
			100000000	1782.2	1966.7					1723.7	3486.0	3824.6	3988.3	3222.1	2354.6	3597.1	2900.8	3656.6	3781.6	181%	120%	209%	83%	96%	95%	
			1000000000	20017.0	16512.0					19349.9	16821.8	33319.7	36614.1	22560.1	25577.5	24852.5	33152.1	34365.4	25552.1	113%	155%	128%	197%	103%	70%	
random		10000000	383.9	379.0	424.3					386.1	417.8	501.6	563.8	562.0	540.2	531.3	533.0	659.5	147%	148%	127%	138%	128%	131%		
	100000000	3328.6	3399.3	3238.9	3231.1					3328.4	3337.7	5474.0	5330.7	5268.0	5270.0	5477.6	5563.4	164%	157%	163%	163%	165%	167%			
	1000000000	32352.9	31037.7	31733.6	30821.7					31811.0	31409.1	45518.6	45717.6	44688.9	46190.7	44354.8	46027.6	141%	147%	141%	150%	139%	147%			
	sequential	10000000	256.5	236.5	247.6					246.3	244.9	231.5	327.9	305.9	286.7	334.7	269.7	285.0	128%	129%	116%	136%	110%	123%		
100000000		2307.2	1772.8	2050.0	2101.9					1743.7	1801.8	3151.5	2854.8	2024.4	2452.5	2922.3	2257.1	137%	161%	99%	117%	168%	125%			
1000000000		16102.3	13148.3	13940.3	13602.0					16612.3	14928.7	21910.9	22075.3	21065.4	22577.9	20459.1	20330.3	136%	168%	151%	166%	123%	136%			
patched		i5	cycle	10000000	257.0					220.7	460.6	665.2	563.6	541.2	538.1	512.7	450.7	392.8	711.2	902.7	209%	232%	98%	59%	126%	167%
	100000000			2562.1	1981.4					1981.8	4087.3	4792.5	4790.1	4877.9	4350.7	3491.0	3448.4	4088.3	5400.2	190%	220%	176%	84%	85%	113%	
	500000000			8753.0	8734.6	8605.1	11675.8	21465.5	27779.6	15696.9	21063.3	20172.7	18849.6	21256.0	24166.5	179%	241%	234%	161%	99%	87%					
	10000000			521.8	455.5	474.2	409.3	483.8	501.2	862.7	867.6	928.3	821.2	787.1	924.3	165%	190%	196%	201%	163%	184%					
	100000000			3977.5	3961.2	3735.5	4075.8	3996.0	3947.4	7018.6	7019.7	7287.9	7009.5	7049.7	7601.2	176%	177%	195%	172%	176%	193%					
	500000000			18181.1	18055.4	17611.3	17935.6	17909.1	18103.5	34829.6	34488.6	33485.4	33860.2	34146.4	34233.1	192%	191%	190%	189%	191%	189%					
	sequential		10000000	287.0	260.1	239.2	263.1	217.0	385.2	403.4	408.4	437.6	434.9	420.1	567.5	141%	157%	183%	165%	194%	147%					
			100000000	2404.2	2077.7	2161.3	2431.6	2627.6	2431.5	4083.7	4156.1	3803.3	4211.7	3350.0	3798.4	170%	200%	176%	173%	127%	156%					
			500000000	9458.7	8337.3	8318.1	7562.3	11009.0	9114.8	19276.6	15787.7	17004.8	14700.5	14904.5	14521.5	204%	189%	204%	194%	135%	159%					
			xeon	cycle	10000000	224.2	273.5	337.4	438.2	422.3	464.0	313.7	289.7	351.4	331.6	425.4	614.4	140%	106%	104%	76%	101%	132%			
	100000000				1774.5	1716.4	2199.7	3098.8	3850.8	3753.9	2474.8	2410.8	2497.8	3937.0	2974.9	4080.3	139%	140%	114%	127%	77%	109%				
	1000000000				15064.2	16107.6	17006.2	21357.4	34717.0	35533.8	20111.9	26335.9	23221.9	30438.4	36588.8	27505.2	134%	163%	137%	143%	105%	77%				
	10000000				402.9	377.9	379.2	396.8	414.6	501.3	587.8	577.7	543.3	524.5	556.5	666.4	146%	153%	143%	132%	134%	133%				
	random			100000000	3636.3	3253.2	3160.3	3328.9	3179.6	3380.7	5464.4	5487.6	5392.7	5240.6	5412.4	5548.8	150%	169%	171%	157%	170%	164%				
1000000000		32541.8		31861.6	30841.5	31319.0	30412.3	30878.9	47112.4	46278.2	46355.0	44693.2	45371.2	45704.6	145%	145%	150%	143%	149%	148%						

indexscan	master	i5	sequential	1000000	211.5	266.3	221.8	184.6	232.8	273.8	282.7	342.7	258.8	345.0	309.1	335.7	134%	129%	117%	187%	133%	123%
				10000000	2109.2	2030.9	1654.3	2073.0	1790.2	2063.4	2724.3	2695.6	2456.1	2244.5	2226.6	2456.9	129%	133%	148%	108%	124%	119%
				100000000	16286.9	17867.9	17126.9	16418.7	15020.2	14866.1	24748.9	22511.6	21073.4	22895.1	27114.6	22983.6	152%	126%	123%	139%	181%	155%
			cycle	1000000	11.7	14.8	26.6	168.2	1569.7	460.4	11.8	13.3	26.3	171.8	1563.3	453.0	100%	90%	99%	102%	100%	98%
				10000000	12.0	13.5	25.7	165.5	1545.0	15162.5	11.8	13.1	25.6	165.1	1542.1	15333.0	99%	96%	99%	100%	100%	101%
				50000000	14.1	14.7	27.8	163.2	1554.1	15351.3	14.4	14.2	28.5	164.2	1564.2	15383.1	102%	97%	102%	101%	101%	100%
			random	1000000	12.4	13.4	26.4	168.9	1045.5	432.6	13.0	13.4	26.8	170.5	1018.6	447.1	104%	100%	101%	101%	97%	103%
				10000000	11.3	13.7	25.7	169.4	1558.5	9590.1	12.1	13.6	26.1	165.2	1545.0	9443.7	107%	100%	101%	97%	99%	98%
				50000000	12.4	14.3	27.3	167.9	1558.9	15646.4	12.5	14.3	28.9	166.2	1540.2	15366.7	100%	99%	106%	99%	99%	98%
			sequential	1000000	12.3	12.1	12.3	17.4	24.8	89.1	14.0	12.0	12.3	15.7	23.9	118.3	113%	99%	101%	90%	96%	133%
				10000000	12.3	12.3	12.6	14.4	25.3	91.7	11.8	12.1	11.9	13.9	25.0	120.2	96%	99%	94%	96%	99%	131%
				50000000	12.7	12.1	12.9	15.0	23.4	91.1	12.9	13.2	12.8	14.5	22.6	95.0	102%	109%	99%	97%	96%	104%
	patched	i5	cycle	1000000	11.8	13.5	23.4	113.3	484.7	355.7	11.1	13.4	22.4	113.5	438.2	360.1	93%	99%	96%	100%	90%	101%
				10000000	11.5	12.6	22.3	119.9	1041.3	4400.4	11.4	12.9	22.6	120.1	1029.9	4429.3	100%	103%	102%	100%	99%	101%
				100000000	12.0	14.6	26.9	143.3	1121.6	10198.2	12.2	14.5	26.3	142.8	1097.7	10107.2	102%	100%	98%	100%	98%	99%
			random	1000000	11.6	13.1	22.9	118.1	522.0	355.8	11.0	12.0	22.0	117.9	509.0	354.3	95%	92%	96%	100%	98%	100%
				10000000	11.7	12.9	22.7	119.9	1096.4	5441.8	11.3	11.9	23.1	120.5	1091.4	5418.1	96%	92%	102%	100%	100%	100%
				100000000	13.3	13.8	27.5	144.4	1158.2	10806.8	11.9	14.0	27.2	145.2	1191.1	10841.8	89%	101%	99%	101%	103%	100%
			sequential	1000000	12.6	11.3	13.1	13.0	21.9	96.1	11.7	11.8	11.7	13.1	21.5	96.0	93%	104%	90%	101%	98%	100%
				10000000	12.0	11.9	12.1	13.5	21.5	96.1	12.5	11.1	12.0	13.4	21.4	96.1	104%	94%	100%	100%	99%	100%
				100000000	12.4	11.7	12.6	13.7	22.4	97.1	11.8	12.1	12.3	13.2	21.5	96.8	96%	103%	98%	96%	96%	100%
			cycle	1000000	12.1	13.8	26.1	170.3	1570.2	411.2	12.3	12.6	15.1	34.6	191.9	414.7	102%	92%	58%	20%	12%	101%
				10000000	11.7	13.3	25.9	164.9	1534.4	15422.4	12.1	12.5	14.3	31.6	190.0	1666.2	104%	93%	55%	19%	12%	11%
				50000000	12.4	14.2	29.0	169.9	1564.4	15362.3	12.9	13.5	16.8	31.5	190.3	1684.8	104%	95%	58%	19%	12%	11%
			random	1000000	12.0	13.5	27.0	171.1	1027.5	450.3	13.4	12.8	16.8	30.2	178.6	466.5	112%	94%	62%	18%	17%	104%
				10000000	11.5	13.5	26.3	165.8	1556.7	9438.1	11.9	12.3	14.4	30.8	185.1	1781.0	104%	91%	55%	19%	12%	19%
				50000000	12.2	13.9	28.4	168.4	1546.1	15693.3	12.2	13.6	16.2	32.7	185.3	1744.9	100%	98%	57%	19%	12%	11%
			sequential	1000000	12.2	12.1	12.4	14.2	22.8	89.5	11.8	11.9	13.6	16.8	23.4	120.1	97%	99%	109%	118%	103%	134%
				10000000	11.8	12.0	12.7	14.6	28.8	89.3	11.7	12.4	12.1	15.0	23.8	118.8	99%	103%	95%	102%	83%	133%
				50000000	12.5	12.5	12.8	14.8	22.5	89.9	12.5	13.0	13.5	15.0	24.7	92.9	100%	104%	105%	102%	110%	103%
seqscan	master	i5	cycle	1000000	12.0	13.3	22.9	113.3	481.5	360.0	11.9	12.9	14.2	25.0	134.5	357.5	99%	97%	62%	22%	28%	99%
				10000000	12.5	13.0	22.8	120.1	1039.5	4431.9	12.0	13.0	13.8	23.6	136.4	1259.8	96%	100%	61%	20%	13%	28%
				100000000	11.9	13.8	27.0	144.0	1102.4	10164.9	12.6	13.2	15.3	26.6	118.8	1250.6	106%	96%	57%	18%	11%	12%
			random	1000000	11.9	12.7	22.8	121.2	523.0	352.4	12.5	11.7	13.2	24.1	124.0	359.9	105%	92%	58%	20%	24%	102%
				10000000	11.9	13.1	23.0	120.4	1096.1	5422.5	11.7	12.1	13.8	25.0	134.2	1151.7	98%	92%	60%	21%	12%	21%
				100000000	12.3	13.9	26.7	145.8	1171.8	10851.0	12.7	13.3	16.5	27.7	119.7	1225.2	103%	95%	62%	19%	10%	11%
			sequential	1000000	11.9	12.1	13.1	13.2	22.4	97.5	12.0	12.9	12.6	14.6	22.5	98.5	101%	106%	96%	110%	100%	101%
				10000000	11.6	12.6	12.3	14.8	21.3	95.3	12.2	12.2	12.3	13.7	22.4	98.2	104%	97%	100%	93%	105%	103%
				100000000	12.1	12.1	12.9	13.8	22.5	96.9	12.6	11.9	12.9	14.9	22.7	99.0	105%	98%	100%	108%	101%	102%
	patched	i5	cycle	1000000	358.0	349.3	419.2	434.0	406.9	533.6	420.9	331.1	393.2	443.6	417.6	481.3	118%	95%	94%	102%	103%	90%
				10000000	3511.8	3190.7	3435.0	3178.4	3181.9	3313.9	3147.0	3152.4	3168.0	3206.6	3196.5	3300.8	90%	99%	92%	101%	100%	100%
				50000000	15463.8	15359.3	15459.0	15395.5	16428.8	16826.9	15338.8	15416.1	15367.4	15377.3	16647.8	15653.1	99%	100%	99%	100%	101%	93%
			random	1000000	424.4	357.5	361.4	413.1	495.4	472.8	410.2	407.7	393.2	360.3	439.3	475.3	97%	114%	109%	87%	89%	101%
				10000000	3201.1	3102.8	3330.7	3182.4	3186.4	3305.9	3195.5	3094.5	3199.6	3484.0	3267.7	3310.5	100%	100%	96%	109%	103%	100%
				50000000	16479.8	16259.7	15304.7	15363.0	15331.0	15542.6	15469.5	17375.3	15373.2	15519.3	15467.6	15565.2	94%	107%	100%	101%	101%	100%
			sequential	1000000	386.9	382.8	400.7	357.1	362.3	411.2	430.1	404.5	372.9	363.4	370.7	394.0	111%	106%	93%	102%	102%	96%
				10000000	3448.7	3463.7	3203.3	3283.0	3502.1	3282.9	3215.7	3181.7	3213.3	3208.8	3124.7	3220.3	93%	92%	100%	98%	89%	98%
				50000000	15327.1	15402.3	15309.0	15455.2	15421.5	15444.9	15474.1	15396.9	16431.5	15418.2	17729.9	15832.0	101%	100%	107%	100%	115%	103%
	xeon		cycle	1000000	233.5	265.1	251.6	268.5	275.5	356.4	262.8	260.3	262.3	265.0	282.4	355.6	113%	98%	104%	99%	103%	100%
				10000000	2380.5	2294.3	2340.2	2318.6	2328.9	2401.2	2361.5	2326.9	2330.6	2362.9	2313.9	2371.4	99%	101%	100%	102%	99%	99%
				100000000	22775.3	22374.8	22833.2	22658.4	22709.8	22705.6	22674.6	22610.6	22534.6	22658.4	22693.3	22759.8	100%	101%	99%	100%	100%	100%
			random	1000000	267.2	271.3	264.2	261.2	277.9	353.0	263.7	272.8	266.4	258.5	279.7	366.7	99%	101%	101%	99%	101%	104%

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					100000000	12.2	14.8	27.8	125.6	1107.1	10069.0	12.6	14.0	17.0	29.2	128.7	1299.5	103%	94%	61%	23%	12%	13%		
					10000000	12.6	13.6	22.7	118.6	507.9	398.9	13.0	12.9	14.4	26.3	116.1	513.2	104%	95%	63%	22%	23%	129%		
					100000000	11.4	13.0	24.0	127.6	1095.0	4855.1	11.7	12.3	14.9	25.6	143.1	1053.6	102%	95%	62%	20%	13%	22%		
					1000000000	12.8	14.6	27.2	129.7	1103.6	10763.2	13.1	13.0	16.5	29.6	126.3	1276.1	102%	89%	61%	23%	11%	12%		
					10000000	12.9	12.4	12.6	15.1	24.6	99.9	13.1	12.7	12.6	14.3	25.8	111.7	102%	102%	100%	94%	105%	112%		
					100000000	12.9	13.2	13.3	16.4	23.7	99.9	12.4	12.8	13.3	14.6	26.0	111.7	96%	97%	100%	89%	110%	112%		
					1000000000	12.9	11.8	14.7	15.5	24.1	100.0	13.3	13.1	13.8	14.9	24.9	114.6	103%	111%	93%	96%	103%	115%		
	indexscan	master	i5	cycle	10000000	12.7	13.6	25.5	165.9	1555.3	6806.5	12.6	13.0	26.2	169.1	1557.9	6833.7	99%	95%	103%	102%	100%	100%		
					100000000	11.6	13.7	26.3	164.3	1672.4	15377.2	11.8	14.1	25.8	170.4	1636.9	15309.3	101%	103%	98%	104%	98%	100%		
					500000000	12.6	15.2	28.2	170.1	1539.8	15437.9	12.8	14.4	28.1	168.0	1554.2	15396.6	101%	95%	100%	99%	101%	100%		
				10000000	12.6	13.3	25.4	163.9	1390.4	6260.0	11.7	15.3	25.0	165.0	1404.2	6190.1	93%	115%	99%	101%	101%	99%			
				100000000	12.3	14.6	28.8	165.7	1629.9	9486.9	12.0	13.2	26.3	168.2	1548.3	9362.1	98%	90%	91%	101%	95%	99%			
				500000000	11.7	14.1	27.9	164.4	1549.7	15669.7	12.3	14.2	27.4	163.4	1534.2	15475.0	105%	101%	98%	99%	99%	99%			
			sequential	10000000	11.6	11.7	12.7	21.8	31.7	127.7	12.1	11.8	13.0	21.6	33.4	101.8	105%	101%	102%	99%	105%	80%			
				100000000	12.0	12.3	12.4	16.9	25.1	123.7	11.9	12.1	12.6	18.0	25.7	97.8	99%	98%	101%	106%	102%	79%			
				500000000	12.1	11.9	12.8	14.7	25.3	94.4	12.1	12.6	12.8	17.5	23.3	123.4	100%	105%	100%	119%	92%	131%			
		xeon	cycle	10000000	11.6	12.0	22.1	119.8	1094.6	546.2	11.5	12.6	23.1	120.1	1092.0	541.5	99%	106%	105%	100%	100%	99%			
				100000000	11.5	12.2	23.2	124.5	1024.1	4033.5	11.8	11.9	22.8	121.5	1021.8	4056.2	102%	97%	98%	98%	100%	101%			
				1000000000	12.2	14.9	27.1	128.4	1100.2	10053.1	12.5	14.0	25.0	124.9	1107.2	10069.7	102%	94%	92%	97%	101%	100%			
			random	10000000	11.4	12.0	22.4	117.8	934.4	3411.7	12.0	11.9	22.0	118.0	928.1	3434.2	106%	99%	98%	100%	99%	101%			
				100000000	12.3	13.8	24.5	121.8	1073.0	4851.1	11.8	13.6	22.2	125.9	1087.2	4771.2	96%	99%	91%	103%	101%	98%			
				1000000000	13.3	14.7	25.0	126.9	1095.2	10761.0	13.1	13.5	26.3	126.9	1109.7	10737.2	99%	92%	106%	100%	101%	100%			
		sequential	10000000	11.9	11.4	12.6	17.9	27.0	103.5	11.6	11.2	12.0	17.5	27.3	104.7	98%	98%	96%	98%	101%	101%				
			100000000	12.6	11.8	12.1	13.8	22.6	99.8	11.5	11.2	12.1	15.1	23.3	98.8	91%	95%	100%	110%	103%	99%				
			1000000000	13.4	12.5	13.8	14.9	22.8	100.7	12.8	12.0	13.6	14.9	22.7	101.9	95%	96%	98%	100%	100%	101%				
		patched	i5	cycle	10000000	12.4	13.5	27.7	168.6	1560.5	6859.8	13.0	12.4	14.3	33.0	205.8	964.9	104%	92%	52%	20%	13%	14%		
					100000000	11.9	13.2	25.8	166.6	1599.0	15336.1	11.5	12.7	14.4	33.4	198.5	1699.0	97%	96%	56%	20%	12%	11%		
					500000000	13.0	15.0	27.4	166.5	1544.1	15460.7	12.4	13.6	16.0	33.9	189.6	1686.6	95%	91%	58%	20%	12%	11%		
10000000	11.7			13.1	23.9	167.2	1390.8	6250.9	11.3	13.0	14.7	31.8	172.2	875.4	97%	99%	61%	19%	12%	14%					
100000000	11.8			13.5	26.8	161.2	1560.7	9569.8	11.7	13.2	14.8	33.7	193.0	1686.3	99%	98%	55%	21%	12%	18%					
500000000	12.1			14.6	28.2	166.2	1565.5	15564.1	12.0	13.0	17.3	32.7	198.9	1636.1	100%	89%	61%	20%	13%	11%					
sequential	10000000		11.9	11.7	12.8	22.1	30.1	130.5	12.2	12.5	13.4	17.4	52.0	422.7	103%	106%	105%	79%	173%	324%					
	100000000		12.2	12.1	12.4	16.7	22.8	125.2	11.9	11.8	12.2	16.9	24.3	95.1	97%	97%	98%	101%	107%	76%					
	500000000		12.5	12.3	12.2	15.9	23.7	91.9	12.3	12.1	13.0	16.9	25.2	124.3	98%	98%	107%	106%	106%	135%					
xeon	cycle		10000000	12.6	12.6	24.1	119.8	1094.7	541.2	12.3	12.3	13.6	24.8	115.0	1045.8	97%	98%	57%	21%	11%	193%				
			100000000	12.3	14.1	23.3	120.4	1031.8	4064.5	11.5	11.6	13.6	26.9	139.3	1271.8	94%	83%	58%	22%	14%	31%				
			1000000000	12.1	14.8	26.4	127.2	1107.0	10109.2	12.7	13.4	17.0	28.5	123.9	1257.4	105%	91%	64%	22%	11%	12%				
	random	10000000	12.2	13.4	22.6	119.6	930.0	3451.1	12.9	12.6	13.2	25.2	113.1	1040.6	106%	94%	58%	21%	12%	30%					
		100000000	12.2	13.1	22.7	121.4	1077.5	4864.0	12.4	12.2	14.4	25.1	135.6	1122.6	102%	93%	64%	21%	13%	23%					
		1000000000	12.8	15.1	27.8	128.3	1113.2	10776.2	12.8	12.7	15.8	29.1	123.8	1231.1	100%	84%	57%	23%	11%	11%					
sequential	10000000	12.6	12.9	12.7	17.5	28.5	105.8	12.5	12.6	12.4	15.9	41.1	290.3	99%	98%	98%	91%	144%	274%						
	100000000	12.4	11.9	13.0	15.6	23.4	99.3	12.3	12.7	13.2	14.5	23.8	102.0	99%	107%	102%	93%	102%	103%						
	1000000000	12.7	12.0	14.1	14.9	23.5	101.9	12.2	13.7	13.7	15.6	24.2	104.0	96%	115%	97%	105%	103%	102%						
	seqscan	master	i5	cycle	10000000	351.0	329.9	383.7	345.0	400.8	403.7	390.1	320.9	357.9	394.2	367.9	388.0	111%	97%	93%	114%	92%	96%		
					100000000	3128.9	3079.7	3129.9	3150.1	3423.6	3123.2	3084.7	3092.8	3106.2	3141.8	3642.1	3392.2	99%	100%	99%	100%	106%	109%		
				500000000	15180.4	15147.6	15122.8	15196.4	15175.3	15154.7	15079.2	15110.5	15107.3	15129.3	15155.9	15243.3	99%	100%	100%	100%	100%	101%			
				10000000	338.2	385.3	380.4	369.4	362.2	431.5	398.0	363.5	341.4	328.9	371.0	347.8	118%	94%	90%	89%	97%	81%			
			100000000	3108.7	3158.2	3407.2	3388.0	3349.4	3146.9	3104.1	3123.7	3314.6	3179.5	3104.3	3111.8	100%	99%	97%	94%	93%	99%				
			500000000	15140.6	15090.0	15340.4	17940.0	15174.1	15143.6	15069.5	15755.6	15155.7	15083.9	15087.5	15207.5	100%	104%	99%	84%	99%	100%				
			10000000	398.9	366.6	396.5	399.0	392.4	330.5	409.6	349.3	352.3	346.3	373.2	407.1	103%	95%	89%	87%	95%	123%				
			100000000	3053.6	3223.3	3566.2	3088.4	3147.7	3180.6	3068.8	3172.8	3254.7	3160.4	3126.7	3312.6	100%	98%	91%	102%	99%	104%				
		500000000	15167.8	15161.7	15121.3	15709.0	16918.8	15899.5	15161.4	15130.0	15001.9	15163.8	15123.1	15107.1	100%	100%	99%	97%	89%	95%					
							10000000	351.0	329.9	383.7	345.0	400.8	403.7	390.1	320.9	357.9	394.2	367.9	388.0	111%	97%	93%	114%	92%	96%
							100000000	3128.9	3079.7	3129.9	3150.1	3423.6	3123.2	3084.7	3092.8	3106.2	3141.8	3642.1	3392.2	99%	100%	99%	100%	106%	109%
							500000000	15180.4	15147.6	15122.8	15196.4	15175.3	15154.7	15079.2	15110.5	15107.3	15129.3	15155.9	15243.3	99%	100%	100%	100%	100%	101%
							10000000	338.2	385.3	380.4	369.4	362.2	431.5	398.0	363.5	341.4	328.9	371.0	347.8	118%	94%	90%	89%	97%	81%
							100000000	3108.7	3158.2	3407.2	3388.0	3349.4	3146.9	3104.1	3123.7	3314.6	3179.5	3104.3	3111.8	100%	99%	97%	94%	93%	99%
							500000000	15140.6	15090.0	15340.4	17940.0	15174.1	15143.6	15069.5	15755.6	15155.7	15083.9	15087.5	15207.5	100%	104%	99%	84%	99%	100%
							10000000	398.9	366.6	396.5	399.0	392.4	330.5	409.6	349.3	352.3	346.3	373.2	407.1	103%	95%	89%	87%	95%	123%
100000000	3053.6						3223.3	3566.2	3088.4	3147.7	3180.6	3068.8	3172.8	3254.7	3160.4	3126.7	3312.6	100%	98%	91%	102%	99%	104%		

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