MEDIAN of duration							prefetch	matches														
							0						32									
caching	test	scan_type	build	machine	dataset	rows	1	10	100	1000	10000	100000	1	10	100	1000	10000	100000	1	10	100	1000	10000	100000
cached	btree	bitmapscan	master	i5	cycle	1000000	7.9	8.0	8.2	9.8	23.6	98.7	8.0	7.9	8.1	9.8	24.6	105.9	102%	98%	99%	100%	104%	107%
						10000000	7.8	8.1	8.1	9.8	23.6	178.1	7.8	7.9	8.1	9.8	24.7	188.9	99%	98%	100%	100%	105%	106%
						50000000	8.0	8.0	8.1	9.7	24.2	171.9	7.9	8.1	8.1	9.9	25.0	179.2	99%	102%	100%	103%	103%	104%
					random	1000000	7.8	7.9	8.3	9.9	22.4	96.8	7.9	7.9	8.2	9.9	23.7	103.3	101%	100%	99%	100%	106%	107%
						10000000	7.9	7.9	8.1	9.8	23.4	153.9	7.8	7.7	8.1	10.0	24.9	167.3	99%	98%	100%	102%	106%	109%
						50000000	7.9	8.1	8.2	9.8	24.0	166.1	7.9	7.9	8.2	9.9	24.9	175.9	100%	98%	100%	101%	104%	106%
					sequential	1000000	7.8	8.0	8.0	8.4	12.8	54.3	7.9	7.9	8.0	8.4	12.9	55.2	100%	99%	101%	101%	100%	102%
						10000000	8.0	7.9	7.8	8.5	12.9	54.7	8.0	7.9	7.9	8.5	12.9	55.5	100%	100%	102%	100%	101%	101%
						50000000	8.0	7.8	8.0	8.5	12.9	55.0	7.9	8.0	8.1	8.4	13.1	56.0	99%	102%	101%	100%	102%	102%
				xeon	cycle	1000000	9.1	9.3	9.4	11.1	26.7	124.1	9.5	10.6	10.9	12.9	28.4	132.6	105%	114%	115%	117%	106%	107%
						10000000	9.9	10.2	9.0	12.4	27.4	203.6	9.9	9.1	9.8	12.4	27.6	216.7	100%	90%	108%	100%	101%	106%
						100000000	10.4	10.0	10.9	11.2	27.0	207.4	9.4	9.8	10.5	11.7	27.8	225.7	91%	99%	96%	104%	103%	109%
					random	1000000	9.1	10.2	10.0	11.7	26.4	110.6	9.5	10.5	10.1	12.5	26.4	118.8	104%	103%	101%	107%	100%	107%
						10000000	10.4	9.4	10.0	11.4	27.4	187.5	10.6	10.1	9.9	11.6	28.2	211.8	102%	107%	99%	102%	103%	113%
						100000000	10.2	10.1	10.2	11.8	27.3	199.8	9.0	11.0	10.7	12.1	27.9	211.4	88%	109%	105%	102%	102%	106%
					sequential	1000000	9.5	10.4	9.3	10.9	15.1	64.9	9.8	10.5	9.0	10.7	15.2	66.1	103%	101%	96%	99%	101%	102%
						10000000	9.7	10.1	10.1	10.1	15.3	65.3	9.6	10.3	9.9	10.6	16.1	64.7	100%	101%	98%	105%	105%	99%
						100000000	9.8	9.4	9.7	10.9	15.6	65.4	9.6	10.2	10.3	9.5	16.3	66.3	98%	109%	106%	88%	105%	101%
			patched	i5	cycle	1000000	7.7	7.8	8.1	9.6	23.6	98.4	7.8	7.9	8.0	9.7	24.6	105.6	101%	101%	99%	100%	104%	107%
						10000000	8.0	7.9	8.0	9.6	23.3	177.8	7.8	7.9	8.1	9.7	24.7	179.6	97%	100%	101%	102%	106%	101%
						50000000	8.0	8.0	8.2	9.6	23.7	167.8	8.0	8.0	8.1	9.8	24.6	179.1	101%	100%	99%	101%	104%	107%
					random	1000000	7.8	7.9	8.2	9.7	22.1	96.9	7.9	7.8	8.2	9.8	23.5	103.6	102%	99%	100%	102%	106%	107%
						10000000	7.7	7.8	8.4	9.7	23.6	152.6	7.8	8.0	8.1	9.7	24.5	166.7	102%	102%	97%	101%	104%	109%
						50000000	7.9	7.9	8.2	9.7	23.8	165.3	7.9	8.0	8.1	9.8	24.8	176.2	100%	101%	99%	102%	104%	107%
					sequential	1000000	7.9	7.9	8.1	8.4	12.6	54.2	7.7	7.8	7.9	8.3	12.7	55.1	98%	99%	97%	98%	101%	102%
						10000000	7.7	7.8	7.9	8.4	12.7	54.4	7.8	7.8	7.9	8.4	12.7	55.2	101%	100%	100%	100%	100%	101%
						50000000	7.8	7.9	7.9	8.5	12.9	55.0	7.9	7.9	7.9	8.5	12.9	55.6	101%	100%	101%	101%	100%	101%
				xeon	cycle	1000000	9.4	10.3	10.6	10.9	26.9	124.0	9.2	9.3	9.1	12.5	28.1	131.7	98%	90%	86%	114%	104%	106%
						10000000	9.9	8.9	9.1	12.9	27.8	203.9	10.2	10.3	9.1	12.3	27.7	217.4	103%	116%	100%	96%	100%	107%
						100000000	9.6	10.1	11.2	12.4	27.4	206.5	9.3	10.0	9.9	10.8	28.0	210.2	97%	99%	88%	87%	102%	102%
					random	1000000	9.6	10.4	10.7	12.9	26.3	111.0	10.0	10.4	10.4	12.5	26.8	126.8	104%	100%	97%	97%	102%	114%
						10000000	10.3	10.4	10.7	11.8	28.1	189.7	9.6	10.2	10.8	11.4	27.9	201.3	94%	98%	101%	96%	99%	106%
						100000000	9.6	10.5	11.0	11.6	27.1	202.4	9.5	10.5	10.3	11.6	27.8	227.3	99%	100%	93%	100%	102%	112%
					sequential	1000000	10.2	10.4	9.6	10.8	15.1	65.6	9.5	10.3	8.8	11.3	15.9	65.5	93%	99%	92%	104%	105%	100%
						10000000	10.0	10.3	9.9	11.1	15.6	66.2	10.1	9.7	10.5	10.4	16.1	66.9	101%	94%	105%	94%	103%	101%
						100000000	9.8	10.2	9.2	10.1	15.0	66.3	10.4	9.9	9.3	10.2	15.6	66.1	106%	97%	101%	101%	104%	100%
		indexscan	master	i5	cycle	1000000	8.0	8.0	7.9	9.5	20.7	83.2	7.8	7.8	8.1	9.1	20.2	83.2	98%	97%	103%	97%	98%	100%
						10000000	7.9	7.9	8.1	9.4	20.3	124.0	7.8	8.0	8.1	9.3	20.4	124.4	100%	101%	100%	99%	101%	100%
						50000000	7.9	8.1	8.2	9.6	20.5	124.8	8.0	7.9	8.2	9.4	20.6	124.1	101%	98%	100%	98%	100%	99%
					random	1000000	7.8	7.9	8.0	9.6	19.1	81.2	7.9	8.0	8.1	9.5	19.4	81.4	101%	102%	101%	98%	101%	100%
						10000000	7.8	7.8	8.2	9.5	20.3	112.9	7.9	8.0	8.1	9.4	20.0	113.1	101%	102%	99%	99%	99%	100%
						50000000	7.9	8.0	8.2	9.6	20.8	122.1	8.0	7.9	8.3	9.3	20.6	122.2	101%	98%	101%	97%	99%	100%
					sequential	1000000	7.9	7.8	7.9	8.5	12.9	54.7	7.9	7.7	8.0	8.4	12.8	54.4	99%	99%	102%	98%	99%	100%
						10000000	8.0	7.9	7.9	8.4	13.0	55.6	7.8	7.9	8.0	8.5	13.0	54.8	98%	100%	101%	101%	100%	99%
						50000000	7.9	7.9	8.0	8.5	12.9	55.5	8.0	8.0	8.1	8.5	12.9	55.3	101%	101%	102%	100%	100%	100%
				xeon	cycle	1000000	9.3	9.3	9.2	10.8	22.4	103.3	9.2	10.5	10.5	12.6	22.4	106.5	100%	113%	114%	116%	100%	103%
						10000000	10.2	9.7	9.0	11.6	23.7	155.1	10.1	8.9	9.4	11.6	24.5	154.7	99%	92%	104%	100%	104%	100%
						100000000	10.5	9.4	10.8	10.7	23.5	160.1	9.6	9.8	10.5	10.7	23.7	159.8	91%	104%	97%	100%	101%	100%
					random	1000000	9.6	10.3	9.8	10.9	22.8	93.7	10.1	10.3	9.7	12.0	22.8	93.9	105%	100%	99%	109%	100%	100%

					10000000	10.1	9.6	9.3	10.6	23.6	137.0	10.4	10.2	9.5	11.4	23.5	148.7	103%	107%	103%	108%	99%	109%
					100000000	9.6	9.7	10.5	11.4	23.8	146.1	9.0	10.7	10.6	12.3	22.7	154.8	94%	111%	100%	108%	96%	106%
				sequential	1000000	9.3	10.0	9.2	10.8	15.5	67.4	10.0	10.1	9.2	11.0	15.3	68.1	108%	101%	99%	102%	99%	101%
					10000000	9.4	10.3	10.2	10.4	15.5	67.7	9.6	10.1	9.5	10.2	15.9	66.4	102%	98%	94%	98%	103%	98%
					100000000	9.3	9.1	9.5	10.7	16.1	79.0	9.8	10.4	9.7	9.8	16.2	77.1	106%	113%	102%	92%	100%	98%
		patched	i5	cycle	1000000	7.8	7.8	7.8	9.2	20.1	83.6	7.8	7.8	8.0	9.4	21.6	88.1	99%	101%	101%	102%	107%	105%
					10000000	7.8	7.8	8.1	9.4	21.0	123.6	7.9	7.9	8.0	9.5	21.6	137.0	101%	101%	99%	101%	103%	111%
					50000000	8.0	7.8	8.0	9.5	20.3	124.5	7.9	7.9	8.1	9.5	21.7	136.7	99%	101%	102%	100%	107%	110%
				random	1000000	7.7	7.9	8.2	9.3	19.1	81.2	7.9	7.8	8.0	9.5	21.1	86.5	102%	98%	98%	102%	110%	106%
					10000000	7.9	7.9	8.1	9.3	20.2	112.4	7.9	7.8	7.9	9.5	21.6	129.6	101%	99%	98%	102%	107%	115%
					50000000	7.9	7.9	8.1	9.6	20.2	122.3	8.0	7.9	8.2	9.7	21.7	137.0	101%	100%	102%	101%	107%	112%
				sequential	1000000	7.9	7.8	7.8	8.4	12.8	55.0	7.9	7.9	8.0	8.4	13.2	59.0	100%	100%	102%	101%	103%	107%
					10000000	7.7	7.9	7.8	8.4	13.0	55.3	7.9	7.9	8.0	8.8	13.2	59.3	102%	100%	102%	105%	102%	107%
					50000000	7.9	7.8	8.0	8.4	12.9	56.2	8.0	7.7	8.0	8.6	13.7	59.6	101%	98%	100%	103%	106%	106%
			xeon	cycle	1000000	9.0	10.6	11.0	10.5	22.8	107.1	9.1	9.9	9.1	12.3	24.2	109.0	101%	93%	83%	116%	106%	102%
					10000000	9.8	9.2	9.4	12.2	23.7	156.5	9.8	10.1	9.2	11.8	24.5	171.5	100%	109%	98%	97%	103%	110%
					100000000	9.5	10.0	10.5	12.0	23.5	163.0	9.2	9.7	9.7	10.5	24.0	159.8	96%	97%	93%	87%	102%	98%
				random	1000000	9.6	10.4	10.6	12.1	22.6	96.1	10.1	10.3	10.0	12.3	23.0	102.7	105%	99%	94%	102%	102%	107%
					10000000	10.4	10.9	10.9	11.1	24.7	148.3	9.5	10.8	10.8	11.1	24.2	161.3	91%	99%	99%	100%	98%	109%
					100000000	9.7	10.4	10.8	11.0	24.0	151.7	9.4	9.8	9.8	11.0	24.0	172.2	97%	95%	91%	100%	100%	113%
				sequential	1000000	10.3	10.4	9.4	11.3	15.1	66.3	9.2	10.2	8.9	11.6	15.9	65.4	89%	98%	95%	102%	105%	99%
					10000000	9.6	10.3	10.3	11.1	16.2	68.2	10.1	9.7	10.3	10.5	16.0	69.6	105%	94%	100%	94%	99%	102%
					100000000	9.9	10.5	9.2	10.1	15.1	81.1	9.8	10.0	9.8	10.5	15.8	69.7	100%	95%	107%	104%	105%	86%
	seqscan	master	i5	cycle	1000000	140.4	139.7	139.7	142.0	146.8	180.2	140.2	140.1	140.7	142.1	148.8	181.2	100%	100%	101%	100%	101%	101%
					10000000	1311.5	1318.1	1300.5	1319.2	1322.9	1410.2	1316.3	1315.4	1308.8	1305.0	1322.7	1369.1	100%	100%	101%	99%	100%	97%
					50000000	15368.4	15309.8	15378.4	16638.8	16605.8	15636.7	15337.4	15267.8	16905.0	15578.7	15365.1	15426.6	100%	100%	110%	94%	93%	99%
				random	1000000	140.3	140.0	140.4	140.9	147.3	181.1	140.2	141.5	140.1	141.8	146.5	182.5	100%	101%	100%	101%	99%	101%
					10000000	1306.0	1301.5	1331.0	1301.8	1328.2	1357.9	1304.9	1307.6	1304.9	1309.0	1313.0	1368.0	100%	100%	98%	101%	99%	101%
					50000000	16505.4	15317.6	15221.3	15433.6	15364.7	15321.3	15319.7	15341.0	15239.1	15358.4	16152.4	15350.7	93%	100%	100%	100%	105%	100%
				sequential	1000000	142.2	139.5	142.0	142.4	143.5	174.5	139.9	139.8	141.0	141.9	144.0	139.2	98%	100%	99%	100%	100%	80%
					10000000	1304.1	1312.4	1304.9	1300.0	1309.6	1346.0	1301.1	1306.2	1309.6	1310.1	1307.2	1351.1	100%	100%	100%	101%	100%	100%
					50000000	15317.3	15370.2	16425.9	15614.4	15363.8	15472.7	15326.5	15329.5	15504.9	15277.2	15345.3	15387.9	100%	100%	94%	98%	100%	99%
			xeon	cycle	1000000	160.2	162.0	163.2	163.3	166.2	215.4	160.8	163.8	161.3	168.2	165.8	216.8	100%	101%	99%	103%	100%	101%
					10000000	1491.8	1485.3	1468.6	1491.1	1497.5	1550.8	1496.7	1507.1	1578.7	1485.1	1501.3	1546.6	100%	101%	107%	100%	100%	100%
					100000000	15161.0	16103.7	15029.4	14925.9	14977.9	15050.0	14755.0	15228.1	14960.4	14697.3	14894.4	15092.6	97%	95%	100%	98%	99%	100%
				random	1000000	162.0	160.7	162.5	162.0	165.3	191.7	159.6	165.1	159.8	163.2	168.2	211.8	99%	103%	98%	101%	102%	110%
					10000000	1490.0	1462.9	1495.9	1493.2	1490.3	1564.1	1492.0	1488.3	1474.3	1468.0	1493.8	1558.6	100%	102%	99%	98%	100%	100%
					100000000	15033.4	16161.8	16409.2	16286.7	16074.2	15125.0	14964.4	14844.3	14841.0	14862.7	14851.7	15067.4	100%	92%	90%	91%	92%	100%
				sequential	1000000	158.9	162.4	159.2	162.8	166.0	159.7	163.9	161.0	160.3	160.6	164.7	161.1	103%	99%	101%	99%	99%	101%
					10000000	1503.8	1493.5	1493.5	1487.5	1496.5	1514.6	1500.2	1494.7	1498.5	1481.8	1486.6	1514.8	100%	100%	100%	100%	99%	100%
					100000000	16275.4	15018.4	16159.2	16334.7	16021.7	14972.8	14737.0	14960.2	14887.1	14743.9	14714.9	14734.3	91%	100%	92%	90%	92%	98%
		patched	i5	cycle	1000000	139.6	138.9	138.2	140.0	148.3	139.6	139.9	138.6	138.6	142.5	145.5	181.6	100%	100%	100%	102%	98%	130%
					10000000	1310.6	1329.0	1293.9	1290.7	1312.0	1351.6	1292.2	1300.3	1303.0	1308.6	1316.7	1372.3	99%	98%	101%	101%	100%	102%
					50000000	15336.7	15299.8	16570.6	16565.1	15257.4	16134.7	15331.4	15329.7	15539.3	15334.0	15271.7	15261.1	100%	100%	94%	93%	100%	95%
				random	1000000	138.6	138.6	141.3	139.2	145.3	182.9	137.5	141.8	138.1	139.3	147.0	181.5	99%	102%	98%	100%	101%	99%
					10000000	1297.1	1299.0	1287.4	1292.7	1341.0	1357.2	1294.3	1290.0	1354.5	1291.2	1299.5	1353.4	100%	99%	105%	100%	97%	100%
					50000000	15220.9	15984.4	15192.0	18819.6	15217.8	15420.5	15171.1	15261.4	15190.5	15282.3	16502.3	15292.4	100%	95%	100%	81%	108%	99%
				sequential	1000000	138.6	141.1	138.5	140.1	141.4	174.0	138.3	138.0	139.5	140.0	141.7	176.1	100%	98%	101%	100%	100%	101%
					10000000	1292.4	1311.3	1315.1	1295.2	1297.6	1344.7	1287.8	1297.3	1299.8	1300.8	1297.2	1352.5	100%	99%	99%	100%	100%	101%
					50000000	15322.8	16273.3	15277.7	15220.8	15195.9	15359.5	15262.6	15503.3	15491.8	15271.2	15274.0	15336.6	100%	95%	101%	100%	101%	100%
			xeon	cycle	1000000	162.8	162.8	163.7	164.9	169.5	215.3	161.6	162.5	165.6	162.7	170.3	215.0	99%	100%	101%	99%	100%	100%
					10000000	1503.9	1493.4	1530.4	1504.6	1529.1	1573.4	1531.6	1527.7	1488.3	1520.2	1518.6	1564.8	102%	102%	97%	101%	99%	99%

						100000000	14983.0	14868.6	15156.8	15127.3	15144.3	15298.5	14931.4	15041.0	15014.3	14810.1	15120.2	15257.2	100%	101%	99%	98%	100%	100%
					random	1000000	163.1	164.2	161.8	166.5	169.7	213.9	165.5	163.6	164.1	166.0	168.7	214.9	101%	100%	101%	100%	99%	100%
						10000000	1516.2	1498.8	1485.3	1512.5	1501.8	1577.8	1512.1	1501.3	1501.9	1494.5	1530.9	1576.2	100%	100%	101%	99%	102%	100%
						100000000	14903.1	15085.1	15051.7	15188.7	15175.5	15230.5	15060.4	15001.8	15097.6	14952.0	15053.5	15379.2	101%	99%	100%	98%	99%	101%
					sequential	1000000	163.1	163.5	162.7	163.9	169.3	198.8	163.3	163.6	161.6	161.8	167.2	201.7	100%	100%	99%	99%	99%	101%
						10000000	1480.7	1503.6	1484.5	1510.1	1508.7	1544.1	1496.5	1489.8	1537.2	1492.5	1507.5	1549.4	101%	99%	104%	99%	100%	100%
						100000000	14891.0	15049.8	15197.6	14912.1	14907.9	14939.9	15242.5	14958.8	15010.7	15020.6	14821.2	15124.6	102%	99%	99%	101%	99%	101%
	btree-sort	bitmapscan	master	i5	cycle	1000000	99.3	91.8	100.2	80.9	122.8	248.9	104.2	92.4	95.9	93.9	114.1	228.9	105%	101%	96%	116%	93%	92%
						10000000	1563.7	1259.2	1352.6	1528.7	1295.4	1926.3	1655.9	1532.6	1275.0	1687.5	1861.2	2361.0	106%	122%	94%	110%	144%	123%
						50000000	14387.8	13627.0	11144.7	14730.8	20761.6	22762.5	15119.0	17880.6	13809.2	15514.8	15380.8	22998.6	105%	131%	124%	105%	74%	101%
					random	1000000	134.3	128.5	120.6	121.2	152.4	244.5	129.6	138.2	135.3	130.6	156.6	221.2	96%	108%	112%	108%	103%	90%
						10000000	2444.5	2484.2	2382.7	2436.4	2401.1	2638.0	2601.9	2518.5	2470.8	2636.0	2596.1	2921.2	106%	101%	104%	108%	108%	111%
						50000000	18549.6	18429.1	17737.3	18388.5	18038.0	18294.3	34929.1	34570.1	34785.8	34356.9	34839.1	34534.1	188%	188%	196%	187%	193%	189%
					sequential	1000000	81.6	99.5	79.4	93.5	78.4	119.4	89.3	93.8	91.7	79.5	110.2	138.9	109%	94%	116%	85%	141%	116%
						10000000	1412.9	1321.0	1303.1	1555.7	1595.1	1322.8	1474.5	1621.3	1315.1	1387.2	1388.6	1415.4	104%	123%	101%	89%	87%	107%
						50000000	13951.9	11072.5	12711.2	13622.1	14984.6	11204.5	16726.2	16662.3	18088.0	14847.7	14054.8	3403.2	120%	150%	142%	109%	94%	30%
				xeon	cycle	1000000	90.8	91.7	100.8	132.0	168.3	274.2	89.3	93.8	98.4	125.4	170.5	242.4	98%	102%	98%	95%	101%	88%
						10000000	2023.1	1916.0	2004.2	1745.3	2071.1	2501.6	2166.2	1829.8	2196.0	2152.1	1985.4	2884.3	107%	96%	110%	123%	96%	115%
						100000000	15199.6	13101.1	14504.1	18636.7	17219.9	15707.7	14521.3	17742.0	16848.3	19390.1	19296.4	16335.8	96%	135%	116%	104%	112%	104%
					random	1000000	169.3	151.9	153.6	182.5	175.9	228.9	201.3	145.1	180.6	157.6	200.7	287.0	119%	96%	118%	86%	114%	125%
						10000000	2849.4	2928.8	2816.1	2827.9	2714.0	3016.4	3231.3	3159.0	3245.4	3136.9	3014.0	3427.2	113%	108%	115%	111%	111%	114%
						100000000	31677.3	30086.4	30596.0	28687.7	28130.0	30254.0	32789.4	33125.4	31337.1	31828.6	31660.6	31486.5	104%	110%	102%	111%	113%	104%
					sequential	1000000	99.7	93.7	77.0	107.4	94.1	146.5	118.0	127.6	114.0	124.7	107.9	130.0	118%	136%	148%	116%	115%	89%
						10000000	1465.6	1740.3	1894.5	1975.2	1924.0	1888.2	2112.8	2087.3	1788.1	1651.9	2002.7	2061.1	144%	120%	94%	84%	104%	109%
						100000000	18690.0	16826.8	17162.3	14625.6	16619.5	14039.2	19264.6	16876.2	13702.9	19194.7	14888.6	18150.4	103%	100%	80%	131%	90%	129%
			patched	i5	cycle	1000000	104.2	78.8	81.2	106.7	111.1	216.0	109.9	88.3	103.2	109.0	124.0	241.1	105%	112%	127%	102%	112%	112%
						10000000	1574.2	1561.0	1455.6	1522.7	1805.0	2065.2	1702.1	1303.6	1581.8	1812.2	1938.6	2388.4	108%	84%	109%	119%	107%	116%
						50000000	11375.9	9169.0	9336.1	13994.0	19168.9	23938.4	12417.3	12783.1	16281.9	14035.3	12451.2	22145.3	109%	139%	174%	100%	65%	93%
					random	1000000	122.9	117.3	132.0	135.7	145.8	244.8	137.5	133.9	122.5	132.7	149.0	218.1	112%	114%	93%	98%	102%	89%
						10000000	2532.2	2242.8	2393.5	2277.2	2280.0	2476.3	2612.7	2548.1	2682.7	2495.7	2575.3	2939.9	103%	114%	112%	110%	113%	119%
						50000000	17891.8	18189.0	17531.1	18101.3	17515.4	17996.9	26639.4	25395.5	25359.9	25454.8	25411.6	25739.8	149%	140%	145%	141%	145%	143%
					sequential	1000000	79.6	75.2	83.8	92.4	96.1	102.0	94.6	84.1	74.7	90.7	107.7	104.7	119%	112%	89%	98%	112%	103%
						10000000	1520.5	1213.3	1271.0	1611.2	1212.8	1680.0	1617.1	1336.4	1283.6	1355.3	1374.7	1419.2	106%	110%	101%	84%	113%	84%
						50000000	10938.5	11957.2	11668.5	8438.6	12285.0	8963.1	14835.6	10782.2	11453.9	12229.5	17052.7	15522.5	136%	90%	98%	145%	139%	173%
				xeon	cycle	1000000	130.8	128.6	110.5	124.7	91.9	231.7	143.8	136.6	104.1	137.3	141.9	288.7	110%	106%	94%	110%	154%	125%
						10000000	1594.5	1636.9	1983.2	1724.3	1963.8	2248.9	1895.6	2084.5	1941.3	1708.8	2119.1	2488.7	119%	127%	98%	99%	108%	111%
						100000000	17553.7	13834.4	18023.3	17774.1	19628.2	16076.2	16089.9	15804.5	13640.3	17014.5	17465.0	16887.2	92%	114%	76%	96%	89%	105%
					random	1000000	189.2	140.5	155.8	181.4	164.7	252.5	173.2	162.3	185.4	158.8	198.7	263.4	91%	116%	119%	88%	121%	104%
						10000000	2915.1	2833.4	2891.5	2706.2	2884.7	2938.1	3208.3	3319.4	3067.4	3166.7	3153.0	3313.2	110%	117%	106%	117%	109%	113%
						100000000	32046.6	30177.5	29813.7	30780.1	29119.8	29878.9	32470.1	31773.2	32264.0	31553.1	32714.7	32416.0	101%	105%	108%	103%	112%	108%
					sequential	1000000	96.4	99.8	107.5	96.9	112.2	146.4	132.6	97.6	127.1	108.1	92.0	149.2	138%	98%	118%	112%	82%	102%
						10000000	1999.3	1776.1	1910.5	1906.3	1930.9	1779.1	1741.1	1611.2	2114.4	1715.4	1866.7	1988.8	87%	91%	111%	90%	97%	112%
						100000000	15184.4	16403.2	16844.9	17044.1	15960.5	13452.2	16997.2	14263.2	17472.8	18241.7	17190.6	15852.2	112%	87%	104%	107%	108%	118%
		indexscan	master	i5	cycle	1000000	8.0	8.1	8.3	9.6	20.7	84.7	7.9	8.1	8.2	9.5	20.6	84.5	99%	101%	98%	99%	99%	100%
						10000000	8.0	8.2	8.3	9.6	20.8	126.1	8.1	8.0	8.2	9.4	21.5	125.4	101%	98%	99%	98%	104%	99%
						50000000	8.1	8.1	8.3	9.7	21.9	125.7	8.1	8.0	8.3	9.5	21.5	125.9	100%	99%	100%	98%	98%	100%
					random	1000000	8.0	7.9	8.2	9.6	19.3	84.0	7.9	7.9	8.2	9.4	19.4	82.7	99%	100%	100%	98%	100%	98%
						10000000	8.1	8.3	8.4	9.4	21.8	115.3	8.0	8.1	8.2	9.7	20.3	114.5	99%	98%	98%	103%	93%	99%
						50000000	8.2	8.1	8.3	9.9	22.1	123.8	8.1	8.2	8.6	9.6	22.1	124.4	99%	102%	103%	97%	100%	100%
					sequential	1000000	8.1	8.0	8.1	8.6	13.4	56.4	8.0	8.1	8.1	9.0	12.9	55.7	98%	101%	99%	104%	97%	99%
						10000000	8.0	8.1	8.1	8.6	13.1	57.7	8.0	8.0	8.1	8.6	13.1	56.2	100%	98%	100%	100%	100%	98%
						50000000	8.3	8.0	8.2	9.1	13.4	57.5	8.1	8.5	8.2	8.9	13.6	57.6	98%	106%	100%	98%	102%	100%

				xeon	cycle	1000000	9.5	9.7	9.5	11.1	22.6	98.2	9.6	9.5	9.4	10.9	23.1	99.2	101%	97%	99%	98%	102%	101%
						10000000	9.3	9.0	9.5	10.3	23.9	155.9	9.3	9.3	9.4	10.2	23.9	155.8	100%	103%	99%	99%	100%	100%
						100000000	9.2	9.1	9.5	10.7	23.8	162.4	9.3	9.2	9.4	10.6	24.1	161.6	101%	102%	100%	99%	101%	100%
					random	1000000	9.5	9.5	9.6	10.7	23.3	102.6	9.1	9.4	9.3	10.7	22.9	103.0	96%	99%	96%	100%	98%	100%
						10000000	9.0	9.2	9.4	10.4	23.3	133.6	9.2	9.3	9.3	10.6	23.3	146.3	102%	101%	99%	102%	100%	109%
						100000000	9.3	9.5	9.7	10.7	24.7	161.7	9.2	9.1	9.6	10.6	24.4	143.4	100%	95%	99%	100%	99%	89%
					sequential	1000000	9.8	9.5	9.5	10.1	15.5	67.8	9.4	9.4	9.5	9.9	15.5	67.8	96%	99%	100%	98%	100%	100%
						10000000	9.2	9.1	9.2	9.6	15.1	68.3	9.5	9.1	9.4	9.6	14.9	67.3	104%	100%	102%	100%	99%	99%
						100000000	9.3	9.4	9.3	9.8	15.2	68.1	9.2	9.3	9.3	9.8	14.9	65.9	99%	99%	100%	100%	98%	97%
			patched	i5	cycle	1000000	8.1	8.0	8.2	9.5	20.3	85.4	8.0	8.0	8.1	9.7	21.9	89.4	99%	100%	99%	102%	108%	105%
						10000000	7.9	8.1	8.1	9.5	21.1	126.2	8.0	8.0	8.1	9.7	22.2	140.1	100%	100%	100%	102%	105%	111%
						50000000	8.2	8.1	8.5	9.5	22.2	129.2	8.0	8.1	8.4	9.9	23.2	140.5	98%	99%	100%	105%	105%	109%
					random	1000000	8.0	8.0	8.1	9.6	19.5	82.9	8.0	8.0	8.2	9.6	21.1	88.2	100%	100%	101%	100%	108%	106%
						10000000	7.9	8.0	8.3	9.7	20.6	116.0	8.1	8.1	8.2	9.6	22.9	131.3	102%	101%	99%	99%	111%	113%
						50000000	8.4	8.2	8.3	9.6	21.1	125.5	8.2	8.2	8.3	9.9	23.4	138.1	98%	100%	100%	103%	111%	110%
					sequential	1000000	8.0	8.0	8.1	8.8	13.3	56.4	8.0	8.0	8.0	8.6	13.4	60.2	100%	100%	100%	98%	100%	107%
						10000000	8.0	8.0	8.1	8.6	13.4	56.9	8.0	8.1	8.1	8.7	15.2	60.2	100%	101%	99%	100%	113%	106%
						50000000	8.2	8.3	8.0	8.7	13.4	58.0	8.1	8.2	8.1	9.0	15.0	61.1	99%	98%	101%	103%	112%	105%
				xeon	cycle	1000000	9.5	9.3	9.8	10.7	23.4	99.0	9.4	9.3	10.0	11.1	24.5	109.2	99%	100%	102%	105%	105%	110%
						10000000	9.3	9.0	9.3	10.7	24.1	156.4	9.3	9.2	9.5	10.7	24.6	171.5	100%	103%	102%	100%	102%	110%
						100000000	9.2	9.1	9.4	10.5	23.7	162.0	9.4	9.4	9.3	10.8	24.7	162.0	103%	103%	98%	103%	105%	100%
					random	1000000	9.5	9.6	9.2	10.8	23.8	101.8	9.3	9.3	9.5	10.8	24.2	105.8	98%	97%	103%	100%	102%	104%
						10000000	9.0	9.2	9.3	10.3	23.5	145.9	9.3	9.1	9.1	10.9	24.8	161.8	104%	100%	98%	106%	105%	111%
						100000000	9.1	9.1	9.3	10.8	25.7	157.3	9.2	9.3	9.3	10.7	24.8	171.4	101%	103%	100%	99%	97%	109%
					sequential	1000000	9.8	9.6	9.5	10.5	15.1	67.6	9.6	9.4	9.5	10.4	15.5	69.6	98%	98%	101%	99%	103%	103%
						10000000	9.0	9.3	9.3	10.0	14.9	68.4	9.2	9.2	9.2	9.9	15.9	70.4	102%	99%	99%	99%	107%	103%
						100000000	9.1	9.4	9.4	10.1	15.4	65.1	9.6	9.4	9.3	10.0	15.4	71.0	105%	100%	99%	100%	100%	109%
	seqscan		master	i5	cycle	1000000	175.3	173.0	180.4	183.0	196.9	298.2	179.0	175.0	176.9	178.5	197.2	306.0	102%	101%	98%	98%	100%	103%
						10000000	1677.7	1652.2	1637.7	1664.8	1718.0	1880.1	1701.4	1640.6	1648.6	1709.5	1705.2	1918.1	101%	99%	101%	103%	99%	102%
						50000000	15695.4	15679.9	15547.2	15728.3	15861.5	15844.0	15700.8	16754.4	15632.7	15697.7	15664.1	16015.2	100%	107%	101%	100%	99%	101%
					random	1000000	182.4	182.0	179.9	182.4	196.4	304.5	179.7	181.1	176.9	187.0	198.5	307.3	99%	99%	98%	103%	101%	101%
						10000000	1706.8	1712.8	1711.5	1686.0	1741.8	1919.8	1716.1	1661.7	1697.4	1694.0	1700.2	1926.6	101%	97%	99%	100%	98%	100%
						50000000	15611.8	18237.6	15649.9	15690.6	15890.0	15970.5	15662.0	15743.9	15687.5	15670.3	15782.2	15806.0	100%	86%	100%	100%	99%	99%
					sequential	1000000	179.8	177.1	173.8	183.2	185.1	225.4	180.3	172.7	178.7	175.3	181.9	228.9	100%	97%	103%	96%	98%	102%
						10000000	1677.4	1675.4	1640.0	1640.5	1639.9	1851.7	1673.8	1690.2	1705.1	1699.3	1639.1	1724.6	100%	101%	104%	104%	100%	93%
						50000000	15703.2	15666.1	15672.6	16128.5	16009.2	15762.1	15630.7	15636.7	15608.7	15707.1	15694.9	15726.7	100%	100%	100%	97%	98%	100%
				xeon	cycle	1000000	198.7	204.4	200.3	210.2	223.8	334.1	196.9	197.7	204.3	203.8	224.3	331.0	99%	97%	102%	97%	100%	99%
						10000000	1938.5	1891.5	1887.3	1836.4	1912.0	2107.7	1900.7	1932.3	1873.9	1863.6	1928.1	2123.8	98%	102%	99%	101%	101%	101%
						100000000	18854.5	18470.0	18747.1	19039.0	19290.3	19748.0	18674.8	18984.4	18722.4	18606.6	19172.2	19120.1	99%	103%	100%	98%	99%	97%
					random	1000000	201.8	202.2	202.9	209.0	222.0	333.3	202.9	199.3	202.9	207.2	223.7	346.3	101%	99%	100%	99%	101%	104%
						10000000	1896.8	1921.0	1905.1	1918.5	1915.0	2147.6	1885.9	1901.9	1920.8	1915.9	1948.6	2187.4	99%	99%	101%	100%	102%	102%
						100000000	19105.0	19207.2	19097.4	18697.3	18906.7	19817.9	18836.3	19120.7	19215.9	19375.8	19278.0	19811.9	99%	100%	101%	104%	102%	100%
					sequential	1000000	198.2	203.6	203.2	201.6	210.5	252.5	195.9	201.8	205.6	206.6	210.8	254.1	99%	99%	101%	102%	100%	101%
						10000000	1849.6	1862.9	1928.5	1942.5	1869.6	1918.1	1878.5	1889.0	1919.9	1886.2	1957.0	1920.6	102%	101%	100%	97%	105%	100%
						100000000	20453.2	19280.7	18514.0	18680.9	19086.5	19740.1	19057.1	19177.6	19916.1	18476.6	18900.8	18732.1	93%	99%	108%	99%	99%	95%
			patched	i5	cycle	1000000	180.3	178.9	181.0	177.8	200.5	307.8	182.5	180.7	184.1	182.2	195.6	296.7	101%	101%	102%	102%	98%	96%
						10000000	1672.8	1678.4	1672.8	1700.9	1731.4	1935.1	1685.7	1695.7	1664.3	1662.7	1695.6	1941.7	101%	101%	99%	98%	98%	100%
						50000000	15686.3	16463.9	15684.8	15743.6	15657.2	15807.0	15619.1	15743.9	16260.6	15654.5	15791.9	15765.7	100%	96%	104%	99%	101%	100%
					random	1000000	179.1	181.1	180.1	184.4	203.1	310.3	177.4	181.1	178.2	180.5	199.9	302.4	99%	100%	99%	98%	98%	97%
						10000000	1707.9	1681.4	1747.1	1705.3	1777.1	1922.1	1766.2	1698.6	1723.1	1693.4	1741.0	1948.9	103%	101%	99%	99%	98%	101%
						50000000	15782.5	16790.6	15698.4	15649.9	15698.3	15917.7	15744.0	15761.0	15755.9	15691.1	16378.9	15921.2	100%	94%	100%	100%	104%	100%
					sequential	1000000	177.4	175.0	179.6	180.7	184.0	230.9	174.3	179.0	182.3	177.9	187.3	227.4	98%	102%	102%	98%	102%	98%

						10000000	1731.7	1688.0	1699.5	1669.1	1682.7	1753.0	1721.5	1701.7	1722.2	1728.2	1718.7	1754.3	99%	101%	101%	104%	102%	100%
						50000000	15634.2	15606.0	16810.2	15697.8	15633.5	15754.7	15586.4	15710.2	15586.9	16987.7	15749.5	16861.9	100%	101%	93%	108%	101%	107%
				xeon	cycle	1000000	205.2	208.6	208.7	210.1	225.6	335.2	207.9	208.4	210.3	206.4	227.2	337.7	101%	100%	101%	98%	101%	101%
						10000000	1968.0	1903.4	1972.9	1886.6	2017.0	2233.0	1903.1	1886.3	1960.3	1954.1	1943.3	2183.0	97%	99%	99%	104%	96%	98%
						100000000	19282.2	18958.9	19158.8	19059.8	19574.7	19542.8	18832.6	19405.6	18898.8	18899.8	19601.5	19650.1	98%	102%	99%	99%	100%	101%
					random	1000000	208.2	208.0	206.7	212.1	232.0	336.9	207.1	210.8	208.8	208.8	227.3	342.9	99%	101%	101%	98%	98%	102%
						10000000	1966.8	1951.5	1964.4	1963.2	1977.9	2239.2	1962.0	1974.1	1949.6	1976.0	1999.8	2216.2	100%	101%	99%	101%	101%	99%
						100000000	19531.9	19648.3	19570.6	19599.0	19513.1	20198.8	19726.7	19180.9	19641.8	19759.6	19655.8	20155.6	101%	98%	100%	101%	101%	100%
					sequential	1000000	209.3	205.4	207.1	204.4	215.4	266.5	203.6	203.2	201.7	202.9	207.3	263.0	97%	99%	97%	99%	96%	99%
						10000000	1966.1	1974.5	1898.2	1901.9	1925.4	1974.4	1917.1	1968.8	1881.1	1952.1	1948.5	1984.0	98%	100%	99%	103%	101%	100%
						100000000	19227.9	19409.8	19202.0	19476.5	19700.9	19613.0	19368.2	19511.8	19528.6	18945.0	19054.3	18954.9	101%	101%	102%	97%	97%	97%
	hash	bitmapscan	master	i5	cycle	1000000	7.8	7.7	8.2	9.6	24.0	102.9	7.9	8.0	8.1	9.9	25.3	107.8	101%	104%	99%	103%	105%	105%
						10000000	7.9	7.9	8.2	9.6	23.7	174.6	7.8	8.1	8.1	9.8	24.7	182.2	98%	102%	99%	101%	104%	104%
						50000000	8.0	7.9	8.2	10.1	24.1	181.5	8.1	8.0	8.1	9.8	25.0	184.6	101%	102%	99%	97%	103%	102%
					random	1000000	7.9	7.9	8.1	9.8	22.6	100.1	8.0	8.0	8.1	9.7	23.8	108.3	101%	101%	100%	99%	105%	108%
						10000000	7.8	7.8	8.1	9.7	23.9	158.8	7.8	7.9	8.2	9.9	24.9	170.6	100%	101%	100%	102%	104%	107%
						50000000	8.0	8.1	8.2	9.8	24.1	169.6	7.9	8.1	8.1	10.5	25.1	181.8	99%	100%	99%	108%	104%	107%
					sequential	1000000	7.9	7.7	7.9	8.5	13.2	57.9	7.8	7.9	8.1	8.5	13.3	58.6	98%	102%	102%	100%	101%	101%
						10000000	7.9	7.8	8.0	8.5	13.2	57.8	7.9	7.8	8.1	8.3	13.2	58.6	100%	99%	101%	98%	100%	101%
						50000000	8.0	8.1	8.0	8.5	13.2	58.1	7.9	8.1	8.0	8.9	13.3	58.9	99%	101%	100%	104%	101%	101%
				xeon	cycle	1000000	10.1	10.4	9.7	11.1	27.3	128.9	9.4	10.1	10.3	10.9	28.2	131.1	93%	97%	105%	98%	103%	102%
						10000000	10.1	9.7	9.5	11.6	28.3	215.4	10.4	9.7	9.5	12.9	28.2	235.5	104%	100%	100%	111%	100%	109%
						100000000	10.6	9.1	10.5	11.9	28.4	225.9	10.3	9.4	10.2	11.3	28.5	205.5	97%	103%	97%	96%	100%	91%
					random	1000000	10.4	9.8	10.1	11.0	26.5	113.4	10.2	10.2	10.6	11.8	26.9	128.7	98%	104%	105%	107%	101%	113%
						10000000	9.5	9.7	9.1	11.0	27.7	192.5	8.8	9.9	9.3	12.1	27.7	220.1	93%	101%	102%	110%	100%	114%
						100000000	10.0	9.7	10.3	12.3	27.7	206.3	10.4	9.8	10.1	11.4	28.0	219.1	104%	100%	98%	93%	101%	106%
					sequential	1000000	10.0	9.6	10.0	10.5	15.5	83.2	9.4	9.3	9.9	9.7	16.5	70.0	94%	97%	99%	93%	106%	84%
						10000000	9.8	9.9	9.7	11.1	16.4	70.0	10.1	10.3	9.9	10.0	15.8	70.3	103%	104%	102%	90%	96%	100%
						100000000	10.2	10.1	10.1	10.4	15.5	71.5	10.0	10.3	10.4	10.9	15.6	71.2	98%	102%	103%	105%	100%	100%
			patched	i5	cycle	1000000	7.9	7.9	8.1	9.8	24.1	102.4	7.9	7.9	8.1	9.8	25.0	110.2	99%	100%	100%	101%	104%	108%
						10000000	7.7	7.9	8.1	9.7	25.0	180.8	7.9	7.8	8.0	9.8	25.0	182.2	102%	99%	98%	100%	100%	101%
						50000000	8.0	8.4	8.1	9.7	24.1	172.3	7.9	7.8	8.1	9.9	25.0	183.0	99%	93%	99%	102%	104%	106%
					random	1000000	7.8	7.8	8.0	9.7	22.4	100.7	7.8	7.8	8.0	9.8	24.0	107.1	100%	99%	100%	101%	107%	106%
						10000000	7.9	8.0	8.2	9.6	24.6	158.0	7.9	7.7	8.1	9.7	24.9	169.4	99%	97%	99%	102%	101%	107%
						50000000	7.8	7.9	8.1	9.8	24.3	168.4	7.9	7.9	8.1	10.7	24.9	180.1	101%	100%	100%	110%	103%	107%
					sequential	1000000	7.8	7.8	7.9	8.4	13.0	57.6	7.9	7.9	8.0	8.4	13.1	58.5	101%	100%	101%	100%	101%	101%
						10000000	7.9	7.8	8.0	8.3	13.0	57.6	7.8	7.8	7.8	8.4	13.3	58.4	99%	101%	97%	101%	102%	101%
						50000000	7.9	7.9	7.9	8.5	13.1	58.1	8.0	7.9	8.1	9.5	13.2	58.7	101%	100%	102%	112%	101%	101%
				xeon	cycle	1000000	9.8	9.8	9.8	10.8	27.0	128.5	9.9	10.2	10.0	12.7	28.1	136.3	101%	104%	102%	117%	104%	106%
						10000000	10.3	9.9	9.6	12.0	28.8	206.5	9.4	10.1	9.8	12.3	28.5	233.1	92%	102%	102%	102%	99%	113%
						100000000	10.2	9.3	10.6	11.5	28.7	223.4	10.3	9.7	10.1	11.5	28.2	221.7	101%	105%	96%	100%	98%	99%
					random	1000000	10.4	8.9	10.7	12.8	26.7	122.6	10.2	9.9	10.3	12.4	26.9	131.2	98%	111%	96%	97%	101%	107%
						10000000	9.5	9.6	9.4	12.5	27.3	193.0	9.6	9.4	10.0	12.3	28.4	190.6	101%	99%	106%	98%	104%	99%
						100000000	10.2	9.9	9.9	11.2	29.2	208.5	9.3	9.8	10.2	12.7	28.4	221.0	91%	99%	103%	113%	97%	106%
					sequential	1000000	9.8	8.8	9.5	9.9	16.1	66.8	9.6	9.7	9.9	10.4	15.9	69.8	98%	110%	105%	105%	98%	104%
						10000000	9.7	9.5	10.0	10.7	16.7	68.9	9.8	9.6	10.4	11.1	16.5	68.3	101%	101%	104%	104%	99%	99%
						100000000	9.0	10.2	10.5	10.1	15.8	71.0	9.7	10.2	10.4	10.9	15.7	70.6	108%	100%	99%	108%	100%	99%
		indexscan	master	i5	cycle	1000000	8.0	8.0	8.1	9.5	20.4	91.3	7.8	7.8	8.0	9.4	20.5	89.4	98%	98%	99%	100%	100%	98%
						10000000	7.8	8.0	8.0	9.4	21.0	127.6	7.9	7.9	8.2	9.3	20.9	128.7	100%	98%	102%	99%	100%	101%
						50000000	8.0	8.0	8.2	9.5	20.8	128.4	8.0	8.0	8.4	9.5	20.7	130.9	100%	100%	102%	100%	99%	102%
					random	1000000	7.9	7.9	7.9	9.5	19.9	86.5	7.9	8.0	8.2	9.4	20.1	86.3	100%	101%	103%	99%	101%	100%
						10000000	7.8	7.8	8.1	9.4	20.9	116.1	7.8	7.9	8.2	9.4	20.7	117.0	100%	101%	101%	100%	99%	101%

						50000000	8.0	7.9	8.2	9.6	20.8	125.0	7.9	8.0	8.2	9.7	20.9	126.0	99%	100%	100%	101%	100%	101%
					sequential	1000000	7.8	7.8	8.0	8.7	13.1	57.8	7.9	8.0	7.9	8.5	13.1	58.1	102%	102%	99%	98%	100%	101%
						10000000	7.9	7.8	8.0	8.6	13.3	59.5	7.8	7.8	8.0	8.5	13.5	59.1	99%	101%	100%	99%	102%	99%
						50000000	7.9	7.9	8.1	8.7	13.5	59.4	8.0	7.9	8.1	9.5	13.6	59.8	102%	100%	101%	108%	101%	101%
				xeon	cycle	1000000	9.3	9.9	9.5	10.5	23.7	107.9	9.6	10.2	10.0	11.0	23.4	106.6	103%	104%	105%	105%	99%	99%
						10000000	9.7	9.3	9.3	10.9	23.8	160.1	10.3	9.5	9.5	12.3	24.3	155.9	106%	102%	102%	113%	102%	97%
						100000000	10.3	9.0	10.0	11.0	23.5	167.3	10.3	9.0	10.0	10.9	24.0	162.7	100%	100%	99%	99%	102%	97%
					random	1000000	9.9	9.5	9.6	10.6	23.1	103.4	10.4	10.0	10.8	11.4	23.4	104.6	104%	105%	112%	108%	101%	101%
						10000000	8.9	9.7	9.3	10.1	23.3	152.5	9.1	9.5	9.3	12.2	23.6	148.7	102%	98%	99%	121%	101%	97%
						100000000	9.8	10.1	10.0	11.5	23.4	157.4	10.2	9.8	10.1	11.4	24.4	176.4	104%	96%	101%	99%	105%	112%
					sequential	1000000	9.4	9.2	9.5	10.4	15.4	69.6	9.7	9.5	9.5	9.6	15.7	69.7	104%	104%	100%	92%	102%	100%
						10000000	10.4	9.9	9.7	10.2	16.2	71.3	10.1	10.2	9.8	10.1	16.2	70.8	97%	103%	101%	99%	100%	99%
						100000000	9.9	9.9	9.9	10.9	15.2	71.0	9.8	10.4	10.6	10.8	15.5	70.6	100%	105%	106%	99%	102%	100%
			patched	i5	cycle	1000000	7.7	7.9	8.1	9.3	20.4	90.4	7.9	7.9	7.9	9.5	21.7	94.8	102%	100%	98%	103%	106%	105%
						10000000	7.9	7.8	8.0	9.2	21.3	125.9	7.9	7.8	8.0	9.5	21.9	136.9	100%	100%	100%	102%	103%	109%
						50000000	7.9	7.9	8.0	9.3	20.5	127.7	8.1	7.8	8.1	9.7	21.6	137.4	103%	99%	102%	104%	106%	108%
					random	1000000	7.9	7.9	8.0	9.4	19.8	86.5	7.7	7.9	8.1	9.5	20.9	92.0	98%	100%	102%	101%	105%	106%
						10000000	8.1	7.7	8.1	9.3	21.2	115.7	7.8	7.9	8.0	9.6	22.0	129.7	97%	102%	99%	103%	104%	112%
						50000000	7.8	8.0	8.0	9.5	21.7	124.8	8.0	8.1	8.0	9.6	21.7	135.7	103%	101%	100%	101%	100%	109%
					sequential	1000000	7.9	7.8	7.9	8.5	13.1	58.0	7.7	7.7	8.0	8.5	13.8	60.5	98%	98%	101%	100%	105%	104%
						10000000	7.8	7.9	7.9	8.5	13.2	58.4	7.8	7.9	7.9	8.5	13.6	60.5	100%	100%	100%	100%	103%	103%
						50000000	8.0	7.9	8.1	8.6	13.2	59.0	7.9	8.0	8.0	8.5	13.8	60.7	99%	101%	99%	99%	104%	103%
				xeon	cycle	1000000	9.7	10.0	10.1	10.5	23.8	106.6	9.6	10.2	10.2	11.9	24.2	114.7	98%	103%	101%	112%	102%	108%
						10000000	10.1	9.9	9.2	12.0	24.8	164.9	9.6	10.0	9.4	12.1	24.4	174.5	96%	101%	101%	101%	98%	106%
						100000000	10.3	9.0	9.4	11.4	24.4	166.4	9.9	9.7	8.8	11.0	24.6	176.4	96%	108%	94%	97%	101%	106%
					random	1000000	10.2	9.2	10.3	11.9	22.8	105.0	10.2	9.9	10.4	12.1	23.0	108.7	100%	107%	101%	101%	101%	103%
						10000000	9.1	9.8	9.2	11.7	23.5	148.3	9.8	8.8	10.1	12.2	24.3	165.8	107%	90%	109%	105%	103%	112%
						100000000	10.0	9.5	9.6	11.5	25.3	161.4	9.2	9.9	9.8	11.9	24.9	174.2	92%	104%	102%	104%	98%	108%
					sequential	1000000	9.5	9.0	9.7	10.5	16.6	69.0	9.6	9.1	9.7	10.2	15.8	73.2	101%	101%	100%	97%	95%	106%
						10000000	9.9	9.5	9.9	10.7	16.0	70.7	10.1	9.6	9.8	10.6	17.4	70.5	102%	101%	98%	99%	109%	100%
						100000000	8.9	10.5	10.5	9.9	16.1	71.3	9.7	10.4	10.3	11.0	16.1	72.4	110%	100%	98%	112%	100%	102%
	seqscan		master	i5	cycle	1000000	140.1	145.1	140.1	140.6	146.1	181.6	139.6	140.3	140.1	140.8	146.5	185.4	100%	97%	100%	100%	100%	102%
						10000000	1306.1	1324.6	1298.7	1306.3	1314.9	1369.4	1301.7	1328.8	1318.0	1318.5	1321.3	1376.0	100%	100%	101%	101%	100%	100%
						50000000	15315.2	15197.3	15696.4	15576.3	15382.8	15329.0	15350.0	15213.4	15453.3	15310.3	15343.3	15435.1	100%	100%	98%	98%	100%	101%
					random	1000000	140.7	139.9	140.1	141.2	148.9	182.9	140.0	139.9	139.8	140.9	145.8	181.0	99%	100%	100%	100%	98%	99%
						10000000	1303.0	1302.5	1307.2	1303.7	1320.2	1369.5	1304.6	1358.2	1329.3	1309.4	1316.1	1376.2	100%	104%	102%	100%	100%	100%
						50000000	15354.0	15194.1	15292.1	15303.3	15277.8	15546.8	15562.5	15814.3	15272.0	15794.5	15339.6	16066.6	101%	104%	100%	103%	100%	103%
					sequential	1000000	140.1	140.3	139.9	140.6	147.3	174.0	139.4	139.7	140.0	140.1	144.1	174.7	100%	100%	100%	100%	98%	100%
						10000000	1298.4	1309.9	1310.5	1309.2	1322.0	1357.6	1305.6	1309.3	1305.5	1300.7	1337.1	1338.6	101%	100%	100%	99%	101%	99%
						50000000	15355.4	15374.8	15236.6	15376.7	15353.2	16404.2	15305.9	15388.2	16290.5	15380.7	15347.2	15376.6	100%	100%	107%	100%	100%	94%
				xeon	cycle	1000000	162.6	162.5	160.7	162.3	164.8	231.2	159.0	159.4	160.2	166.7	166.9	214.9	98%	98%	100%	103%	101%	93%
						10000000	1491.4	1487.5	1473.0	1488.1	1524.7	1596.3	1495.3	1473.9	1480.9	1490.3	1501.4	1560.9	100%	99%	101%	100%	98%	98%
						100000000	16433.3	16149.0	16103.6	16309.9	14712.3	15090.6	14830.6	14993.8	14908.3	14714.6	15179.3	15067.8	90%	93%	93%	90%	103%	100%
					random	1000000	160.3	171.6	161.4	161.1	163.7	225.3	160.3	160.1	161.5	161.0	167.8	211.1	100%	93%	100%	100%	102%	94%
						10000000	1488.8	1493.8	1466.6	1478.0	1510.6	1565.2	1465.0	1478.0	1467.8	1493.8	1491.3	1605.9	98%	99%	100%	101%	99%	103%
						100000000	16255.4	16274.5	16289.2	16091.5	16437.9	16416.4	14955.3	14894.6	14793.8	14727.2	14998.3	15009.6	92%	92%	91%	92%	91%	91%
					sequential	1000000	161.6	159.6	165.2	159.9	167.6	160.5	159.8	159.0	161.0	162.5	166.5	199.0	99%	100%	98%	102%	99%	124%
						10000000	1491.9	1488.9	1553.4	1485.3	1495.3	1551.4	1480.8	1489.5	1484.6	1492.4	1471.3	1507.1	99%	100%	96%	100%	98%	97%
						100000000	16814.3	16144.8	15076.6	16043.7	16067.9	16202.8	14861.4	14924.7	14961.6	15191.2	15058.4	15092.1	88%	92%	99%	95%	94%	93%
			patched	i5	cycle	1000000	137.5	138.2	139.9	139.1	145.2	183.0	138.2	138.5	138.2	140.9	146.6	181.9	100%	100%	99%	101%	101%	99%
						10000000	1284.3	1304.8	1293.0	1297.4	1303.1	1356.5	1292.2	1298.0	1295.8	1295.6	1306.5	1354.3	101%	99%	100%	100%	100%	100%
						50000000	15320.0	15296.5	15743.7	15168.1	15153.0	15364.3	15126.6	15203.1	15263.8	15222.2	15128.1	15341.7	99%	99%	97%	100%	100%	100%

					random	1000000	138.2	140.7	138.3	139.1	145.3	184.0	137.7	140.0	138.1	140.5	145.4	182.3	100%	99%	100%	101%	100%	99%
						10000000	1307.0	1288.8	1291.7	1286.9	1300.0	1350.0	1296.1	1320.6	1304.9	1292.9	1309.6	1352.8	99%	102%	101%	100%	101%	100%
						50000000	15259.2	15171.8	15266.8	15164.3	15215.9	15329.8	15714.3	15182.0	15282.8	15295.2	15747.5	15205.1	103%	100%	100%	101%	103%	99%
					sequential	1000000	139.9	138.4	139.3	138.5	143.9	174.1	141.9	137.8	138.0	139.2	141.8	138.4	101%	100%	99%	100%	99%	79%
						10000000	1295.9	1292.0	1292.6	1325.4	1302.2	1334.0	1291.0	1311.4	1299.0	1298.6	1298.7	1328.5	100%	101%	100%	98%	100%	100%
						50000000	15305.9	15182.0	16348.8	16090.2	15293.9	15309.7	17207.2	15244.6	15142.5	15993.1	15258.6	15716.3	112%	100%	93%	99%	100%	103%
				xeon	cycle	1000000	163.8	159.2	161.9	162.6	169.1	164.0	163.5	162.3	162.0	164.0	166.9	215.1	100%	102%	100%	101%	99%	131%
						10000000	1515.5	1496.5	1485.0	1507.9	1498.8	1573.4	1509.3	1485.7	1482.7	1532.6	1495.6	1562.2	100%	99%	100%	102%	100%	99%
						100000000	14857.8	14869.4	15037.5	15046.7	14842.9	15266.6	15009.4	15082.7	14912.4	14839.0	14859.5	15259.5	101%	101%	99%	99%	100%	100%
					random	1000000	165.3	163.4	163.2	163.9	167.7	190.0	163.3	162.0	163.1	163.4	168.5	191.6	99%	99%	100%	100%	100%	101%
						10000000	1484.8	1483.2	1493.4	1525.4	1510.2	1565.0	1542.0	1496.7	1491.0	1520.0	1504.5	1576.3	104%	101%	100%	100%	100%	101%
						100000000	15170.1	15105.9	14947.8	15110.6	14891.6	15175.3	14979.2	15233.5	14949.5	15249.9	15151.6	15467.5	99%	101%	100%	101%	102%	102%
					sequential	1000000	163.8	164.7	163.4	161.5	165.4	201.4	161.1	163.0	162.0	163.0	165.6	204.5	98%	99%	99%	101%	100%	102%
						10000000	1478.0	1511.8	1502.8	1480.6	1502.9	1533.4	1482.9	1507.2	1499.3	1515.5	1510.4	1534.3	100%	100%	100%	102%	101%	100%
						100000000	14859.3	15129.5	15146.7	15129.7	14850.3	15059.6	14822.8	15363.0	15111.7	15125.9	15050.1	15248.5	100%	102%	100%	100%	101%	101%
uncached	btree	bitmapscan	master	i5	cycle	1000000	11.4	12.9	25.1	167.8	1562.4	485.5	11.9	12.5	13.2	30.6	205.4	455.0	104%	96%	52%	18%	13%	94%
						10000000	11.3	13.1	26.5	178.0	1564.9	15167.5	11.6	11.8	13.6	32.2	194.8	1692.1	103%	90%	51%	18%	12%	11%
						50000000	12.0	13.7	26.6	173.0	1555.6	15484.8	11.6	12.8	14.9	32.4	183.2	1669.2	96%	94%	56%	19%	12%	11%
					random	1000000	11.6	13.2	27.0	168.6	867.1	490.6	12.0	12.1	14.5	30.3	181.1	731.9	103%	92%	53%	18%	21%	149%
						10000000	11.2	12.7	26.4	167.0	1586.2	7498.7	12.0	12.2	14.2	30.0	178.4	1506.3	106%	97%	54%	18%	11%	20%
						50000000	11.8	14.6	26.1	163.8	1566.2	15951.7	11.8	12.9	16.2	31.4	182.1	1658.3	101%	89%	62%	19%	12%	10%
					sequential	1000000	12.0	11.7	11.9	14.9	24.0	91.0	11.4	11.4	11.9	13.6	26.8	132.9	95%	97%	100%	91%	112%	146%
						10000000	12.0	11.9	12.3	14.2	22.9	88.8	11.7	11.7	12.1	13.9	24.5	129.9	97%	98%	99%	98%	107%	146%
						50000000	11.6	11.6	12.2	14.8	22.2	90.3	11.6	11.7	11.8	13.5	25.6	127.2	100%	101%	97%	91%	115%	141%
				xeon	cycle	1000000	12.7	13.7	23.2	119.3	282.4	379.8	12.9	14.1	15.2	29.9	147.1	498.8	102%	103%	66%	25%	52%	131%
						10000000	13.9	14.4	24.1	123.5	1027.8	2930.8	13.7	13.0	14.9	29.1	141.6	1373.3	99%	90%	62%	24%	14%	47%
						100000000	15.1	15.2	28.1	123.2	1099.8	10216.6	14.2	14.8	17.8	28.9	124.8	1298.4	94%	98%	63%	23%	11%	13%
					random	1000000	12.9	14.6	24.0	121.9	554.1	368.0	12.9	14.9	15.3	28.4	115.0	495.6	100%	102%	64%	23%	21%	135%
						10000000	13.2	13.9	24.3	123.0	1093.3	4903.3	14.9	14.4	14.7	28.5	142.9	1037.6	113%	103%	60%	23%	13%	21%
						100000000	13.9	15.9	27.1	124.5	1107.8	10790.0	12.8	15.5	17.9	30.3	124.7	1258.8	92%	97%	66%	24%	11%	12%
					sequential	1000000	13.2	14.5	12.5	16.1	22.4	92.0	14.1	14.2	12.3	15.5	23.7	106.1	107%	98%	98%	96%	106%	115%
						10000000	12.9	15.0	15.3	14.9	23.3	95.4	12.5	14.7	14.2	14.6	25.7	112.3	97%	98%	93%	98%	110%	118%
						100000000	14.8	13.7	13.6	17.3	24.1	94.5	13.7	15.3	15.1	13.7	25.3	106.5	93%	111%	111%	79%	105%	113%
			patched	i5	cycle	1000000	11.2	12.2	24.9	165.7	1553.3	417.6	11.0	11.1	12.7	27.8	148.3	630.6	99%	91%	51%	17%	10%	151%
						10000000	11.6	12.6	25.6	164.6	1578.3	14801.4	11.3	12.1	13.6	29.0	165.0	1784.9	97%	96%	53%	18%	10%	12%
						50000000	11.4	14.0	30.2	170.3	1531.6	15179.3	11.7	12.8	15.1	30.7	145.3	1306.1	102%	92%	50%	18%	9%	9%
					random	1000000	10.7	13.3	24.9	175.6	873.4	404.2	11.1	11.8	13.8	32.4	131.4	557.8	104%	88%	55%	18%	15%	138%
						10000000	11.4	12.7	26.3	163.4	1589.2	7775.6	11.0	12.1	14.0	25.5	152.7	1144.5	96%	96%	53%	16%	10%	15%
						50000000	11.6	13.7	27.6	174.5	1562.6	16002.4	11.6	13.0	14.4	31.4	157.6	1295.0	101%	95%	52%	18%	10%	8%
					sequential	1000000	11.2	11.2	11.7	13.7	23.0	88.9	11.2	11.1	11.8	14.2	23.7	118.1	100%	99%	101%	104%	103%	133%
						10000000	11.3	11.5	12.6	15.1	20.9	88.2	11.2	11.4	11.7	14.5	22.0	106.8	100%	99%	93%	96%	105%	121%
						50000000	11.7	11.4	12.2	16.2	21.7	86.6	11.4	11.4	11.8	13.6	23.3	108.6	97%	100%	97%	84%	108%	125%
				xeon	cycle	1000000	12.9	15.8	25.0	119.6	285.1	378.2	12.7	13.5	13.7	28.3	146.9	494.1	98%	85%	55%	24%	52%	131%
						10000000	13.6	13.5	23.1	125.0	1024.7	2922.4	13.4	14.5	14.3	28.3	141.2	1380.7	98%	107%	62%	23%	14%	47%
						100000000	13.6	15.6	27.6	125.7	1092.4	10262.1	13.6	14.8	17.3	27.7	123.6	1295.2	100%	95%	63%	22%	11%	13%
					random	1000000	13.4	15.7	25.3	122.5	553.8	363.6	13.1	14.3	15.4	29.1	115.2	512.9	98%	91%	61%	24%	21%	141%
						10000000	14.0	15.0	25.8	125.6	1096.8	4903.5	13.2	14.9	15.7	27.2	139.7	1031.1	95%	99%	61%	22%	13%	21%
						100000000	12.6	16.0	27.2	126.0	1094.2	10735.9	12.1	15.0	18.3	28.9	123.4	1274.8	97%	93%	67%	23%	11%	12%
					sequential	1000000	14.2	14.4	13.5	15.6	22.9	95.4	13.6	14.3	12.4	15.9	25.3	108.2	95%	99%	92%	102%	110%	113%
						10000000	13.9	14.6	13.9	15.7	24.4	95.3	14.0	13.7	14.3	15.5	25.6	106.2	101%	93%	103%	99%	105%	111%
						100000000	14.0	13.7	13.3	15.0	24.0	94.9	14.0	14.0	13.1	14.9	25.6	109.4	100%	102%	99%	100%	107%	115%
		indexscan	master	i5	cycle	1000000	11.5	12.4	25.1	167.0	1555.9	482.8	10.6	12.5	25.5	167.0	1558.4	398.9	92%	101%	102%	100%	100%	83%

						10000000	11.4	13.2	25.5	175.3	1561.2	15159.7	11.4	13.1	25.4	178.0	1586.5	15014.5	100%	99%	100%	102%	102%	99%
						50000000	11.6	13.5	26.8	172.7	1544.5	15946.7	11.5	13.6	28.0	165.1	1538.3	15372.3	99%	101%	105%	96%	100%	96%
					random	1000000	12.2	13.1	24.7	166.2	812.1	471.0	11.5	12.9	25.7	166.1	871.8	420.9	94%	99%	104%	100%	107%	89%
						10000000	11.6	12.7	26.8	159.0	1601.6	7481.6	11.7	12.9	27.7	171.7	1549.3	7453.8	101%	102%	103%	108%	97%	100%
						50000000	11.8	13.2	27.5	163.1	1551.5	15365.1	11.2	13.4	28.5	173.2	1531.6	16155.8	95%	101%	104%	106%	99%	105%
					sequential	1000000	10.9	11.5	11.8	13.7	21.4	89.4	10.9	11.5	11.9	14.8	23.6	95.8	99%	101%	100%	109%	110%	107%
						10000000	11.6	11.4	12.5	14.3	22.1	91.9	11.3	12.0	11.9	14.0	22.9	91.5	98%	105%	95%	98%	104%	100%
						50000000	11.8	11.6	12.0	14.2	23.5	91.3	11.8	11.9	11.7	15.5	21.9	91.8	100%	103%	97%	109%	94%	101%
				xeon	cycle	1000000	12.4	13.2	23.7	118.9	371.0	352.9	12.4	14.9	24.9	122.1	331.5	354.9	100%	113%	105%	103%	89%	101%
						10000000	13.9	14.3	24.0	123.1	1026.5	3001.5	13.2	13.9	23.7	123.5	1034.5	3003.2	95%	97%	99%	100%	101%	100%
						100000000	14.8	15.0	27.8	124.0	1090.3	10191.7	14.1	15.9	27.6	124.0	1098.4	10145.1	95%	106%	99%	100%	101%	100%
					random	1000000	13.0	14.9	23.7	119.4	552.7	347.1	13.2	15.4	22.8	123.5	545.0	348.3	101%	103%	96%	103%	99%	100%
						10000000	13.6	14.3	24.6	122.4	1082.2	4815.7	14.6	14.2	25.1	124.9	1085.0	4828.8	107%	99%	102%	102%	100%	100%
						100000000	13.5	14.9	27.0	123.8	1087.4	10709.1	12.2	16.0	27.8	127.5	1101.2	10744.1	91%	107%	103%	103%	101%	100%
					sequential	1000000	13.0	13.4	13.2	15.8	23.4	92.8	13.5	13.0	12.5	15.7	22.4	98.2	103%	97%	95%	99%	96%	106%
						10000000	13.8	14.4	15.2	14.5	23.9	97.1	13.1	13.4	14.3	14.6	24.3	96.9	95%	93%	94%	101%	102%	100%
						100000000	14.2	13.1	14.0	16.3	24.2	96.1	13.5	14.0	14.4	14.6	24.8	96.1	95%	107%	103%	89%	102%	100%
			patched	i5	cycle	1000000	11.5	12.2	26.2	162.0	1528.9	396.1	10.9	11.7	13.1	26.6	176.9	387.9	95%	96%	50%	16%	12%	98%
						10000000	11.1	12.7	25.6	169.2	1570.8	14909.7	11.2	12.2	13.4	28.1	159.7	1505.9	101%	96%	52%	17%	10%	10%
						50000000	11.5	13.6	27.3	169.1	1524.2	15136.6	11.6	12.9	18.1	28.5	142.3	1251.8	102%	95%	66%	17%	9%	8%
					random	1000000	10.9	12.2	26.9	173.9	863.5	399.1	10.8	11.8	13.5	29.5	191.6	400.1	99%	97%	50%	17%	22%	100%
						10000000	11.1	12.3	25.4	163.5	1563.4	7760.5	11.5	12.2	13.6	25.6	150.0	1591.8	104%	100%	54%	16%	10%	21%
						50000000	11.1	13.1	27.1	170.5	1524.4	15935.6	11.8	13.1	14.7	28.1	162.5	1297.1	106%	101%	54%	17%	11%	8%
					sequential	1000000	11.2	11.3	11.8	13.8	21.2	88.4	11.0	11.0	11.4	15.6	22.0	96.1	98%	98%	97%	113%	104%	109%
						10000000	11.3	11.1	11.9	13.5	21.1	90.8	11.4	11.1	11.6	13.6	21.6	94.2	101%	100%	98%	101%	102%	104%
						50000000	11.6	11.3	12.6	13.1	20.3	88.7	11.2	11.0	13.0	16.4	22.6	92.2	96%	97%	103%	125%	111%	104%
				xeon	cycle	1000000	12.2	14.9	24.7	118.4	391.2	351.4	12.2	13.7	13.7	28.0	128.4	353.4	101%	92%	55%	24%	33%	101%
						10000000	13.3	13.4	23.5	123.5	1029.6	3042.9	13.4	13.7	14.2	27.1	138.5	1189.7	100%	102%	60%	22%	13%	39%
						100000000	13.1	15.5	27.1	124.3	1095.1	10178.8	12.7	15.4	18.0	27.4	120.3	1252.3	97%	99%	66%	22%	11%	12%
					random	1000000	13.5	15.0	24.2	120.9	549.6	351.2	13.3	14.0	15.6	28.5	123.8	355.9	99%	93%	65%	24%	23%	101%
						10000000	14.5	15.3	25.4	120.4	1103.7	4838.1	13.6	14.7	16.2	26.6	137.8	1122.6	93%	96%	64%	22%	12%	23%
						100000000	13.0	15.0	26.6	125.5	1104.1	10722.5	12.7	14.8	17.8	29.8	118.1	1227.4	98%	99%	67%	24%	11%	11%
					sequential	1000000	13.7	14.1	12.8	16.0	22.3	94.2	13.2	14.2	12.6	16.0	24.5	97.3	96%	101%	99%	100%	110%	103%
						10000000	13.5	14.1	14.3	15.9	24.7	97.6	13.5	13.8	14.4	15.7	25.0	99.3	100%	98%	100%	99%	101%	102%
						100000000	13.4	14.5	13.1	14.1	24.2	95.5	13.7	13.7	13.3	15.0	24.7	99.1	102%	95%	101%	106%	102%	104%
		seqscan	master	i5	cycle	1000000	372.9	351.2	359.4	360.5	380.0	425.3	361.9	373.4	349.4	370.8	365.3	374.0	97%	106%	97%	103%	96%	88%
						10000000	3155.3	3134.8	3731.0	3662.5	3337.2	3208.8	3149.4	3165.1	3196.8	3462.8	3183.5	3147.8	100%	101%	86%	95%	95%	98%
						50000000	15286.0	15297.0	15879.3	16094.2	15245.5	15289.9	15223.5	15260.9	16067.0	16413.3	15300.5	15359.3	100%	100%	101%	102%	100%	100%
					random	1000000	340.2	345.9	351.8	346.3	427.5	458.6	394.1	374.0	343.2	366.3	380.4	409.5	116%	108%	98%	106%	89%	89%
						10000000	3216.5	3415.6	3442.2	3177.6	3232.3	3155.3	3450.9	3337.6	3475.9	3231.5	3176.3	3185.0	107%	98%	101%	102%	98%	101%
						50000000	15536.1	15631.7	15535.9	15542.5	16756.3	15333.8	15244.2	15208.2	15210.0	15277.5	15645.4	15366.9	98%	97%	98%	98%	93%	100%
					sequential	1000000	396.7	340.7	343.5	376.5	412.2	413.9	381.1	389.9	350.6	389.2	357.3	364.9	96%	114%	102%	103%	87%	88%
						10000000	3330.1	3191.8	3180.0	3307.1	3323.6	3207.0	3174.9	3165.6	3174.2	3357.9	3083.9	3215.3	95%	99%	100%	102%	93%	100%
						50000000	15257.5	15291.9	17000.9	17277.4	15246.0	15357.2	16035.3	15283.9	16462.4	15248.9	15263.7	16131.8	105%	100%	97%	88%	100%	105%
				xeon	cycle	1000000	249.2	251.3	250.3	251.4	252.7	283.8	247.3	251.0	250.5	252.0	249.6	284.2	99%	100%	100%	100%	99%	100%
						10000000	2204.0	2206.2	2193.0	2208.8	2203.5	2249.2	2204.1	2205.6	2201.8	2211.0	2203.4	2249.9	100%	100%	100%	100%	100%	100%
						100000000	21733.1	21842.0	21728.0	21534.5	21666.8	21825.8	21584.9	21550.8	21566.8	21493.5	21573.1	21813.5	99%	99%	99%	100%	100%	100%
					random	1000000	249.4	249.8	249.8	250.2	232.9	267.2	251.3	253.0	245.4	247.0	231.9	283.3	101%	101%	98%	99%	100%	106%
						10000000	2176.8	2192.3	2139.1	2209.6	2200.3	2239.9	2212.2	2198.4	2184.2	2208.6	2203.2	2240.7	102%	100%	102%	100%	100%	100%
						100000000	21511.4	21514.0	21815.1	21468.6	21514.6	21582.7	21582.1	21704.6	21824.4	21456.9	21547.6	21535.9	100%	101%	100%	100%	100%	100%
					sequential	1000000	248.9	251.5	249.9	253.9	196.1	250.6	240.8	252.3	246.5	252.4	255.2	245.8	97%	100%	99%	99%	130%	98%
						10000000	2196.6	2219.0	2214.9	2219.0	2201.8	2361.9	2199.7	2220.1	2215.4	2217.1	2206.7	2278.1	100%	100%	100%	100%	100%	96%

					100000000	21594.2	21836.1	21519.9	21714.2	21472.0	21757.6	21510.4	21847.9	21563.9	21686.2	21418.1	21782.2	100%	100%	100%	100%	100%	100%
			patched	i5	cycle	1000000	434.4	411.2	368.5	350.5	362.3	352.2	393.7	392.8	356.9	371.0	369.2	390.3	91%	96%	97%	106%	102%	111%
						10000000	3109.5	3165.7	3150.5	3352.6	3574.5	3190.3	3432.2	3112.9	3108.4	3149.5	3436.0	3427.6	110%	98%	99%	94%	96%	107%
						50000000	16211.6	15182.1	18927.6	15247.8	15199.4	16314.2	15189.9	15271.0	16407.0	16099.0	15233.3	15287.0	94%	101%	87%	106%	100%	94%
					random	1000000	402.9	383.1	333.3	382.0	384.2	356.3	362.1	350.4	340.5	372.0	375.3	369.1	90%	91%	102%	97%	98%	104%
						10000000	3388.6	3143.0	3336.3	3340.7	3206.0	3108.7	3201.2	3151.0	3241.8	3169.9	3076.8	3242.9	94%	100%	97%	95%	96%	104%
						50000000	15192.7	15603.8	15182.9	17551.8	15204.0	15317.8	15167.8	15118.1	15122.6	16357.0	16187.1	15300.9	100%	97%	100%	93%	106%	100%
					sequential	1000000	395.0	404.2	345.0	370.7	411.6	394.7	349.5	386.8	348.0	405.6	374.1	440.7	88%	96%	101%	109%	91%	112%
						10000000	3401.5	3153.2	3132.4	3324.5	3117.4	3230.4	3393.5	3407.5	3143.5	3343.3	3094.3	3173.1	100%	108%	100%	101%	99%	98%
						50000000	15158.4	16653.7	15204.7	15157.9	15256.4	15440.7	15144.6	16373.7	16002.1	15170.9	16225.0	15950.6	100%	98%	105%	100%	106%	103%
				xeon	cycle	1000000	250.3	253.2	251.0	254.0	248.5	285.1	249.1	253.7	253.0	249.6	190.9	281.9	99%	100%	101%	98%	77%	99%
						10000000	2222.7	2206.8	2226.1	2212.4	2200.7	2262.8	2232.0	2210.9	2204.9	2216.6	2195.2	2130.3	100%	100%	99%	100%	100%	94%
						100000000	21658.3	21611.4	21649.6	21692.9	21706.7	21874.2	21716.7	21687.1	21524.3	21574.2	21760.6	21979.4	100%	100%	99%	99%	100%	100%
					random	1000000	250.8	252.2	249.0	247.5	251.0	287.3	252.9	251.9	254.0	253.0	231.4	285.6	101%	100%	102%	102%	92%	99%
						10000000	2214.5	2213.9	2193.4	2220.4	2206.0	2240.1	2202.1	2208.7	2203.3	2225.8	2233.3	2239.9	99%	100%	100%	100%	101%	100%
						100000000	21563.8	21690.0	21929.5	21657.5	21742.5	21827.3	21740.2	21716.3	22010.0	21577.1	21671.8	21696.5	101%	100%	100%	100%	100%	99%
					sequential	1000000	251.4	251.0	250.2	252.0	255.9	280.9	252.3	254.0	250.8	252.1	197.2	281.6	100%	101%	100%	100%	77%	100%
						10000000	2191.6	2229.4	2217.4	2257.3	2199.9	2249.5	2196.6	2222.5	2222.7	2220.1	2208.5	2232.0	100%	100%	100%	98%	100%	99%
						100000000	21587.3	21920.0	21580.4	21800.5	21615.4	21894.1	21636.5	21838.9	21463.6	21730.6	21538.5	21792.4	100%	100%	99%	100%	100%	100%
	btree-sort	bitmapscan	master	i5	cycle	1000000	283.6	274.2	497.5	573.6	497.3	596.3	509.5	446.5	486.9	517.8	615.8	961.1	180%	163%	98%	90%	124%	161%
						10000000	2346.5	1885.9	2391.9	4058.9	4749.1	4401.9	4056.7	3861.0	3132.9	4072.3	4839.3	5560.3	173%	205%	131%	100%	102%	126%
						50000000	11382.5	9343.7	8085.7	12361.2	21754.0	24514.4	16157.0	18323.1	14312.1	16716.0	15643.4	23581.8	142%	196%	177%	135%	72%	96%
					random	1000000	507.5	493.8	468.2	501.0	547.3	550.7	897.4	885.2	835.1	867.0	923.1	881.8	177%	179%	178%	173%	169%	160%
						10000000	3980.6	3958.5	4648.0	4041.9	4098.6	4102.1	6976.9	7379.6	7351.9	6924.0	6855.6	7302.7	175%	186%	158%	171%	167%	178%
						50000000	19069.5	18650.5	17771.1	17944.9	18033.3	18286.7	34616.3	34686.6	34137.8	33614.0	34198.9	33276.7	182%	186%	192%	187%	190%	182%
					sequential	1000000	251.6	303.8	254.6	276.6	214.8	260.6	479.0	515.7	517.3	414.7	520.8	523.1	190%	170%	203%	150%	242%	201%
						10000000	2241.1	2136.7	1964.8	2451.7	2688.5	1952.1	3676.8	4070.8	3342.8	3586.4	3732.8	3503.2	164%	191%	170%	146%	139%	179%
						50000000	9872.4	7983.9	8945.0	9290.6	10938.6	8224.4	17126.6	16125.7	19052.6	15665.9	14803.4	8801.8	173%	202%	213%	169%	135%	107%
				xeon	cycle	1000000	214.0	203.3	390.2	444.8	422.3	521.2	263.1	306.9	353.6	363.1	411.3	638.1	123%	151%	91%	82%	97%	122%
						10000000	2220.5	2083.8	2373.7	3484.0	3550.0	3949.9	3087.3	2719.7	3659.7	3683.5	3112.7	3960.0	139%	131%	154%	106%	88%	100%
						100000000	15934.8	13935.0	15728.0	21505.7	34289.5	35383.7	20230.8	24084.7	24433.5	33250.5	35705.5	26428.9	127%	173%	155%	155%	104%	75%
					random	1000000	391.5	407.1	376.1	405.8	402.2	462.7	525.1	563.1	553.8	497.4	605.4	653.6	134%	138%	147%	123%	151%	141%
						10000000	3542.9	3323.0	3182.3	3173.1	3156.0	3415.2	4780.1	4822.9	4988.0	4533.0	4547.1	4843.4	135%	145%	157%	143%	144%	142%
						100000000	33082.3	32055.6	31819.1	30558.8	30142.0	32288.3	45409.2	45849.1	44961.1	44177.7	44461.3	44181.4	137%	143%	141%	145%	148%	137%
					sequential	1000000	235.6	193.0	183.4	257.2	212.4	233.8	335.7	371.8	391.3	309.5	283.4	230.6	143%	193%	213%	120%	133%	99%
						10000000	1655.0	1890.7	2121.0	2129.6	2113.4	2064.5	2871.2	2847.2	2467.0	2305.2	2883.0	2815.4	173%	151%	116%	108%	136%	136%
						100000000	19876.0	17910.6	18237.2	15594.0	17898.3	15354.7	26167.1	23730.1	19638.7	26775.3	21747.7	26280.5	132%	132%	108%	172%	122%	171%
			patched	i5	cycle	1000000	322.2	234.4	385.6	614.4	507.7	475.8	502.5	315.0	483.6	567.2	568.5	740.3	156%	134%	125%	92%	112%	156%
						10000000	2252.8	2260.9	2233.2	4101.9	5524.3	4345.2	3252.1	2481.5	2982.7	3894.4	3695.2	5712.0	144%	110%	134%	95%	67%	131%
						50000000	10474.0	8398.0	8584.6	13328.7	22613.5	25842.8	13536.5	16312.0	12484.3	15315.7	17256.5	18588.3	129%	194%	145%	115%	76%	72%
					random	1000000	506.0	446.0	452.5	474.2	486.5	528.1	742.2	652.6	706.1	665.1	653.6	720.2	147%	146%	156%	140%	134%	136%
						10000000	3955.3	3732.8	4048.8	4128.3	3708.8	3983.7	5519.6	5855.9	5637.2	5548.2	5173.1	5991.4	140%	157%	139%	134%	139%	150%
						50000000	17922.4	18532.5	17427.5	17612.5	17760.8	18094.3	28611.6	28139.6	25736.1	25078.0	25498.6	27380.7	160%	152%	148%	142%	144%	151%
					sequential	1000000	240.6	240.9	261.6	255.2	271.5	219.2	386.2	416.2	348.2	437.0	423.7	348.0	161%	173%	133%	171%	156%	159%
						10000000	2209.7	1801.7	1902.0	2425.6	1831.4	2589.0	3929.4	2865.4	2482.0	3512.8	3510.1	3733.9	178%	159%	130%	145%	192%	144%
						50000000	9905.0	11192.9	10692.3	7527.4	9512.1	8101.8	21098.0	11372.5	12254.7	13118.8	13610.4	12955.0	213%	102%	115%	174%	143%	160%
				xeon	cycle	1000000	253.0	270.1	358.6	464.6	386.8	487.5	373.1	398.1	372.7	395.7	414.5	670.4	147%	147%	104%	85%	107%	138%
						10000000	1743.4	1851.0	2299.0	3498.0	3715.9	3775.3	2630.6	3172.8	3166.0	3108.1	3307.1	3429.6	151%	171%	138%	89%	89%	91%
						100000000	18507.3	14800.4	18973.3	20463.4	31305.2	34273.3	22956.3	21475.6	19887.4	28870.3	32890.3	27161.2	124%	145%	105%	141%	105%	79%
					random	1000000	409.6	386.4	379.0	399.6	399.2	482.9	507.0	563.2	572.4	511.1	630.6	664.3	124%	146%	151%	128%	158%	138%
						10000000	3284.5	3228.7	3403.3	3100.7	3261.2	3328.0	4692.9	4919.7	4882.1	4542.7	4644.2	4901.2	143%	152%	143%	147%	142%	147%
						100000000	33253.0	31925.9	31036.4	32071.0	31061.8	31738.9	45431.3	45111.1	44973.5	44159.6	44741.9	44911.6	137%	141%	145%	138%	144%	142%

				sequential	1000000	230.8	209.0	257.3	203.6	222.4	239.5	316.9	281.5	379.3	274.7	273.7	283.8	137%	135%	147%	135%	123%	118%
					10000000	2173.5	1931.7	2144.6	2067.9	2125.0	2009.7	2331.0	2195.1	2958.6	2371.6	2590.3	2736.1	107%	114%	138%	115%	122%	136%
					100000000	16379.3	17033.0	17897.0	17840.2	17318.7	14490.6	23730.9	19886.8	24374.8	25524.8	24521.9	22004.7	145%	117%	136%	143%	142%	152%
	indexscan	master	i5	cycle	1000000	11.5	12.8	27.4	166.6	1554.2	465.1	11.5	13.2	25.7	172.2	1536.0	456.7	99%	103%	94%	103%	99%	98%
					10000000	12.1	13.9	29.1	165.8	1572.8	15209.2	12.5	14.4	27.0	165.5	1573.6	15341.5	103%	104%	93%	100%	100%	101%
					50000000	12.2	14.3	28.5	167.1	1549.9	15232.8	11.7	14.3	28.8	165.0	1563.7	15210.0	96%	100%	101%	99%	101%	100%
				random	1000000	12.3	13.4	25.7	182.3	879.7	430.2	12.1	13.2	27.9	180.1	839.5	401.3	98%	98%	109%	99%	95%	93%
					10000000	12.1	13.5	27.7	167.3	1619.4	7470.4	13.0	14.0	26.2	166.0	1604.5	7541.5	108%	103%	95%	99%	99%	101%
					50000000	12.3	14.5	28.0	167.1	1567.7	15229.7	12.4	14.2	28.1	165.0	1568.8	15349.4	100%	98%	100%	99%	100%	101%
				sequential	1000000	11.8	12.2	12.9	15.2	23.1	91.0	12.6	11.4	12.5	15.1	23.1	98.7	106%	94%	96%	100%	100%	108%
					10000000	12.4	12.6	13.1	14.0	24.4	92.5	12.6	12.3	13.0	14.9	22.8	94.0	102%	98%	99%	106%	93%	102%
					50000000	12.4	12.6	12.9	14.6	24.6	93.1	12.2	12.3	12.4	14.0	22.2	93.4	98%	97%	96%	96%	90%	100%
			xeon	cycle	1000000	13.0	13.5	23.9	118.7	392.8	354.2	13.5	14.8	23.2	118.6	376.5	350.7	104%	110%	97%	100%	96%	99%
					10000000	12.7	12.9	23.5	120.5	1037.6	3105.5	11.8	13.1	23.3	119.7	1038.5	3114.4	93%	101%	99%	99%	100%	100%
					100000000	12.7	14.4	27.2	144.3	1172.2	10204.7	13.3	14.7	27.7	142.2	1127.5	10047.1	105%	102%	102%	99%	96%	98%
				random	1000000	12.9	14.0	23.1	119.4	549.3	348.4	12.2	13.2	23.0	119.6	557.5	353.9	95%	94%	99%	100%	101%	102%
					10000000	12.4	14.4	23.1	120.5	1072.9	4938.5	12.1	13.5	22.8	121.3	1084.8	4911.4	98%	94%	99%	101%	101%	99%
					100000000	12.8	15.7	29.6	140.2	1106.8	10763.3	12.7	15.0	29.0	147.0	1097.7	10792.5	99%	95%	98%	105%	99%	100%
				sequential	1000000	13.7	12.9	13.7	14.9	22.9	99.5	12.6	12.3	13.2	14.1	22.7	96.5	91%	96%	97%	95%	99%	97%
					10000000	12.4	12.3	13.0	13.8	22.0	96.4	12.1	12.1	13.2	14.3	22.4	95.5	98%	98%	101%	103%	102%	99%
					100000000	12.2	13.1	13.8	15.3	22.7	97.4	12.3	13.1	13.7	14.8	23.1	96.4	101%	100%	99%	97%	101%	99%
		patched	i5	cycle	1000000	11.5	12.5	25.6	167.6	1534.7	395.2	11.2	12.1	14.0	28.2	176.6	384.5	98%	97%	55%	17%	12%	97%
					10000000	11.7	13.6	26.4	164.7	1573.5	15041.3	11.9	12.9	14.4	28.9	180.9	1341.3	101%	94%	55%	18%	11%	9%
					50000000	11.7	14.1	29.5	163.6	1543.2	14845.8	11.6	13.7	15.8	30.3	148.4	1247.9	99%	97%	54%	19%	10%	8%
				random	1000000	11.5	12.7	25.7	182.6	854.0	408.1	11.7	12.2	13.9	29.6	177.8	407.4	102%	96%	54%	16%	21%	100%
					10000000	11.8	13.0	26.9	172.8	1577.4	8123.1	12.3	12.4	14.4	26.8	150.2	1584.6	103%	95%	53%	16%	10%	20%
					50000000	12.3	14.2	28.0	168.3	1489.5	15557.3	12.4	14.3	16.5	28.9	143.9	1229.1	101%	101%	59%	17%	10%	8%
				sequential	1000000	11.7	12.2	12.8	13.9	24.7	91.4	11.5	11.6	12.3	16.0	23.3	100.4	99%	95%	96%	115%	94%	110%
					10000000	11.8	12.0	13.3	14.3	22.0	93.7	11.9	12.0	12.5	14.3	22.3	99.4	101%	100%	94%	100%	102%	106%
					50000000	11.8	11.5	13.1	14.2	23.3	94.3	11.9	12.6	12.6	14.5	22.5	95.4	101%	110%	96%	102%	97%	101%
			xeon	cycle	1000000	12.8	13.3	23.6	118.8	374.7	353.3	12.9	12.9	14.8	26.4	127.3	358.7	100%	97%	63%	22%	34%	102%
					10000000	12.8	13.9	23.1	120.5	1038.0	3113.4	12.2	13.1	13.9	24.9	141.1	1199.1	96%	94%	60%	21%	14%	39%
					100000000	12.7	14.9	27.5	125.5	1189.6	10210.2	12.2	14.1	16.2	27.4	120.3	1247.7	96%	95%	59%	22%	10%	12%
				random	1000000	12.3	13.5	23.3	119.6	569.3	353.6	12.2	13.5	13.9	25.4	125.4	359.9	99%	100%	59%	21%	22%	102%
					10000000	12.8	13.5	24.0	121.3	1080.2	4875.2	12.9	12.9	14.0	25.2	134.2	1129.5	101%	95%	58%	21%	12%	23%
					100000000	13.0	14.2	28.6	145.8	1129.9	10696.6	12.3	14.8	17.9	29.3	121.4	1221.7	95%	104%	63%	20%	11%	11%
				sequential	1000000	12.4	12.8	13.5	14.7	23.2	97.5	13.1	13.3	13.1	14.8	23.4	103.1	105%	104%	97%	100%	101%	106%
					10000000	12.9	12.0	13.0	14.4	23.2	96.0	12.5	12.4	12.7	14.4	23.0	98.3	97%	103%	97%	100%	99%	102%
					100000000	12.4	12.7	13.4	14.8	22.8	96.9	12.8	12.5	13.3	15.0	23.8	98.0	103%	98%	99%	101%	105%	101%
	seqscan	master	i5	cycle	1000000	375.3	396.2	397.9	412.6	411.3	475.1	415.1	392.5	434.7	422.0	386.5	473.0	111%	99%	109%	102%	94%	100%
					10000000	3258.1	3198.2	4004.0	3271.9	3281.0	3323.7	3427.0	3241.3	3466.8	3182.0	3616.7	3783.7	105%	101%	87%	97%	110%	114%
					50000000	15496.4	15509.3	15520.6	16122.8	15647.7	15712.4	15527.6	16470.1	15531.4	15600.9	15589.2	15830.9	100%	106%	100%	97%	100%	101%
				random	1000000	438.5	439.7	420.7	369.1	446.9	491.8	430.2	417.7	365.5	379.0	434.1	469.7	98%	95%	87%	103%	97%	96%
					10000000	3225.6	3196.2	3671.1	3151.6	3242.7	3294.4	3161.4	3287.4	3320.0	3167.4	3181.9	3342.2	98%	103%	90%	100%	98%	101%
					50000000	15767.9	16886.9	15533.3	15559.0	15582.5	16360.8	15553.4	15548.6	15462.9	15979.4	15636.7	15736.6	99%	92%	100%	103%	100%	96%
				sequential	1000000	454.0	420.4	452.6	387.1	347.7	414.3	474.1	424.9	424.0	385.3	342.9	403.8	104%	101%	94%	100%	99%	97%
					10000000	3399.2	3480.7	3167.3	3407.1	3745.0	3338.1	3426.7	3198.9	3174.4	3380.8	3383.2	3291.4	101%	92%	100%	99%	90%	99%
					50000000	15549.0	15458.7	15598.4	16289.8	16611.9	15604.1	15553.3	15458.7	16163.1	15551.4	15535.5	15613.0	100%	100%	104%	95%	94%	100%
			xeon	cycle	1000000	266.7	270.5	255.1	272.0	286.0	361.4	264.9	263.7	270.4	271.9	285.3	353.8	99%	97%	106%	100%	100%	98%
					10000000	2276.2	2329.5	2342.9	2343.7	2344.3	2459.4	2260.5	2324.7	2331.4	2341.6	2353.5	2462.7	99%	100%	100%	100%	100%	100%
					100000000	22584.4	22352.4	22499.2	22621.1	22715.5	22736.3	22580.2	22520.6	22531.8	22462.9	22695.1	22849.6	100%	101%	100%	99%	100%	100%
				random	1000000	269.1	271.7	270.0	272.8	283.4	360.2	268.0	266.3	268.2	271.9	279.6	376.9	100%	98%	99%	100%	99%	105%

						10000000	2318.8	2363.8	2328.2	2247.5	2353.6	2515.3	2344.7	2356.9	2325.2	2290.6	2337.0	2548.6	101%	100%	100%	102%	99%	101%
						100000000	22467.0	22664.2	22461.8	22618.6	22855.7	22856.2	22499.4	22555.5	22428.3	22533.6	22720.5	22934.5	100%	100%	100%	100%	99%	100%
					sequential	1000000	266.3	270.2	271.5	268.3	272.9	317.1	265.0	269.8	272.9	270.3	273.7	316.4	100%	100%	101%	101%	100%	100%
						10000000	2255.2	2250.9	2302.9	2284.6	2298.7	2389.5	2349.5	2341.8	2343.9	2340.1	2245.8	2387.6	104%	104%	102%	102%	98%	100%
						100000000	22662.6	22624.6	22473.8	22601.3	22619.6	22721.9	22676.9	22444.9	22366.3	22488.1	22530.1	22550.2	100%	99%	100%	99%	100%	99%
				patched	i5	cycle	1000000	381.6	405.9	423.7	400.4	379.1	426.2	400.0	387.5	369.2	405.7	404.5	409.8	105%	95%	87%	101%	107%	96%
							10000000	3209.5	3267.4	3206.8	3163.1	3438.0	3706.7	3160.7	3162.2	3226.3	3186.3	3213.3	3306.2	98%	97%	101%	101%	93%	89%
							50000000	15528.4	15513.4	15552.0	15573.9	15521.1	15768.0	15465.5	15549.4	15818.5	15525.4	15659.3	15736.6	100%	100%	102%	100%	101%	100%
						random	1000000	449.1	385.4	369.3	400.5	382.4	444.1	432.0	381.5	415.1	363.3	378.3	444.8	96%	99%	112%	91%	99%	100%
							10000000	3184.5	3189.7	3188.2	3212.2	3174.7	3352.6	3197.7	3116.7	3108.3	3751.2	3151.9	3328.7	100%	98%	97%	117%	99%	99%
							50000000	15647.5	16419.3	15876.7	15651.1	15595.3	15601.5	15616.1	15513.7	15568.5	16568.2	15975.9	15841.2	100%	94%	98%	106%	102%	102%
						sequential	1000000	386.9	376.5	406.2	365.7	362.8	426.4	382.0	396.6	421.9	371.5	354.0	445.5	99%	105%	104%	102%	98%	104%
							10000000	3165.6	3174.5	3136.5	3154.9	3194.9	3687.9	3157.0	3614.4	3172.2	3198.4	3200.6	3914.3	100%	114%	101%	101%	100%	106%
							50000000	15509.7	15526.3	15827.6	15628.8	15567.3	15569.3	16284.4	15645.6	15562.0	15710.4	15511.9	16243.7	105%	101%	98%	101%	100%	104%
					xeon	cycle	1000000	269.8	272.1	274.9	272.6	249.7	359.7	271.1	271.3	271.4	272.0	244.2	356.8	100%	100%	99%	100%	98%	99%
							10000000	2280.7	2322.9	2358.5	2342.5	2347.1	2459.5	2334.9	2341.3	2359.8	2368.4	2339.0	2442.2	102%	101%	100%	101%	100%	99%
							100000000	22619.1	22530.8	22602.4	22646.0	22892.0	22796.6	22656.6	22624.5	22599.9	22556.4	22784.6	22929.7	100%	100%	100%	100%	100%	101%
						random	1000000	273.2	271.9	271.6	273.0	288.7	373.2	270.4	273.4	277.8	271.5	284.4	371.4	99%	101%	102%	99%	99%	100%
							10000000	2285.2	2370.6	2344.6	2324.8	2378.5	2565.0	2350.9	2384.3	2350.8	2305.5	2371.3	2537.8	103%	101%	100%	99%	100%	99%
							100000000	22626.6	22798.9	22542.2	22786.5	22773.3	23055.3	22647.7	22632.6	22743.8	22685.5	22948.8	22978.6	100%	99%	101%	100%	101%	100%
						sequential	1000000	272.5	270.8	272.9	269.4	277.5	322.7	267.2	271.1	260.8	267.4	272.2	288.0	98%	100%	96%	99%	98%	89%
							10000000	2356.9	2259.6	2350.7	2346.0	2239.6	2378.1	2300.9	2363.1	2323.5	2378.5	2238.6	2388.8	98%	105%	99%	101%	100%	100%
							100000000	22662.5	22713.6	22533.8	22682.7	22748.9	22843.2	22635.0	22714.2	22703.8	22646.0	22669.2	22551.0	100%	100%	101%	100%	100%	99%
	hash		bitmapscan	master	i5	cycle	1000000	11.2	13.8	25.7	166.0	1571.4	486.0	12.2	12.0	13.9	31.2	201.8	505.9	109%	87%	54%	19%	13%	104%
							10000000	11.6	13.5	25.7	174.4	1562.9	14993.4	11.4	12.3	14.2	29.5	180.1	1758.3	98%	91%	55%	17%	12%	12%
							50000000	11.5	14.1	28.3	171.2	1551.4	15383.8	11.8	13.1	15.1	31.8	204.4	1692.4	103%	93%	53%	19%	13%	11%
						random	1000000	11.5	12.7	26.7	175.8	832.9	483.9	11.3	12.2	14.4	31.2	183.7	808.0	98%	96%	54%	18%	22%	167%
							10000000	11.5	13.6	27.4	179.2	1599.1	7478.2	11.3	12.2	14.6	32.2	181.7	1573.7	98%	90%	53%	18%	11%	21%
							50000000	11.3	14.1	28.8	171.9	1566.4	15300.3	11.8	12.8	15.6	30.9	185.8	1655.4	105%	90%	54%	18%	12%	11%
						sequential	1000000	11.7	12.2	13.9	14.4	23.5	92.9	11.5	11.4	11.6	13.7	28.5	139.2	98%	94%	84%	95%	121%	150%
							10000000	13.7	13.9	12.5	15.8	24.7	99.9	11.5	12.4	12.5	13.3	26.4	131.9	84%	89%	100%	84%	107%	132%
							50000000	11.3	12.1	12.0	16.3	23.9	97.6	11.3	11.7	12.1	14.7	25.6	133.3	100%	96%	101%	90%	107%	137%
					xeon	cycle	1000000	13.9	15.3	24.4	122.0	857.2	401.0	13.7	14.9	15.9	27.3	127.3	653.2	98%	97%	65%	22%	15%	163%
							10000000	14.6	14.0	24.0	123.8	1020.6	3410.2	14.0	14.3	14.5	29.6	145.9	1325.8	96%	102%	60%	24%	14%	39%
							100000000	14.6	13.8	27.1	124.6	1101.1	10200.8	13.8	13.6	18.0	29.5	128.0	1314.2	95%	99%	66%	24%	12%	13%
						random	1000000	14.0	14.0	25.3	123.7	609.0	401.7	13.8	15.3	16.3	27.6	120.3	586.0	98%	109%	65%	22%	20%	146%
							10000000	13.5	14.2	23.4	121.8	1091.2	4905.2	12.0	14.2	14.2	29.0	142.7	1047.9	89%	100%	60%	24%	13%	21%
							100000000	14.2	15.0	25.6	127.3	1098.9	10793.5	14.7	14.0	18.1	30.8	124.0	1288.5	104%	93%	70%	24%	11%	12%
						sequential	1000000	13.4	13.1	14.4	15.7	24.2	99.9	13.2	12.6	13.2	13.9	24.9	111.1	98%	96%	91%	89%	103%	111%
							10000000	13.4	13.6	14.3	16.6	26.2	99.9	13.8	13.9	14.0	14.8	26.0	112.5	103%	102%	98%	89%	99%	113%
							100000000	14.1	13.9	14.2	15.5	24.6	103.8	14.0	13.5	14.7	15.6	25.3	113.2	99%	97%	104%	101%	103%	109%
				patched	i5	cycle	1000000	11.1	12.4	24.9	167.3	1532.5	480.0	11.4	12.5	12.9	25.6	162.1	669.8	102%	101%	52%	15%	11%	140%
							10000000	11.6	13.7	27.0	164.9	1580.5	15084.3	11.7	12.1	13.9	27.2	143.9	1376.8	101%	88%	51%	16%	9%	9%
							50000000	11.6	13.5	27.5	163.7	1531.7	14834.6	11.3	12.5	15.9	29.9	146.6	1330.5	97%	93%	58%	18%	10%	9%
						random	1000000	11.2	13.4	26.1	167.0	916.5	483.6	11.7	12.3	14.0	28.0	150.5	700.0	104%	92%	54%	17%	16%	145%
							10000000	11.0	12.7	26.7	167.8	1591.7	7916.6	11.1	12.2	13.6	26.4	145.7	1163.0	101%	96%	51%	16%	9%	15%
							50000000	11.7	13.5	27.7	167.2	1531.8	15753.0	12.2	12.3	15.2	28.5	140.6	1283.9	104%	91%	55%	17%	9%	8%
						sequential	1000000	11.7	11.7	12.2	14.1	21.8	93.7	12.2	11.7	11.8	14.1	24.6	114.0	104%	100%	96%	100%	113%	122%
							10000000	12.7	12.7	14.3	16.4	24.4	93.3	12.0	11.1	12.5	13.8	23.9	119.2	94%	88%	87%	84%	98%	128%
							50000000	11.8	11.7	13.1	14.4	22.0	91.7	11.6	11.6	12.5	15.8	25.2	120.6	99%	99%	96%	110%	115%	132%
					xeon	cycle	1000000	13.3	14.5	23.9	121.9	866.1	408.4	13.4	14.3	14.9	29.5	125.4	654.3	101%	99%	62%	24%	14%	160%
							10000000	13.9	15.3	23.9	124.3	1019.9	3440.6	13.4	13.9	14.8	29.3	141.7	1335.0	96%	91%	62%	24%	14%	39%

					100000000	14.5	14.9	26.9	125.5	1112.4	10142.9	14.2	14.2	17.0	30.5	125.3	1310.1	98%	95%	63%	24%	11%	13%
				random	1000000	14.5	13.3	24.5	127.1	605.7	393.1	14.3	14.9	16.4	27.9	121.8	588.5	99%	112%	67%	22%	20%	150%
					10000000	13.1	13.6	24.1	122.1	1083.0	4824.1	13.6	13.7	15.1	28.9	142.0	1044.4	104%	101%	63%	24%	13%	22%
					100000000	14.6	16.0	28.6	125.8	1111.2	10808.7	13.4	15.1	18.7	31.7	124.1	1273.0	92%	95%	65%	25%	11%	12%
				sequential	1000000	13.9	12.7	13.5	14.6	24.7	100.1	13.3	12.9	13.4	14.2	25.9	110.7	95%	102%	99%	97%	105%	111%
					10000000	13.7	13.3	13.6	15.7	26.6	101.2	13.8	13.3	13.9	15.9	26.3	114.0	101%	100%	102%	101%	99%	113%
					100000000	13.4	14.8	15.3	14.9	25.1	101.9	14.1	14.6	14.5	16.0	25.6	114.6	105%	98%	95%	107%	102%	112%
	indexscan	master	i5	cycle	1000000	11.5	12.7	27.0	161.9	1545.1	7057.6	11.2	13.0	25.8	167.2	1540.3	6926.4	98%	102%	96%	103%	100%	98%
					10000000	12.2	13.0	26.2	172.9	1549.2	15249.1	12.2	13.1	26.1	166.4	1544.9	15775.6	100%	101%	99%	96%	100%	103%
					50000000	11.6	13.5	29.3	171.1	1552.5	15293.4	11.5	14.0	27.7	168.4	1614.8	15337.5	99%	104%	94%	98%	104%	100%
				random	1000000	10.9	12.6	25.8	162.9	1409.0	6277.1	11.1	12.8	27.4	167.2	1399.3	6303.3	102%	102%	106%	103%	99%	100%
					10000000	11.2	13.2	26.5	176.7	1622.3	7480.9	11.5	13.8	25.3	167.8	1590.6	7583.2	103%	105%	95%	95%	98%	101%
					50000000	11.8	14.5	27.6	165.9	1565.1	15350.8	11.8	13.8	30.4	173.3	1562.9	15319.5	99%	95%	110%	104%	100%	100%
				sequential	1000000	11.4	11.6	12.9	19.5	29.3	106.1	11.7	11.5	11.9	20.1	29.8	104.1	102%	99%	93%	103%	102%	98%
					10000000	11.3	12.0	12.3	13.7	22.2	96.0	12.6	12.6	13.5	15.5	22.7	96.7	112%	105%	110%	113%	102%	101%
					50000000	11.6	11.5	12.9	14.8	22.8	102.6	11.9	11.7	12.3	14.3	25.5	95.4	103%	101%	96%	97%	112%	93%
			xeon	cycle	1000000	13.5	14.6	23.7	120.7	1089.4	540.2	12.8	15.9	24.0	120.2	1090.8	537.2	95%	109%	101%	100%	100%	99%
					10000000	13.0	14.0	23.3	122.2	1018.3	3445.4	13.5	14.1	23.5	124.1	1013.4	3518.4	104%	101%	101%	102%	100%	102%
					100000000	14.3	13.6	27.5	124.4	1100.7	10075.3	13.6	14.3	27.8	124.8	1103.9	10058.0	95%	105%	101%	100%	100%	100%
				random	1000000	14.3	12.9	24.4	118.9	938.8	3412.1	14.4	15.4	24.3	119.6	928.6	3427.1	101%	119%	99%	101%	99%	100%
					10000000	12.2	15.1	24.0	119.5	1083.5	4870.3	11.9	13.4	22.9	126.3	1080.9	4830.7	97%	89%	95%	106%	100%	99%
					100000000	13.3	15.4	28.1	121.3	1096.6	10782.3	15.1	14.5	28.8	129.1	1100.0	10799.4	113%	94%	103%	106%	100%	100%
				sequential	1000000	13.2	12.5	13.7	18.3	27.7	110.6	12.8	13.0	12.5	17.8	28.5	105.6	97%	104%	91%	97%	103%	95%
					10000000	14.1	13.3	14.4	15.2	23.7	102.1	13.4	13.4	13.9	15.7	23.4	100.1	95%	101%	96%	104%	99%	98%
					100000000	13.9	14.3	14.7	15.3	23.5	103.6	13.8	13.7	14.5	16.0	23.9	109.0	99%	96%	99%	105%	101%	105%
		patched	i5	cycle	1000000	11.4	12.8	25.4	166.7	1529.9	6658.3	11.1	12.2	12.9	32.9	161.9	941.4	98%	95%	51%	20%	11%	14%
					10000000	11.4	12.9	26.1	165.3	1587.4	15045.6	11.4	12.2	15.7	26.8	156.5	1304.2	100%	95%	60%	16%	10%	9%
					50000000	11.3	13.7	28.0	163.7	1535.4	15031.6	11.6	12.5	16.4	27.8	146.3	1275.5	102%	91%	59%	17%	10%	8%
				random	1000000	11.1	12.3	24.8	160.0	1433.6	6103.9	11.2	11.9	13.4	27.5	169.8	911.0	101%	97%	54%	17%	12%	15%
					10000000	11.2	13.1	26.4	165.9	1594.5	7819.6	11.0	12.3	13.9	29.5	145.0	1545.5	99%	94%	52%	18%	9%	20%
					50000000	11.2	13.8	26.7	172.8	1508.5	15904.0	11.6	12.7	15.3	31.9	146.2	1288.9	103%	93%	57%	18%	10%	8%
				sequential	1000000	11.9	11.3	11.9	18.9	29.5	100.2	11.2	11.2	11.9	18.1	54.4	434.1	95%	99%	100%	96%	184%	433%
					10000000	12.5	11.6	12.2	13.6	22.3	93.2	12.8	12.3	13.8	14.6	23.9	101.8	102%	106%	113%	108%	107%	109%
					50000000	11.6	11.6	12.9	16.6	22.9	92.0	11.8	11.5	12.0	14.3	23.4	97.7	102%	99%	92%	86%	102%	106%
			xeon	cycle	1000000	13.2	13.5	23.4	121.2	1097.3	533.5	13.6	14.4	15.7	28.2	118.2	482.0	103%	107%	67%	23%	11%	90%
					10000000	13.8	14.4	22.8	124.9	1017.5	3457.4	12.8	13.8	14.1	29.4	144.3	1134.6	92%	96%	62%	24%	14%	33%
					100000000	14.7	15.4	27.1	125.6	1098.6	10060.0	13.8	15.0	16.7	29.5	124.7	1287.8	94%	98%	62%	24%	11%	13%
				random	1000000	14.3	13.7	25.3	122.5	937.0	3440.8	14.0	14.0	16.3	27.1	120.1	1062.1	98%	102%	64%	22%	13%	31%
					10000000	12.1	13.3	22.0	125.8	1068.4	4875.6	12.8	12.9	15.0	27.2	139.9	1145.7	106%	97%	68%	22%	13%	23%
					100000000	13.9	15.0	27.9	127.1	1107.6	10748.4	14.1	14.0	18.4	31.3	124.2	1248.3	101%	94%	66%	25%	11%	12%
				sequential	1000000	12.6	12.3	13.4	18.4	28.7	104.5	13.1	12.5	13.6	16.2	42.8	297.3	104%	102%	102%	88%	149%	285%
					10000000	13.3	13.3	14.1	15.7	24.5	101.3	13.2	12.7	13.4	16.0	25.2	103.2	99%	95%	95%	102%	103%	102%
					100000000	12.7	15.1	14.7	15.8	24.3	102.3	13.7	14.6	14.3	16.0	24.6	116.9	108%	97%	97%	101%	101%	114%
	seqscan	master	i5	cycle	1000000	371.0	399.0	358.3	374.1	409.9	387.9	375.2	357.5	342.6	361.1	346.1	346.8	101%	90%	96%	97%	84%	89%
					10000000	3202.2	3498.4	3148.6	3366.3	3159.6	3198.5	3189.3	3224.1	3126.8	3387.2	3116.3	3122.8	100%	92%	99%	101%	99%	98%
					50000000	15298.8	15232.5	15636.8	16038.5	15268.3	15309.8	15249.0	15182.0	15523.2	15473.3	15656.5	15303.3	100%	100%	99%	96%	103%	100%
				random	1000000	351.2	364.5	338.7	386.6	385.6	390.8	350.4	380.1	370.1	372.4	390.4	445.8	100%	104%	109%	96%	101%	114%
					10000000	3356.0	3291.0	3475.2	3487.4	3397.5	3152.1	3082.2	3202.6	3461.3	3635.0	3166.0	3203.3	92%	97%	100%	104%	93%	102%
					50000000	15184.4	16132.7	15238.3	15315.8	15539.1	15768.7	15264.7	15616.7	15170.1	16040.1	15168.3	15341.0	101%	97%	100%	105%	98%	97%
				sequential	1000000	351.9	393.8	366.0	347.1	357.6	399.3	337.6	410.4	382.7	331.5	327.9	398.3	96%	104%	105%	95%	92%	100%
					10000000	3381.2	3142.8	3758.1	3084.8	3213.2	3210.1	3699.4	3287.7	3168.9	3127.8	3196.7	3148.4	109%	105%	84%	101%	99%	98%
					50000000	15261.1	15256.2	15217.5	15260.5	15284.7	16904.9	15216.9	15259.5	16067.8	15308.5	15275.2	15315.5	100%	100%	106%	100%	100%	91%

				xeon	cycle	1000000	252.5	256.3	254.4	255.3	249.1	284.2	255.1	252.8	254.3	252.3	253.1	288.7	101%	99%	100%	99%	102%	102%
						10000000	2185.5	2175.7	2185.1	2191.3	2133.6	2234.2	2191.3	2170.8	2184.8	2174.1	2180.3	2222.4	100%	100%	100%	99%	102%	99%
						100000000	21510.3	21540.6	21509.9	21541.2	21470.2	21571.7	21540.2	21576.7	21450.6	21534.7	21469.0	21631.5	100%	100%	100%	100%	100%	100%
					random	1000000	256.2	247.9	254.3	240.1	251.1	280.0	255.8	247.7	253.9	241.7	249.2	284.6	100%	100%	100%	101%	99%	102%
						10000000	2181.8	2194.4	2196.9	2174.5	2203.5	2215.2	2171.4	2189.4	2187.2	2187.9	2182.1	2252.3	100%	100%	100%	101%	99%	102%
						100000000	21519.2	21480.0	21501.0	21465.7	21510.4	21723.3	21534.1	21559.2	21638.1	21463.6	21499.3	21735.4	100%	100%	101%	100%	100%	100%
					sequential	1000000	253.1	256.2	256.0	251.7	256.7	247.3	251.5	252.1	253.9	254.2	258.8	264.5	99%	98%	99%	101%	101%	107%
						10000000	2190.0	2205.1	2209.1	2179.9	2197.6	2155.3	2190.4	2214.2	2199.9	2191.7	2206.4	2214.4	100%	100%	100%	101%	100%	103%
						100000000	21665.5	21588.2	21491.7	21444.1	21551.6	21537.5	21581.3	21587.4	21449.5	21473.7	21764.0	21516.6	100%	100%	100%	100%	101%	100%
			patched	i5	cycle	1000000	418.8	362.7	332.4	394.1	378.3	370.5	364.5	371.6	324.7	376.4	394.9	395.7	87%	102%	98%	95%	104%	107%
						10000000	3124.2	3231.7	3134.3	3137.3	3414.1	3137.1	3136.8	3210.5	3349.6	3165.0	3402.9	3157.0	100%	99%	107%	101%	100%	101%
						50000000	16134.8	15152.9	17092.8	15095.3	15179.8	15619.1	15117.1	15248.3	15217.2	15147.4	15192.0	15320.5	94%	101%	89%	100%	100%	98%
					random	1000000	382.5	358.4	368.4	388.4	415.4	391.8	337.5	370.5	332.8	354.4	360.3	378.9	88%	103%	90%	91%	87%	97%
						10000000	3114.4	3169.8	3082.1	3201.7	3168.1	3116.7	3133.8	3146.8	3105.8	3148.9	3169.4	3157.1	101%	99%	101%	98%	100%	101%
						50000000	15162.1	15877.4	15203.5	15169.4	15137.0	15212.4	16140.1	15145.4	15192.1	15159.9	15055.6	15189.0	106%	95%	100%	100%	99%	100%
					sequential	1000000	360.1	348.7	332.1	340.2	356.5	388.7	371.2	362.7	353.0	359.4	365.5	334.0	103%	104%	106%	106%	103%	86%
						10000000	3332.0	3344.1	3145.5	3160.2	3159.5	3176.2	3370.6	3034.3	3098.4	3110.6	3168.8	3114.5	101%	91%	99%	98%	100%	98%
						50000000	15180.0	15171.3	16039.9	15223.8	15193.0	15234.6	16287.4	15147.2	15076.7	15248.8	15243.0	15274.7	107%	100%	94%	100%	100%	100%
				xeon	cycle	1000000	254.7	256.9	254.3	256.0	254.8	251.0	253.8	252.9	255.6	258.2	258.9	289.5	100%	98%	101%	101%	102%	115%
						10000000	2198.4	2172.4	2198.6	2160.7	2179.8	2224.3	2201.7	2176.5	2194.6	2198.8	2188.2	2220.0	100%	100%	100%	102%	100%	100%
						100000000	21543.9	21677.8	21543.2	21581.2	21552.4	21739.1	21549.6	21638.3	21512.3	21563.5	21580.7	21687.2	100%	100%	100%	100%	100%	100%
					random	1000000	253.0	248.3	256.4	245.1	254.1	268.7	253.9	248.1	257.7	242.9	255.4	267.4	100%	100%	101%	99%	101%	99%
						10000000	2180.3	2199.9	2197.1	2178.2	2193.1	2221.8	2203.2	2208.2	2201.8	2182.4	2188.8	2232.4	101%	100%	100%	100%	100%	100%
						100000000	21603.9	21557.9	21541.1	21651.8	21523.6	21780.8	21560.2	21632.6	21553.6	21679.1	21627.4	21920.9	100%	100%	100%	100%	100%	101%
					sequential	1000000	256.0	256.9	253.7	253.3	254.0	248.1	254.2	256.2	254.7	252.6	259.1	252.8	99%	100%	100%	100%	102%	102%
						10000000	2192.9	2218.6	2202.1	2179.1	2240.6	2188.9	2185.1	2220.3	2204.2	2189.0	2192.4	2196.0	100%	100%	100%	100%	98%	100%
						100000000	21526.8	21620.2	21542.6	21621.3	21654.4	21583.1	21515.6	21771.3	21569.5	21663.0	21752.8	21545.5	100%	101%	100%	100%	100%	100%